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Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

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HD74HC131

3-to-8-line Decoder/Demultiplexer with Edge-Triggered Address Registers



ADE-205-440 (Z)
1st. Edition
Sep. 2000

Description

The HD74HC131 is 3-to-8 line decoder. It has Address select inputs (A,B,C) and D type register.

Address select data store to D type registers, during the positive going transition of the clock pulse.

Output control ($G_1, \overline{G_2}$) are independent of select input and CLK input, and when G_1 is low or $\overline{G_2} = \text{High}$, all outputs is high.









Features

- High Speed Operation: t_{pd} (CLK to Y) = 20 ns typ ($C_L = 50$ pF)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2$ V to 6 V
- Low Input Current: 1 μA max
- Low Quiescent Supply Current: I_{CC} (static) = 4 μA max ($T_a = 25^\circ\text{C}$)

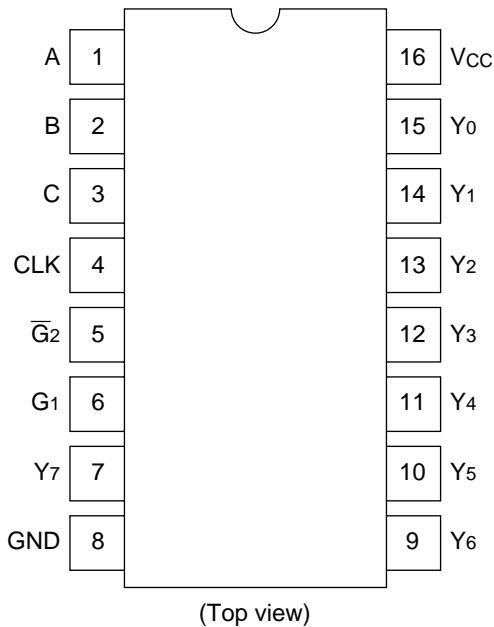
HD74HC131

Function Table

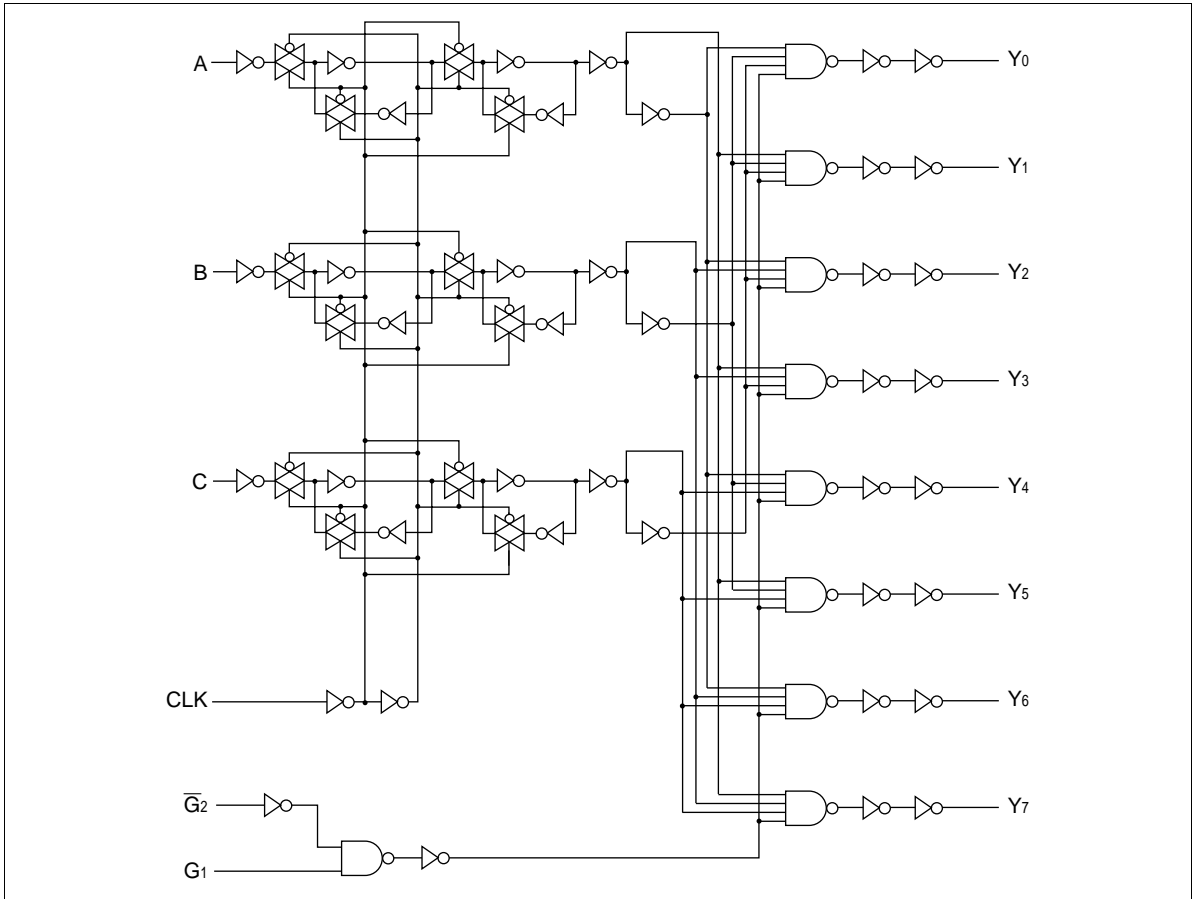
Inputs

Enable			Select			Outputs							
CLK	G1	\overline{G}_2	C	B	A	Y ₀	Y ₁	Y ₂	Y ₃	Y ₄	Y ₅	Y ₆	Y ₇
X	X	H	X	X	X	H	H	H	H	H	H	H	H
X	L	X	X	X	X	H	H	H	H	H	H	H	H
	H	L	L	L	L	L	H	H	H	H	H	H	H
	H	L	L	L	H	H	L	H	H	H	H	H	H
	H	L	L	H	L	H	H	L	H	H	H	H	H
	H	L	L	H	H	H	H	H	L	H	H	H	H
	H	L	H	L	L	H	H	H	H	L	H	H	H
	H	L	H	L	H	H	H	H	H	H	L	H	H
	H	L	H	H	L	H	H	H	H	H	H	L	H
	H	L	H	H	H	H	H	H	H	H	H	H	L
L	H	L	X	X	X	Outputs corresponding to stored address, L; all others H							

Pin Arrangement



Logic Diagram



DC Characteristics

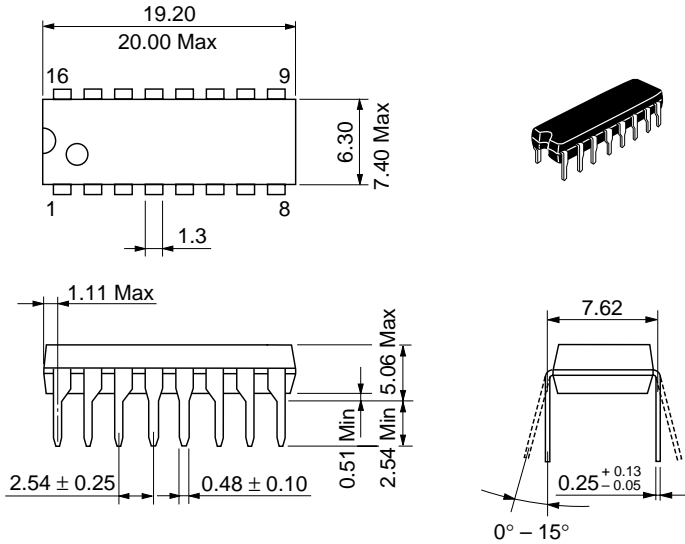
Item	Symbol	V _{CC} (V)	Ta = 25°C		Ta = -40 to +85°C		Unit	Test Conditions		
			Min	Typ	Max	Min			Max	
Input voltage	V _{IH}	2.0	1.5	—	—	1.5	—	V		
		4.5	3.15	—	—	3.15	—			
		6.0	4.2	—	—	4.2	—			
	V _{IL}	2.0	—	—	0.5	—	0.5		V	
		4.5	—	—	1.35	—	1.35			
		6.0	—	—	1.8	—	1.8			
Output voltage	V _{OH}	2.0	1.9	2.0	—	1.9	—	V		Vin = V _{IH} or V _{IL} I _{OH} = -20 μA
		4.5	4.4	4.5	—	4.4	—			
		6.0	5.9	6.0	—	5.9	—			
		4.5	4.18	—	—	4.13	—		I _{OH} = -4 mA	
		6.0	5.68	—	—	5.63	—		I _{OH} = -5.2 mA	
	V _{OL}	2.0	—	0.0	0.1	—	0.1	V	Vin = V _{IH} or V _{IL} I _{OL} = 20 μA	
		4.5	—	0.0	0.1	—	0.1			
		6.0	—	0.0	0.1	—	0.1			
		4.5	—	—	0.26	—	0.33			I _{OL} = 4 mA
		6.0	—	—	0.26	—	0.33			I _{OL} = 5.2 mA
Input current	I _{in}	6.0	—	—	±0.1	—	±1.0	μA	Vin = V _{CC} or GND	
Quiescent supply current	I _{CC}	6.0	—	—	4.0	—	40	μA	Vin = V _{CC} or GND, I _{out} = 0 μA	

AC Characteristics ($C_L = 50$ pF, Input $t_r = t_f = 6$ ns)

Item	Symbol	V_{CC} (V)	Ta = 25°C		Ta = -40 to +85°C		Unit	Test Conditions		
			Min	Typ	Max	Min			Max	
Propagation delay time	t_{PLH}	2.0	—	—	210	—	265	ns	CLK to Y	
	t_{PHL}	4.5	—	20	42	—	53			
		6.0	—	—	36	—	45			
		t_{PLH}	2.0	—	—	140	—	175	ns	G_1 or $\overline{G_2}$ to Y
		t_{PHL}	4.5	—	15	28	—	35		
			6.0	—	—	24	—	30		
Pulse width	t_w	2.0	80	—	—	100	—	ns		
		4.5	16	5	—	20	—			
		6.0	14	—	—	17	—			
Setup time	t_{su}	2.0	50	—	—	65	—	ns		
		4.5	10	2	—	13	—			
		6.0	9	—	—	11	—			
Hold time	t_h	2.0	5	—	—	5	—	ns		
		4.5	5	-1	—	5	—			
		6.0	5	—	—	5	—			
Output rise/fall time	t_{TLH}	2.0	—	—	75	—	95	ns		
	t_{THL}	4.5	—	5	15	—	19			
		6.0	—	—	13	—	16			
Input capacitance	C_{in}	—	—	5	10	—	10	pF		

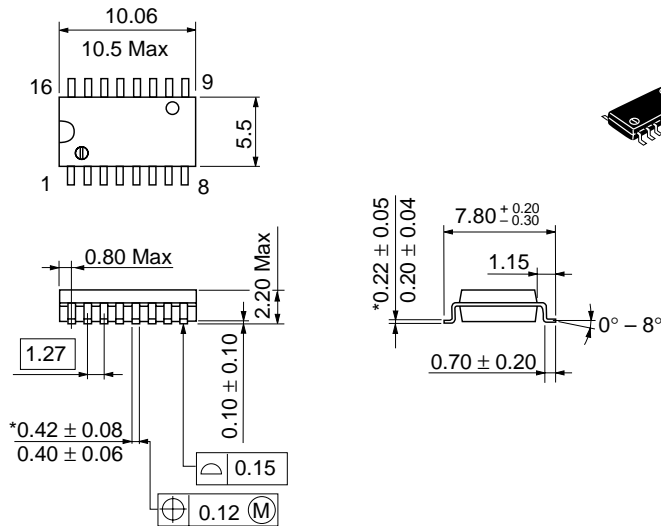
Package Dimensions

Unit: mm



Hitachi Code	DP-16
JEDEC	Conforms
EIAJ	Conforms
Mass (reference value)	1.07 g

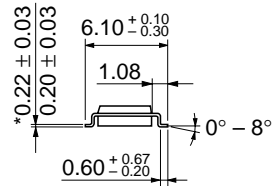
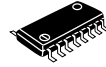
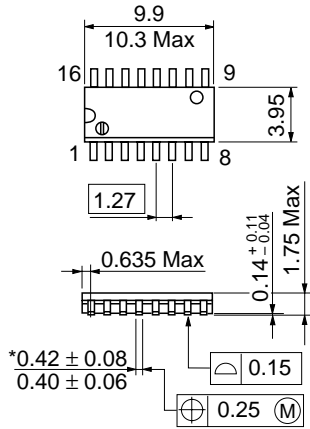
Unit: mm



Hitachi Code	FP-16DA
JEDEC	—
EIAJ	Conforms
Mass (reference value)	0.24 g

*Dimension including the plating thickness
Base material dimension

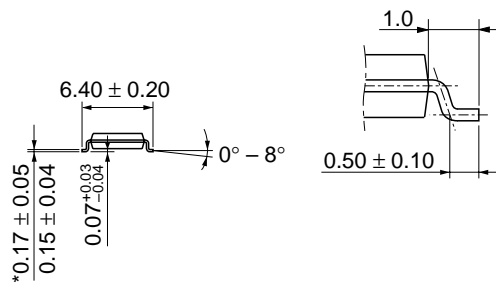
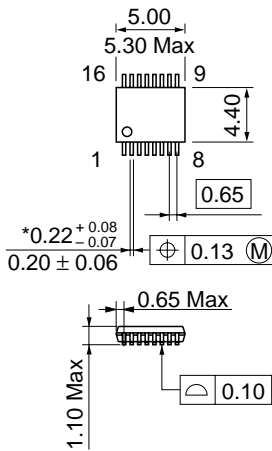
Unit: mm



*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DN
JEDEC	Conforms
EIAJ	Conforms
Mass (reference value)	0.15 g

Unit: mm



*Dimension including the plating thickness
Base material dimension

Hitachi Code	TTP-16DA
JEDEC	—
EIAJ	—
Mass (reference value)	0.05 g

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HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits.

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL NorthAmerica : <http://semiconductor.hitachi.com/>
 Europe : <http://www.hitachi-eu.com/hel/ecg>
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For further information write to:

Hitachi Semiconductor
(America) Inc.
179 East Tasman Drive,
San Jose, CA 95134
Tel: <1> (408) 433-1990
Fax: <1> (408) 433-0223

Hitachi Europe GmbH
Electronic Components Group
Dornacher Straße 3
D-85622 Feldkirchen, Munich
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.
Electronic Components Group.
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 585160

Hitachi Asia Ltd.
Hitachi Tower
16 Collyer Quay #20-00,
Singapore 049318
Tel : <65>-538-6533/538-8577
Fax : <65>-538-6933/538-3877
URL : <http://www.hitachi.com.sg>

Hitachi Asia Ltd.
(Taipei Branch Office)
4/F, No. 167, Tun Hwa North Road,
Hung-Kuo Building,
Taipei (105), Taiwan
Tel : <886>-(2)-2718-3666
Fax : <886>-(2)-2718-8180
Telex : 23222 HAS-TP
URL : <http://www.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower,
World Finance Centre,
Harbour City, Canton Road
Tsim Sha Tsui, Kowloon,
Hong Kong
Tel : <852>-(2)-735-9218
Fax : <852>-(2)-730-0281
URL : <http://www.hitachi.com.hk>

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