

July 2011

FDPF390N15A

N-Channel PowerTrench® MOSFET 150V, 15A, 40m Ω

Features

- $R_{DS(on)}$ = 31m Ω (Typ.)@ V_{GS} = 10V, I_D = 15A
- · Fast Switching Speed
- · Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(\text{on})}$
- · High Power and Current Handling Capability
- · RoHS Compliant

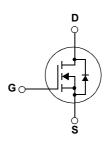
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advance PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Application

- · DC to DC Converters
- · Synchronous Rectification for Server/Telecom PSU
- · Battery Charger
- AC Motor Drives and Uninterruptible Power Supplies
- Off-line UPS





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol		Parameter		Ratings	Units
V _{DSS}	Drain to Source Voltage			150	V
V _{GSS}	Gate to Source Voltage			±20	V
	Drain Current	-Continuous (T _C = 25°C,Silicon Limi	ted)	15	А
ID	Drain Current	-Continuous (T _C = 100°C,Silicon Lin	nited)	10	A
I _{DM}	Drain Current	- Pulsed	(Note 1)	60	Α
E _{AS}	Single Pulsed Avalanche Ene	rgy	(Note 2)	78	mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	6.0	V/ns
D	Davier Dissipation	$(T_C = 25^{\circ}C)$		22	W
P_{D}	Power Dissipation	- Derate above 25°C		0.18	W/°C
T _J , T _{STG}	Operating and Storage Temper	erature Range		-55 to +150	°C
T _L	Maximum Lead Temperature 1/8" from Case for 5 Seconds			300	°C

Thermal Characteristics

Symbol	Parameter Ratings		Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	5.7	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient 62.5		* C/VV

Package Marking and Ordering Information

Ī	Device Marking	Device	Package	Reel Size	Tape Width	Quantity
Ī	FDPF390N15A	FDPF390N15A	TO-220F	-	-	50

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	150	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.1	-	V/°C
1	Zara Cata Valtaga Drain Current	V _{DS} = 120V, V _{GS} = 0V	-	-	1	
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = 120V, T_C = 125^{\circ}C$	-	-	500	μΑ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

١	/ _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2.0	-	4.0	V
F	R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10V, I _D = 15A	-	31	40	mΩ
9) FS	Forward Transconductance	$V_{DS} = 10V, I_D = 15A$ (Note 4)	-	32	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	75777		-	965	1285	pF
C _{oss}	Output Capacitance	V _{DS} = 75V, V _{GS} = 0V ——f = 1MHz		-	96	130	pF
C _{rss}	Reverse Transfer Capacitance	1 - 11/11/2		-	5.8	-	pF
C _{oss(er)}	Energy Related Output Capacitance	$V_{DS} = 75V, V_{GS} = 0V$			169	-	pF
Q _{g(tot)}	Total Gate Charge at 10V			-	14.3	18.6	nC
Q_{gs}	Gate to Source Gate Charge	$V_{DS} = 75V, I_{D} = 27A$			5.0	-	nC
Q _{gs2}	Gate Charge Threshold to Plateau	V _{GS} = 10V		-	2.0	-	nC
Q _{gd}	Gate to Drain "Miller" Charge		(Note 4,5)	-	3.5	-	nC
ESR	Equivalent Series Resistance (G-S)	Drain Open,f = 1MHz		-	1.4	-	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	14	38	ns
t _r		$V_{DD} = 75V, I_D = 27A$	-	10	30	ns
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10V, R_{GEN} = 4.7 Ω	-	20	50	ns
t _f	Turn-Off Fall Time	(Note 4,5)	-	5	20	ns

Drain-Source Diode Characteristics

Is	Maximum Continuous Drain to Source Diode	Maximum Continuous Drain to Source Diode Forward Current		-	15	Α
I _{SM}	Maximum Pulsed Drain to Source Diode For	ward Current	-	-	60	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 15A	-	-	1.25	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 27A, V _{DD} = 75V	-	63	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$ (Note 4)	-	131	-	nC

- Notes:

 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. Starting T_J = 25°C, L = 3 mH, I_{SD} = 7.2 A
- 3. I $_{SD} \leq$ 15A, di/dt \leq 200A/ μ s, $V_{DD} \leq$ BV $_{DSS}$, Starting T $_{J}$ = 25°C
- 4. Pulse Test: Pulse width $\leq 300 \mu s, \, \text{Duty Cycle} \leq 2\%$
- 5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

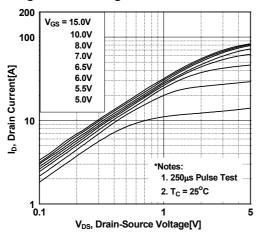


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

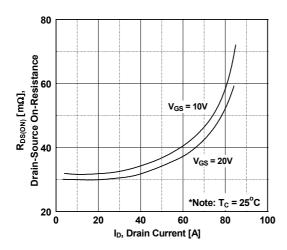


Figure 5. Capacitance Characteristics

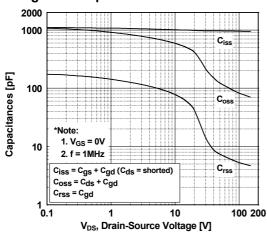


Figure 2. Transfer Characteristics

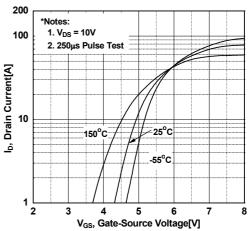


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

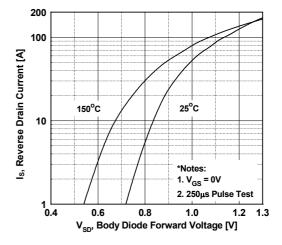
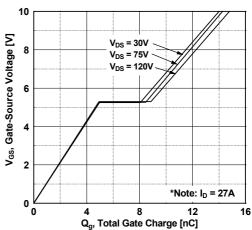


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

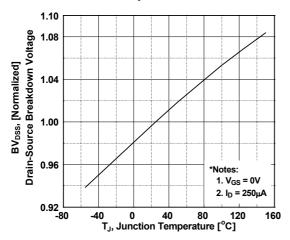


Figure 9. Maximum Safe Operating Area

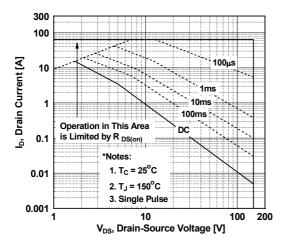


Figure 11. Eoss vs. Drain to Source Volatage

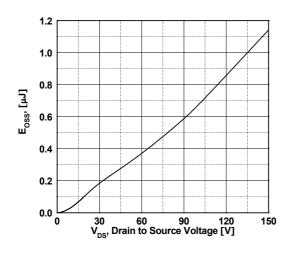


Figure 8. On-Resistance Variation vs. Temperature

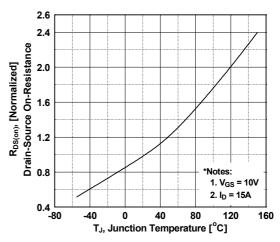


Figure 10. Maximum Drain Current vs. Case Temperature

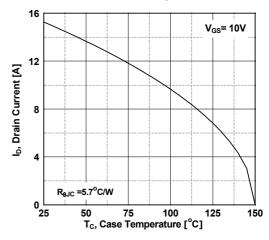
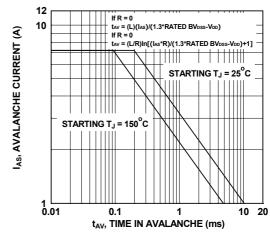
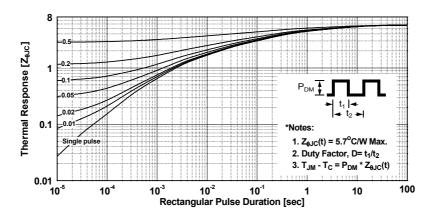


Figure 12. Unclamped Inductive Switching Capability

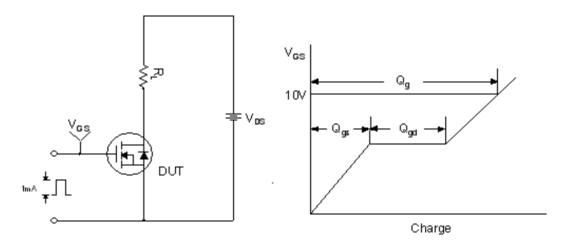


Typical Performance Characteristics (Continued)

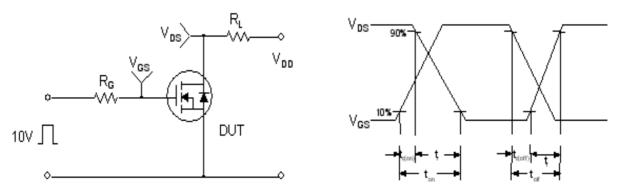




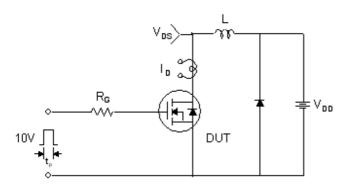
Gate Charge Test Circuit & Waveform

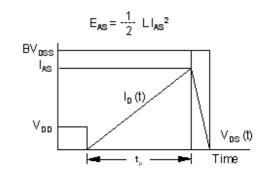


Resistive Switching Test Circuit & Waveforms

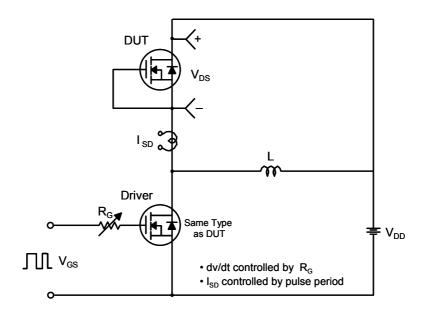


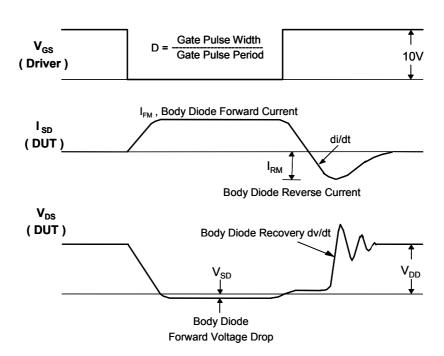
Unclamped Inductive Switching Test Circuit & Waveforms





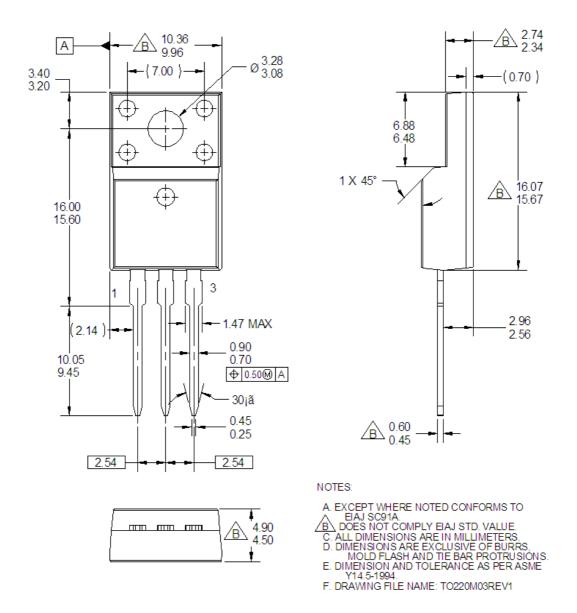
Peak Diode Recovery dv/dt Test Circuit & Waveforms





Package Dimensions

TO-220F (Retractable)



* Front/Back Side Isolation Voltage : AC 2500V

Dimensions in Millimeters





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