TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ126F,TC7SZ126FU

Bus Buffer 3-State Output

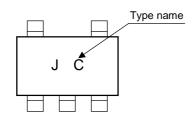
Features

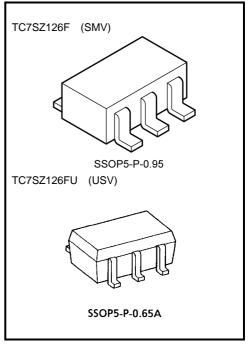
- High output drive: ±24 mA (min) @VCC = 3 V
- Super high speed operation:

 $t_{\mbox{\footnotesize pd}}$ 2.6 ns (typ.) @VcC = 5 V, 50 pF

- Operation voltage range: $V_{CC \text{ (opr)}} = 1.8 \sim 5.5 \text{ V}$
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V VCC.

Marking





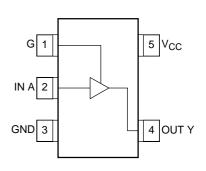
Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5~6	V
DC input voltage	V _{IN}	-0.5~6	V
DC output voltage	V _{OUT}	-0.5~6	V
Input diode current	I _{IK}	±20	mA
Output diode current	I _{OK}	±20	mA
DC output current	l _{OUT}	±50	mA
DC V _{CC} /ground current	I _{CC}	±50	mA
Power dissipation	P _D	200	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10s)	TL	260	°C

Pin Assignment (top view)





Logic Diagram



Truth Table

Int	out	Output			
Α	G	Υ			
Х	L	Z			
L	Н	L			
Н	Н	Н			

X: Don't Care Z: High Impedance

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vaa	1.8~5.5	V	
Supply voltage	Vcc	1.5~5.5 (Note 1)		
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~5.5 (Note 2)	V	
		0~V _{CC} (Note 3)		
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time		$0 \sim 20 \; (V_{CC} = 1.8 \; V, \; 2.5 \; V \pm 0.2 \; V)$		
	dt/dv	$0 \sim 10 \; (V_{CC} = 3.3 \; V \pm 0.3 \; V)$	ns/V	
		$0~5~(V_{CC} = 5.5~V \pm 0.5~V)$		

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: H and Low state



Electrical Characteristics

DC Characteristics

Characteristics Symbol		Cumbal	Test Condition			Ta = 25°C			Ta = -40~85°C		Lloit
		Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	- Unit	
High level		V				0.88 × V _{CC}	_	_	0.88 × V _{CC}	_	
Input voltage	rligirlevei	V _{IH} —		_	2.3~5.5	0.75 × V _{CC}	_	_	0.75 × V _{CC}	_	V
input voitage	Low level	W			1.8		_	0.12 × V _{CC}	_	0.12 × V _{CC}	
	Low level	V _{IL}			2.3~5.5		_	0.25 × V _{CC}		0.25 × V _{CC}	
					1.8	1.7	1.8	_	1.7		
				I _{OH} = -100 μA	2.3	2.2	2.3	_	2.2	_	
				ΙΟΗ - 100 μ/	3.0	2.9	3.0	_	2.9	_	
	High level	Vou	$V_{IN} = V_{IH}$		4.5	4.4	4.5	_	4.4	_	
	riigiricvci	VOH	VIN — VIH	$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9	_	
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	
				$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68	_	2.3	_	
Output valtage			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	V	
Output voltage					1.8	_	0	0.1	_	0.1	l v
			I 100 ·· A	2.3	_	0	0.1	_	0.1		
			$V_{IN} = V_{IL}$	I _{OL} = 100 μA	3.0	_	0	0.1	_	0.1	
	Low level	V			4.5	_	0	0.1	_	0.1	
	Low level	VOL		I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
			I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4		
			I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55		
			I _{OL} = 32 mA	4.5	_	0.22	0.55		0.55		
Input leakage curre	ent	I _{IN}	V _{IN} = 5.5 V or GND		0~5.5	_	_	±1	_	±10	μΑ
3-state output off-s	tate current	l _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \sim 5.5 \text{ V}$		1.8~5.5	_	_	±1	_	±10	μА
Power off leakage	current	l _{OFF}	V _{IN} or V _{OUT} = 5.5 V		0.0	_	_	1	_	10	μА
Quiescent supply of	urrent	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2	_	20	μА

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

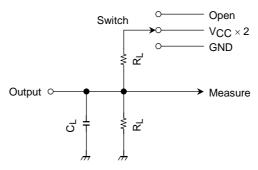
Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.8	2.0	5.3	11.0	2.0	11.5	- ns
			2.5 ± 0.2	0.8	3.4	7.5	0.8	8.0	
Propagation delay time	t _{pLH}		3.3 ± 0.3	0.5	2.5	5.2	0.5	5.5	
Tropagation delay time	t _{pHL}		5.0 ± 0.5	0.5	2.1	4.5	0.5	4.8	
		C _L = 50 pF,	3.3 ± 0.3	1.5	3.2	5.7	1.5	6.0	
		$R_L = 500 \Omega$	5.0 ± 0.5	0.8	2.6	5.0	0.8	5.3	
	t _{pZL}	$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	1.8	2.0	6.1	11.5	2.0	12.0	ns
Output enable time			2.5 ± 0.2	1.5	3.8	8.0	1.5	8.5	
Output enable time			3.3 ± 0.3	1.5	3.2	5.7	1.5	6.0	
			5.0 ± 0.5	0.8	2.3	5.0	0.8	5.3	
Output disable time	t _{pLZ}	$C_L = 50 \text{ pF},$ $R_L = 500 \ \Omega$	1.8	2.0	5.0	11.0	2.0	12.0	
			2.5 ± 0.2	1.0	4.0	8.0	1.5	8.5	
	t _{pHZ}		3.3 ± 0.3	1.0	3.5	5.7	1.0	6.0	
			5.0 ± 0.5	0.5	2.5	4.7	0.5	5.0	
Input capacitance	C _{IN}		0~5.5		4			_	pF
Power dissipation capacitance	C _{PD}	(Note 4)	3.3		17	_	_	_	pF
1 0401 dissipation capacitance	OPD		5.5	_	24	_	_	_	

Note 4: CpD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC

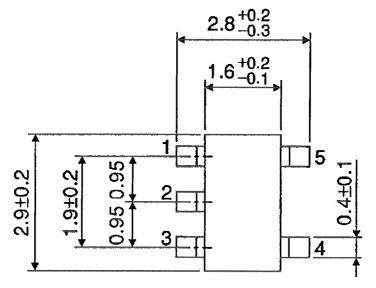
AC Characteristics Measurement Circuit

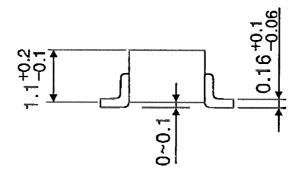


Characteristics	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	$V_{CC} \times 2$
t _{pHZ} , t _{pZH}	GND

Package Dimensions

SSOP5-P-0.95 Unit: mm





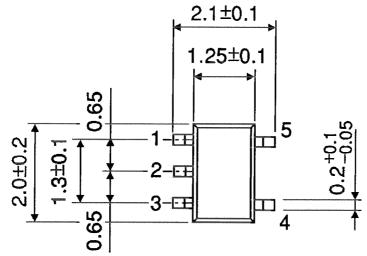
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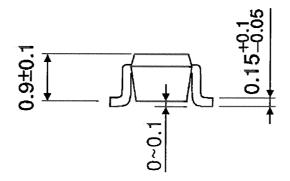
Weight: 0.016 g (typ.)

2001-10-30

Package Dimensions

SSOP5-P-0.65A Unit: mm





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Weight: 0.006 g (typ.)

2001-10-30

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