TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WZ00FU,TC7WZ00FK

#### **Dual 2-Input NAND Gate**

#### **Features**

High output current : ±24 mA (min) at V<sub>CC</sub> = 3 V

• Super high speed operation : t<sub>pd</sub> = 2.4 ns (typ.)

at  $V_{CC}$  = 5 V, 50 pF

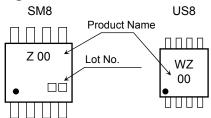
• Operating voltage range : V<sub>CC</sub> = 1.65 to 5.5 V

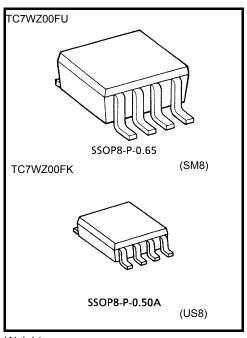
• 5.5-V tolerant inputs

5.5-V power down protection outputs

 Matches the performance of TC74LCX series when operated at 3.3 V V<sub>CC</sub>.

#### Marking





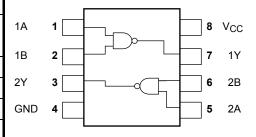
Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>C</sub> C	−0.5 to 6	V	
DC input voltage	V <sub>IN</sub>	−0.5 to 6	V	
DC output voltage	Vour	-0.5 to 6 (Note 1)	V	
DC output voltage	Vout	-0.5 to V <sub>CC</sub> +0.5 (Note 2)	"	
Input diode current	l <sub>IK</sub>	-20	mA	
Output diode current	lok	-20 (Note 3)	mA	
DC output current	lout	±50	mA	
DC V <sub>CC</sub> /ground current	Icc	±50	mA	
Power dissipation	PD	300 (SM8) 200 (US8)	mW	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	
Lead temperature (10s)	TL	260	°C	

## Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0V$ 

Note 2: High or Low state. Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 3: V<sub>OUT</sub> < GND

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# **IEC Logic Symbol**



#### **Truth Table**

Α	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

# **Operating Ranges**

Characteristics	Symbol	Rating	Unit	
Supply voltage	Voc	1.65 to 5.5	V	
Supply voltage	V <sub>CC</sub>	1.5 to 5.5 (Note 4)	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V <sub>OUT</sub>	0 to 5.5 (Note 5)	V	
		0 to V <sub>CC</sub> (Note 6)	V	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
	dt/dv	0 to 20 (V <sub>CC</sub> = 1.80 V $\pm$ 0.15 V, 2.5 V $\pm$ 0.2 V)		
Input rise and fall time		0 to 10 (V <sub>CC</sub> = $3.3 \text{ V} \pm 0.3 \text{ V}$ )	ns/V	
		0 to 5 (V <sub>CC</sub> = 5.0 V ± 0.5 V)		

Note 4: Data retention only

Note 5:  $V_{CC} = 0 V$ 

Note 6: High or low state



### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics		Symbol Test Co.		Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
						Min	Тур.	Max	Min	Max	
High level	V			1.65 to 1.95	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_		
Input	i ligii level	V <sub>IH</sub>	_		2.3 to 5.5	V <sub>CC</sub> × 0.7	_	_	V <sub>CC</sub> × 0.7	_	V
voltage	Low level	V <sub>IL</sub>			1.65 to 1.95	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
	Low level	VIL.		_		_	_	V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3	
					1.65	1.55	1.65	_	1.55	_	
				I <sub>OH</sub> = -100 μA	2.3	2.2	2.3		2.2		
				10Η = -100 μΑ	3.0	2.9	3.0		2.9		
		Vон	.,		4.5	4.4	4.5	_	4.4	_	
	High level		VIN = VIH or VIL	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52	_	1.29	_	V
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9	_	
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	
				I <sub>OH</sub> = -24 mA	3.0	2.3	2.68	_	2.3	_	
Output				$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
voltage		V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	Ι <sub>ΟL</sub> = 100 μΑ	1.65	_	0	0.1	_	0.1	
					2.3	_	0	0.1	_	0.1	
					3.0	_	0	0.1	_	0.1	
					4.5	_	0	0.1	_	0.1	
	Low level			I <sub>OL</sub> = 4 mA	1.65	_	0.08	0.24	_	0.24	
			I <sub>OL</sub> = 8 mA	2.3	_	0.1	0.3	_	0.3	<b> </b>	
				I <sub>OL</sub> = 16 mA	3.0	_	0.15	0.4	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.22	0.55	_	0.55	
				$I_{OL} = 32 \text{ mA}$	4.5	_	0.22	0.55	_	0.55	
Input leakage	current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±1	_	±10	μΑ
Power off lea	kage current	loff	V <sub>IN</sub> or V <sub>OL</sub>	<sub>JT</sub> = 5.5 V	0.0	_	_	1	_	10	μА
Quiescent supply current I <sub>C</sub>		Icc	$V_{IN} = 5.5 \text{ V or GND}$		1.65 to 5.5	_	_	1	_	10	μА

3 2009-09-09

# AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
Propagation delay time	t <sub>PLH</sub>	$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	$1.8\pm0.15$	2.0	5.3	9.6	2.0	9.8	ns
			2.5 ± 0.2	1.2	3.2	5.3	1.2	5.7	
			$3.3 \pm 0.3$	0.8	2.4	3.7	0.8	4.0	
			5.0 ± 0.5	0.5	1.9	2.9	0.5	3.2	
		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	$3.3 \pm 0.3$	1.2	3.0	4.6	1.2	4.9	
			$5.0 \pm 0.5$	0.8	2.4	3.6	0.8	3.9	
Input capacitance	C <sub>IN</sub>	_	0 to 5.5		3.0			_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 7)	3.3		22				pF
			5.5		32			_	

Note 7: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

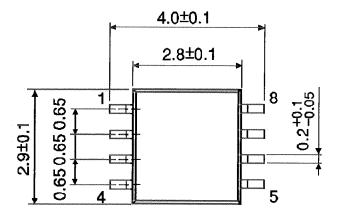
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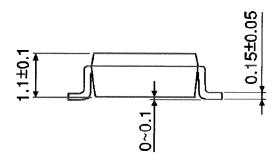
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 

# **Package Dimensions**

SSOP8-P-0.65 Unit: mm

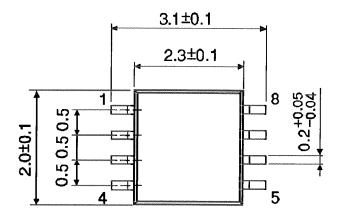


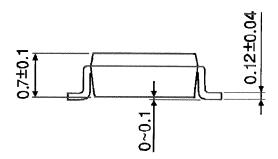


Weight: 0.02 g (typ.)

# **Package Dimensions**

SSOP8-P-0.50A Unit: mm





Weight: 0.01 g (typ.)

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