

TECHNICAL DATA, PROVISIONAL DATA ONLY DATA SHEET 4185, Rev. B

HERMETIC SILICON CARBIDE RECTIFIER

DESCRIPTION: A 1200-VOLT, 20 AMP POWER SILICON CARBIDE RECTIFIER IN A CERAMIC HERMETIC TO-257 PACKAGE (GLASS SEALS NOT AVAILABLE FOR THIS VOLTAGE)

FEATURES:

- NO RECOVERY TIME OR REVERSE RECOVERY LOSSES
- NO TEMPERATURE INFLUENCE ON SWITCHING BEHAVIOR
- **High Frequency Option** Non-magnetic Glidcop leads are available for improved performance at high frequency; use part number prefix SHDG

MAXIMUM RATINGS

ALL RATINGS ARE @ T_C = 25 °C UNLESS OTHERWISE SPECIFIED.

| RATING | SYMBOL | MAX. | UNITS |
|--|-------------------|----------------|-------|
| PEAK INVERSE VOLTAGE | PIV | 1200 | Volts |
| MAXIMUM DC OUTPUT CURRENT (With T_C = 65 $^{\circ}$ C, for part numbers with P and N suffixes) | Io | 20 | Amps |
| MAXIMUM DC OUTPUT CURRENT (With T_C = 65 $^{\circ}$ C, for part number with D suffix or without suffix) | Io | 10 | Amps |
| MAXIMUM REPETITIVE FORWARD SURGE CURRENT (t = 8.3ms, Sine) per leg, T_C = 25 $^{\circ}C$ | I _{FRM} | 50 | Amps |
| MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT (t = 10 μ s, pulse) per leg, T _C = 25 $^{\circ}$ C | I _{FSM} | 250 | Amps |
| MAXIMUM POWER DISSIPATION, T _C = 25 °C | P _d | 40 | W |
| MAXIMUM THERMAL RESISTANCE, Junction to Case (PER DUAL PACKAGE For Common Cathode/Anode Configurations) | R ₀ JC | 1.00 | °C/W |
| MAXIMUM OPERATING AND STORAGE TEMPERATURE RANGE* | Top, Tstg | -55 to +200 | °C |

* Note: SiC semiconductors will handle at or above this operating and storage temperature. However, extended operational use of the packaged device above 175C may reduce its future performance. All qualification testing and screening per MIL-PRF-19500 will only be performed to 175C.

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ELECTRICAL CHARACTERISTICS

| CHARACTERISTIC | TYP | MAX. | UNITS |
|---|------|------|-------|
| MAXIMUM FORWARD VOLTAGE DROP (I_f =10A PER LEG) V_f T_J =25 °C | 1.60 | 1.80 | |
| T _J =175 °C | 2.50 | 3.00 | Volts |
| MAXIMUM REVERSE CURRENT (1200V PIV PER LEG) I_r $T_J = 25$ °C | 0.01 | 0.20 | |
| T _J = 175 °C | 0.02 | 1.00 | mA |
| TOTAL CAPACITIVE CHARGE (V _R =1200V, I _F =10A, di/dt=500A/ μ s and T _J =25°C) Q _C per leg | 60 | N/A | nC |
| MAXIMUM JUNCTION CAPACITANCE (V _r =400V) per leg C _T | 70 | | pF |

Figure 1. Forward Characteristics

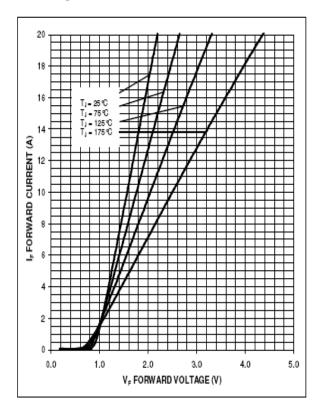
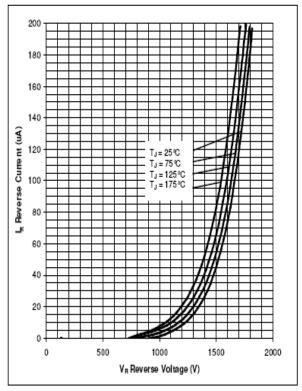


Figure 2. Reverse Characteristics

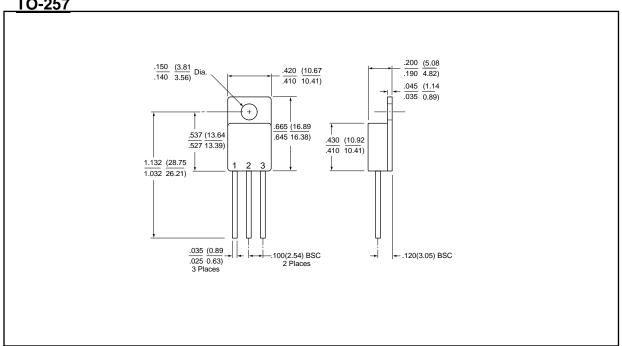


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MECHANICAL DIMENSIONS

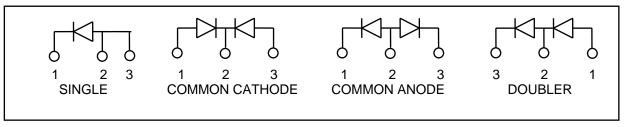




PINOUT TABLE

| TYPE | PIN 1 | PIN 2 | PIN 3 |
|-----------------------------------|-----------|---------|-----------|
| SINGLE RECTIFIER | CATHODE | ANODE | ANODE |
| DUAL RECTIFIER/COMMON CATHODE (P) | ANODE 1 | COMMON | ANODE 2 |
| | | CATHODE | |
| DUAL RECTIFIER/COMMON ANODE (N) | CATHODE 1 | COMMON | CATHODE 2 |
| | | ANODE | |
| DUAL RECTIFIER/DOUBLER (D) | ANODE | ANODE/ | CATHODE |
| | | CATHODE | |

SCHEMATIC



Application Note: Customers should be aware that at the current stage of technical development of SiC, the reverse avalanche capabilities of the device are limited.

Customer designs will need to accommodate these limitations and avoid exposure of the device to this and other potentially damaging conditions in their applications.

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