



Features:

- 3.5" 320x240 dots/pixels (QVGA) resolution color TFT LCD
- High Brightness (430 nits) version available
- Touch panel version available
- Built in timing controller (TCON)
- 24 bit RGB interface

Pin Interface		
Pin #	Symbol	Function
1-2	VBL-	Backlight Cathode Input
3-4	VBL+	Backlight Anode Input
5-6	NC	No Connection
7	POL	Connect to VCOM circuit
8	/RESET	Hardware Reset
9	SPENA	Serial Data Enable Signal
10	SPCLK	Serial Data Clock
11	SPDAT	Serial Data
12-19	B0-B7	Blue data bit 0~7
20-27	G0-G7	Green data bit 0~7
28-35	R0-R7	Red data bit 0~7
36	HSYNC	Horizontal Sync Input
37	VSYNC	Vertical Sync Input
38	DCLK	Pixel Clock
39-40	VDD	Analog Power
41-42	VCC	Digital Power
43-44	NC	Not Used
45	VGL	Gate Off Power
46	NC	No Connection
47	VGH	Gate On Power
48-50	NC	No Connection
51	VCOM	Driving Input
52	ENB	Data Enable Input
53	GND	Ground
54	VSS	Ground

Absolute Maximum Rating				
Item	Sym bol	Condition	Min.	Max
System Power Supply Voltage	VDD	AVSS=0 V	-0.3 V	7.0 V
	VCC	GND=0 V	-0.3 V	7.0 V
	VGH	GND=0V	-0.3 V	32.0 V
	VGL	GND=0V	-22 V	45.0 V
Input Voltage	Vi	-	-0.3 V	VDD+0.3
	V _i	VI	-0.3	VCC+0.3

Electrical Characteristics				
Item	Symbol	Min.	Typ.	Max.
Digital Supply Voltage	VCC	3	3.3V	3.3mA
Digital Operation Current	ICC	-	1.8mA	2.7mA
Analog Supply Voltage	VDD	3.8	5mA	5.5V
Analog Operation Current	IDD	-	5.8mA	8.7mA
Gate On Power	VGH	-	15V	-
Gate Off Power	VGL	-	-10V	-
VCOM High Voltage	VCOMH	2.5	-	4V
VCOM Low Voltage	VCOML	-1.5	-	0V
Backlight Current Consumption	IF	-	20mA	-