

HD74LVC2G53

2-channel Analog Multiplexer/Demultiplexer

REJ03D0156-0200Z

Rev.2.00

Feb.04.2004

Description

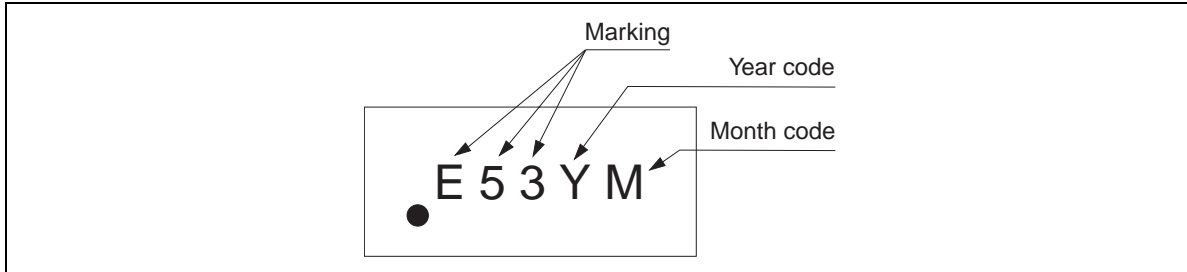
The HD74LVC2G53 has 2-channel analog multiplexer/demultiplexer in an 8 pin package. Applications include signal gating, chopping, modulation, or demodulation (modem), and signal multiplexing for analog to digital and digital to analog conversion systems. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as renesas uni logic series.
- Supply voltage range: 1.65 to 5.5 V
Operating temperature range: -40 to +85°C
- Control inputs: $V_{IH}(\text{Max.}) = 5.5 \text{ V}$ (@ $V_{CC} = 0 \text{ V}$ to 5.5 V)
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LVC2G53CPE	WCSP-8 pin	TBS-8V	CP	E (3,000 pcs/reel)

Article Indication



Function Table

Control inputs

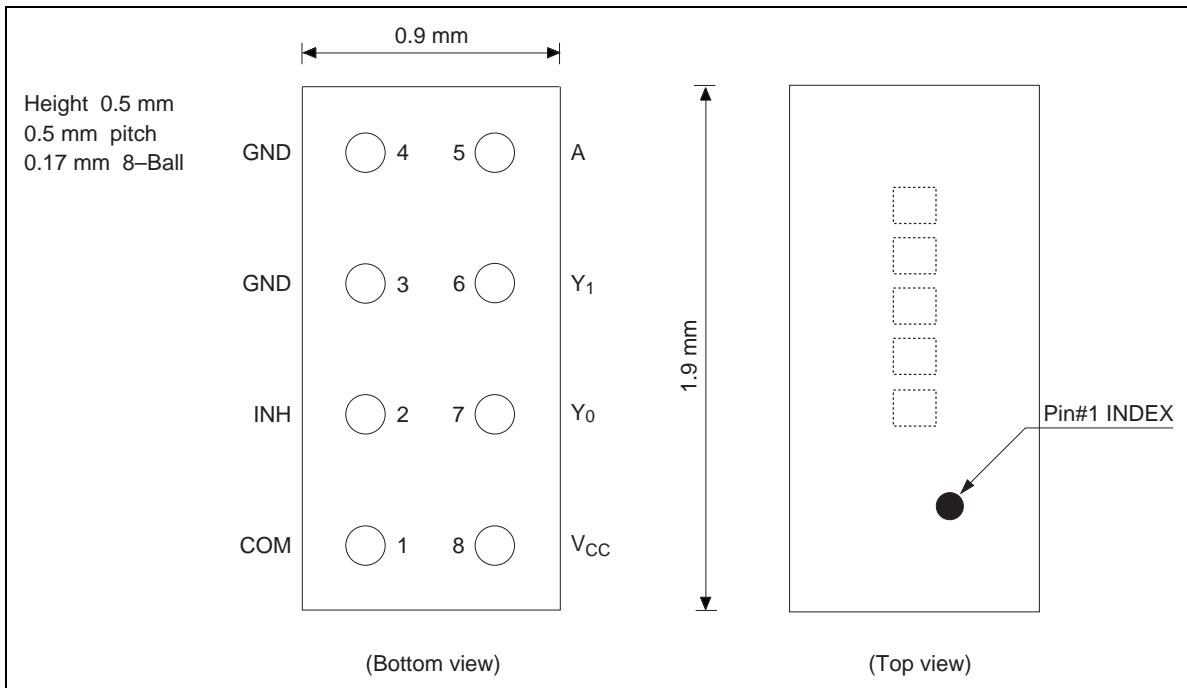
INH	A	On channel
H	X	None
L	H	Y ₁
L	L	Y ₀

H : High level

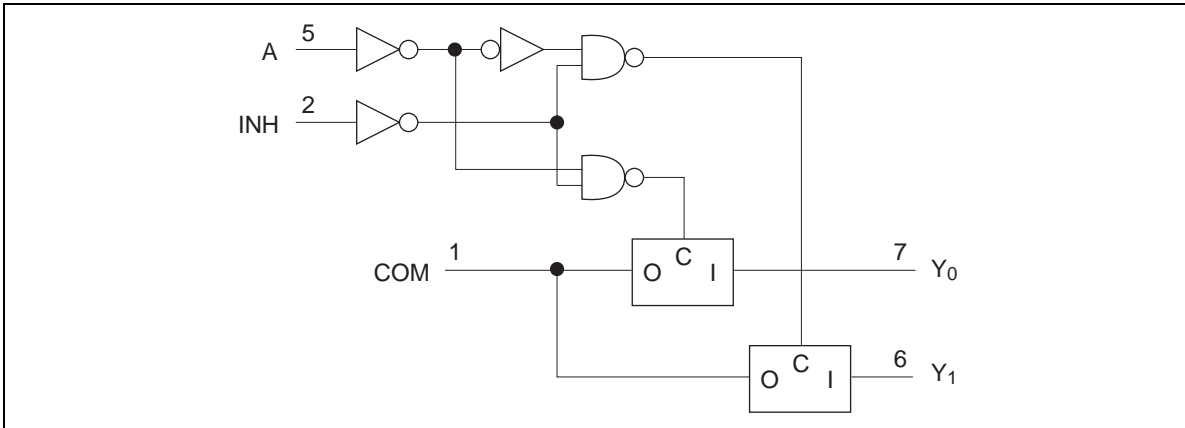
L : Low level

X : Immaterial

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V_{CC}	-0.5 to 6.5	V	
Input voltage range ^{*1}	V_I	-0.5 to 6.5	V	
Output voltage range ^{*1, 2}	V_O	-0.5 to $V_{CC} + 0.5$	V	Output : H or L
Input clamp current	I_{IK}	-50	mA	$V_I < 0$
Output clamp current	I_{OK}	-50	mA	$V_O < 0$
Continuous output current	I_O	± 50	mA	$V_O = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	± 100	mA	
Package Thermal impedance	θ_{ja}	140	$^{\circ}\text{C}/\text{W}$	
Storage temperature	T_{stg}	-65 to 150	$^{\circ}\text{C}$	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
2. This value is limited to 5.5 V maximum.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	1.65	5.5	V	
Input voltage range	V_I	0	5.5	V	
Output voltage range	V_O	0	V_{CC}	V	
Input transition rise or fall rate	$\Delta t / \Delta v$	0	20	ns / V	$V_{CC} = 1.65$ to 1.95 V, 2.3 to 2.7 V
		0	10		$V_{CC} = 3.0$ to 3.6 V
		0	10		$V_{CC} = 4.5$ to 5.5 V
Operating free-air temperature	T_a	-40	85	$^{\circ}\text{C}$	

Note: Unused or floating inputs must be held high or low.

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Electrical Characteristics

- $T_a = -40$ to 85°C

Item	Symbol	V _{CC} (V)	Min	Typ	Max	Unit	Test condition
Input voltage	V _{IH}	1.65 to 1.95	V _{CC} ×0.65	—	—	V	Control input only.
		2.3 to 2.7	V _{CC} ×0.7	—	—		
		3.0 to 3.6	V _{CC} ×0.7	—	—		
		4.5 to 5.5	V _{CC} ×0.7	—	—		
	V _{IL}	1.65 to 1.95	—	—	V _{CC} ×0.35		
		2.3 to 2.7	—	—	V _{CC} ×0.3		
		3.0 to 3.6	—	—	V _{CC} ×0.3		
		4.5 to 5.5	—	—	V _{CC} ×0.3		
On-state switch resistance	R _{ON}	1.65	—	13	30	Ω	I _S = 4 mA
		2.3	—	10	20		I _S = 8 mA
		3.0	—	8.5	17		I _S = 24 mA
		4.5	—	6.5	13		I _S = 32 mA
Peak on resistance R _{ON} (P)	R _{ON} (P)	1.65	—	86.5	120		I _S = 4 mA
		2.3	—	23	30		I _S = 8 mA
		3.0	—	13	20		I _S = 24 mA
		4.5	—	8	15		I _S = 32 mA
Difference of on-state resistance between switches	ΔR _{ON}	1.65	—	—	7		I _S = 4 mA
		2.3	—	—	5		I _S = 8 mA
		3.0	—	—	3		I _S = 24 mA
		4.5	—	—	2		I _S = 32 mA
Off-state switch leakage current	I _S (OFF)	5.5	—	—	±1.0	μA	V _I = V _{CC} and V _O = GND or V _I = GND and V _O = V _{CC} , V _{INH} = V _{IH}
On-state switch leakage current	I _S (ON)	5.5	—	—	±1.0	μA	V _I = V _{CC} or GND, V _{INH} = V _{IL} , V _O = Open
Control input current	I _{IN}	5.5	—	—	±1.0	μA	V _{IN} = V _{CC} or GND
Quiescent supply current	I _{CC}	5.5	—	—	10	μA	V _{IN} = V _{CC} or GND
					1.0*1		
	ΔI _{CC}	5.5	—	—	500	μA	V _C = V _{CC} −0.6 V
Control input capacitance	C _{IC}	5.0	—	3.5	—	pF	
Switch terminal capacitance	C _{I/O} (OFF)	5.0	—	6.5	—	pF	Y
				10			COM
	C _{I/O} (ON)	5.0	—	14.0	—		

Note: 1. $T_a = 25^\circ\text{C}$

Switching Characteristics

- $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation* ¹ delay time	t_{PLH}	—	2.0	ns	$C_L = 30 \text{ pF}, R_L = 1.0 \text{ k}\Omega$	COM or Yn	Yn or COM
	t_{PHL}						
Enable time	t_{ZH}	3.3	9.0		$C_L = 30 \text{ pF}, R_L = 1.0 \text{ k}\Omega$	INH	COM or Yn
	t_{ZL}						
Disable time	t_{HZ}	3.2	10.9		$C_L = 30 \text{ pF}, R_L = 1.0 \text{ k}\Omega$	INH	COM or Yn
	t_{LZ}						
Enable time	t_{ZH}	2.9	10.3		$C_L = 30 \text{ pF}, R_L = 1.0 \text{ k}\Omega$	A	Yn
	t_{ZL}						
Disable time	t_{HZ}	2.1	9.4		$C_L = 30 \text{ pF}, R_L = 1.0 \text{ k}\Omega$	A	Yn
	t_{LZ}						

- $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation* ¹ delay time	t_{PLH}	—	1.2	ns	$C_L = 30 \text{ pF}, R_L = 500 \Omega$	COM or Yn	Yn or COM
	t_{PHL}						
Enable time	t_{ZH}	2.5	6.1		$C_L = 30 \text{ pF}, R_L = 500 \Omega$	INH	COM or Yn
	t_{ZL}						
Disable time	t_{HZ}	2.3	9.3		$C_L = 30 \text{ pF}, R_L = 500 \Omega$	INH	COM or Yn
	t_{LZ}						
Enable time	t_{ZH}	2.1	7.2		$C_L = 30 \text{ pF}, R_L = 500 \Omega$	A	Yn
	t_{ZL}						
Disable time	t_{HZ}	1.4	7.9		$C_L = 30 \text{ pF}, R_L = 500 \Omega$	A	Yn
	t_{LZ}						

- $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation* ¹ delay time	t_{PLH}	—	0.8	ns	$C_L = 50 \text{ pF}, R_L = 500 \Omega$	COM or Yn	Yn or COM
	t_{PHL}						
Enable time	t_{ZH}	2.2	5.4		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	INH	COM or Yn
	t_{ZL}						
Disable time	t_{HZ}	2.3	8.1		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	INH	COM or Yn
	t_{LZ}						
Enable time	t_{ZH}	1.9	5.8		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	A	Yn
	t_{ZL}						
Disable time	t_{HZ}	1.1	7.2		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	A	Yn
	t_{LZ}						

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Switching Characteristics (cont.)

- $V_{CC} = 5.0 \pm 0.5 \text{ V}$

Item	Symbol	Ta = -40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation* ¹ delay time	t _{PLH} t _{PHL}	—	0.6	ns	C _L = 50 pF, R _L = 500 Ω	COM or Yn	Yn or COM
Enable time	t _{ZH} t _{ZL}	1.8	4.5		C _L = 50 pF, R _L = 500 Ω	INH	COM or Yn
Disable time	t _{HZ} t _{LZ}	1.6	8.0		C _L = 50 pF, R _L = 500 Ω	INH	COM or Yn
Enable time	t _{ZH} t _{ZL}	1.3	5.4		C _L = 50 pF, R _L = 500 Ω	A	Yn
Disable time	t _{HZ} t _{LZ}	1.0	5.0		C _L = 50 pF, R _L = 500 Ω	A	Yn

Notes: 1. The propagation delay is calculated RC time constant of typical on-state resistance of the switch and the specified load capacitance, when driven by an ideal voltage source (zero output impedance).

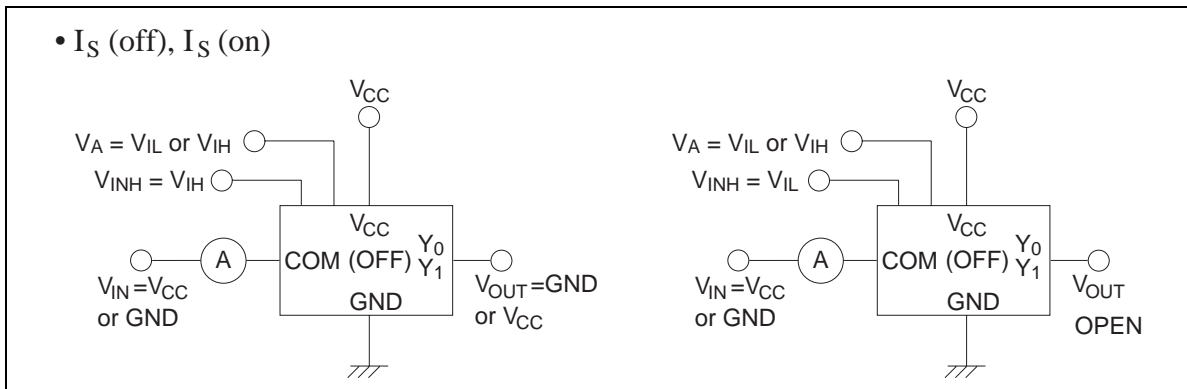
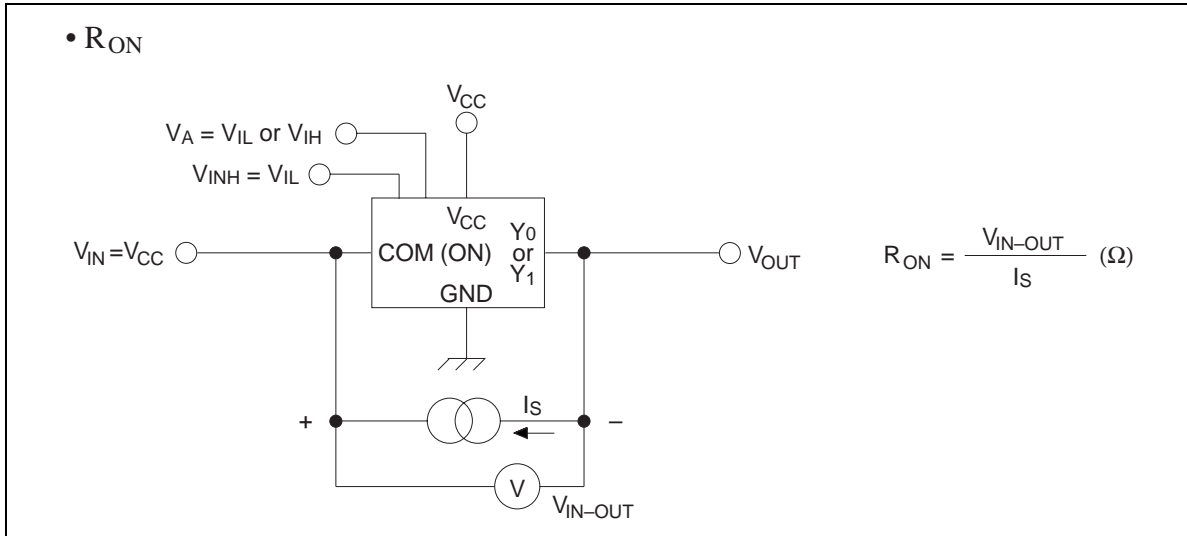
Analog Switch Characteristics

Item	Ta = 25°C			Unit	Test conditions	FROM (Input)	TO (Output)			
	V _{CC} (V)	Min	Typ					Max		
Frequency response (Switch ON)	1.65	—	35	—	MHz	C _L = 50 pF, Adjust fin voltage to obtain 0dBm at output when fin is 1MHz (sine wave). Increase fin frequency until the dB-meter reads -3 dBm.	COM or Y	Y or COM		
	2.3	—	120	—						
	3.0	—	190	—						
	4.5	—	215	—						
	1.65	—	>300	—	C _L = 5 pF, R _L = 50 Ω	20 log(V _O /V _I) = -3 dBm				
	2.3	—	>300	—						
	3.0	—	>300	—						
	4.5	—	>300	—						
Crosstalk (between switches)	1.65	—	-58	—	dB	C _L = 50 pF, Adjust fin voltage to obtain 0dBm at input when fin is 1MHz (sine wave).	COM	Y		
	2.3	—	-58	—						
	3.0	—	-58	—						
	4.5	—	-58	—						
	1.65	—	-42	—	C _L = 5 pF, R _L = 50 Ω					
	2.3	—	-42	—						
	3.0	—	-42	—						
	4.5	—	-42	—						
Crosstalk (Control input to signal output)	1.65	—	35	—	mV	C _L = 50 pF, Adjust RL value to obtain 0A at I _{IN/OUT} when fin is 1MHz (square wave)	INH	COM or Y		
	2.3	—	50	—						
	3.0	—	70	—						
	4.5	—	100	—						
Feed through attenuation (Switch OFF)	1.65	—	-60	—	dB	C _L = 50 pF, Adjust fin voltage to obtain 0dBm at input when fin is 1MHz (sine-wave)	COM or Y	Y or COM		
	2.3	—	-60	—						
	3.0	—	-60	—						
	4.5	—	-60	—						
	1.65	—	-50	—	C _L = 5 pF, R _L = 50 Ω					
	2.3	—	-50	—						
	3.0	—	-50	—						
	4.5	—	-50	—						
Sine-wave distortion	1.65	—	0.1	—	%	C _L = 50 pF, R _L = 10 kΩ, fin = 1kHz (sine-wave)	V _I =1.4V _{P-P} , V _{CC} =1.65V	COM or Y	Y or COM	
	2.3	—	0.025	—						V _I =2.0V _{P-P} , V _{CC} =2.3V
	3.0	—	0.015	—						
	4.5	—	0.01	—						V _I =4.0V _{P-P} , V _{CC} =4.5V
	1.65	—	0.15	—	C _L = 50 pF, R _L = 10 kΩ, fin = 10kHz (sine-wave)					
	2.3	—	0.025	—						
	3.0	—	0.015	—						
	4.5	—	0.01	—						

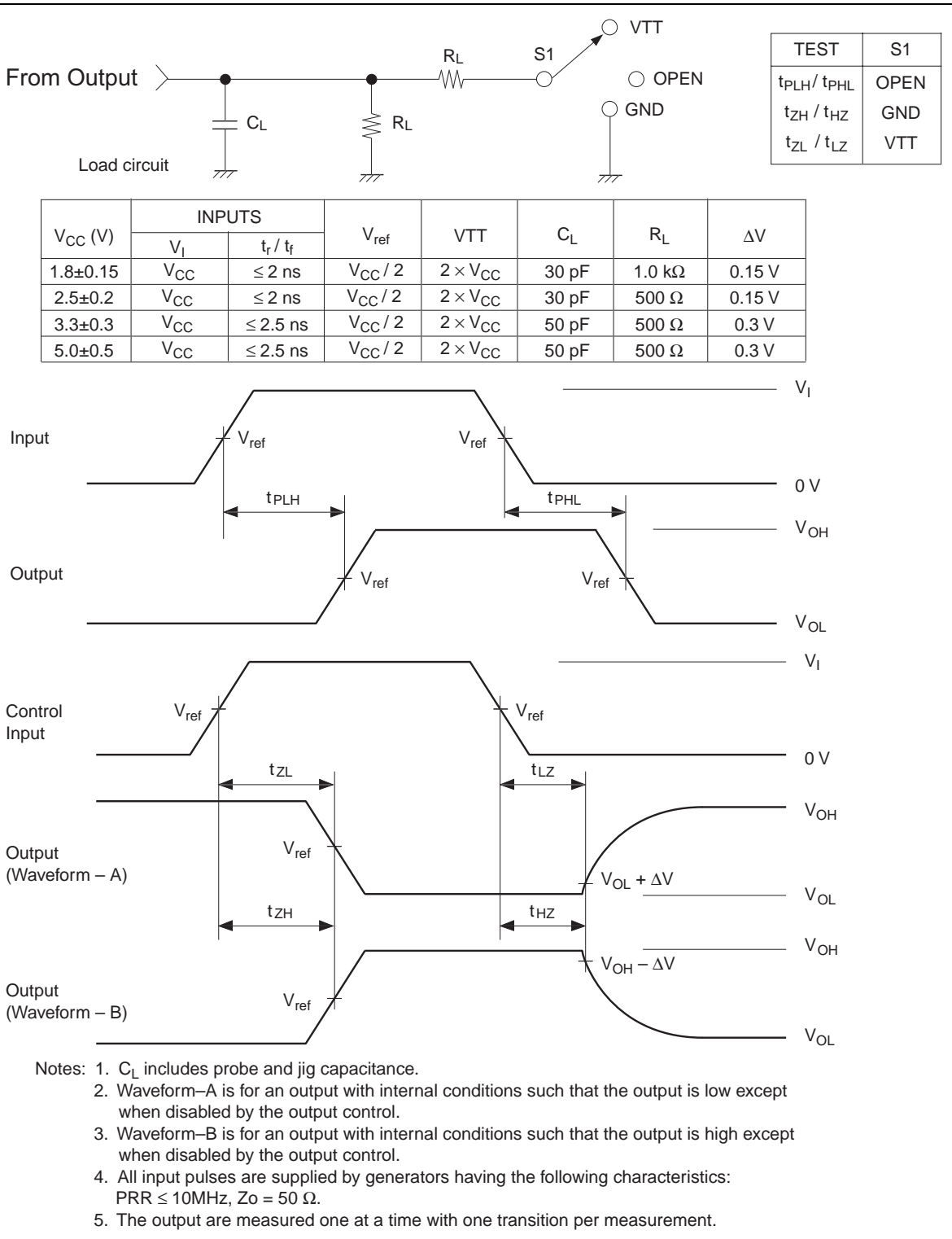
Operating Characteristics

Item	Symbol	V _{CC} (V)	Ta = 25°C			Unit	Test Conditions
			Min	Typ	Max		
Power dissipation capacitance	C _{PD}	1.8	—	9	—	pF	f = 10 MHz
		2.5	—	10	—		
		3.3	—	10	—		
		5.0	—	12	—		

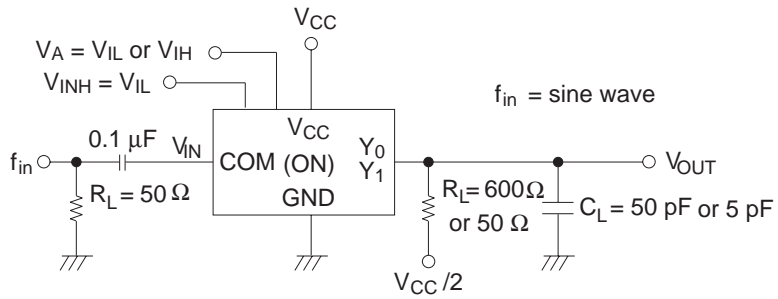
Test Circuit



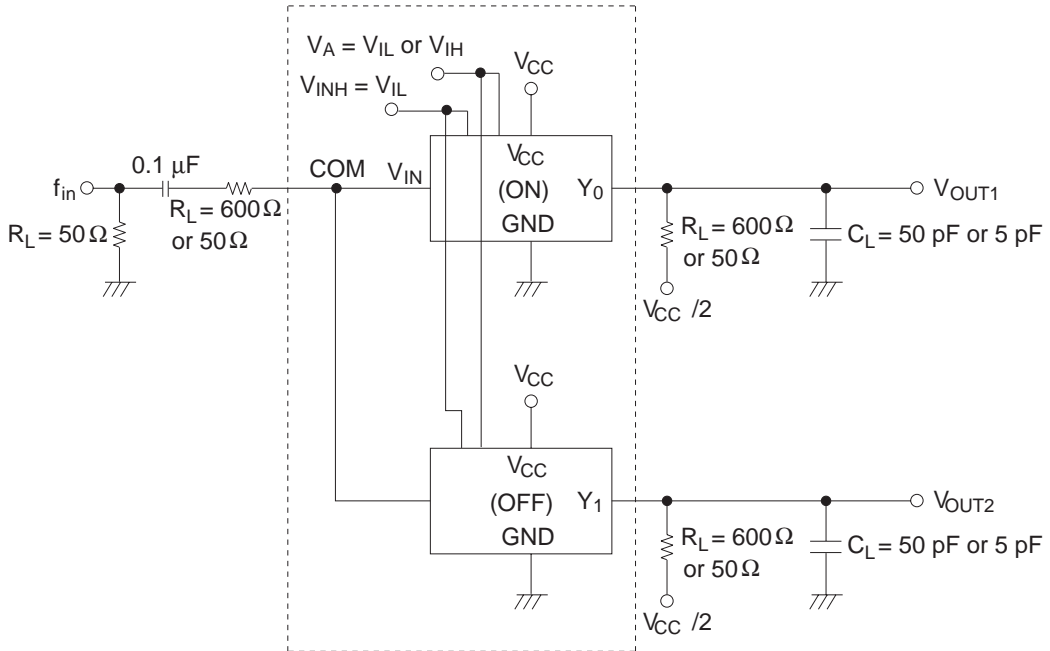
Test Circuit (cont.)



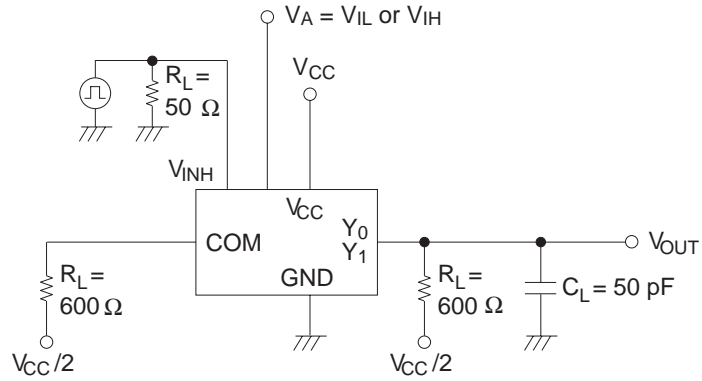
Frequency response (Switch ON)



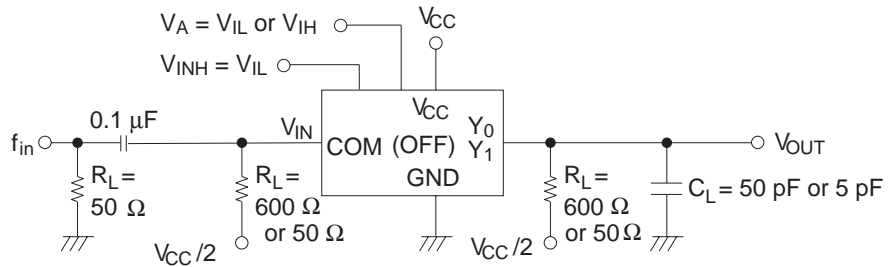
Crosstalk (Between any switches)



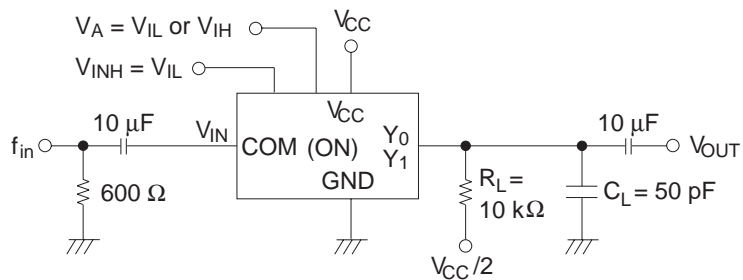
Crosstalk (Control input to signal output)



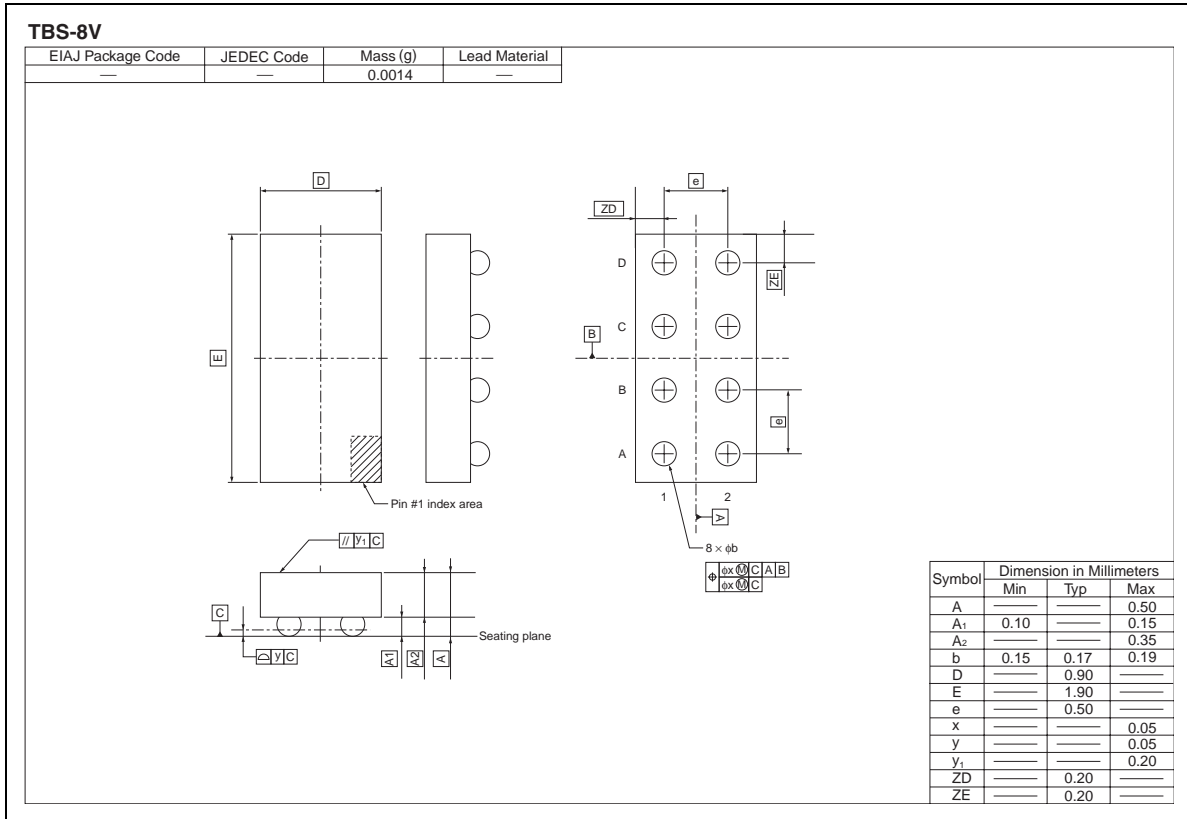
Feedthrough attenuation (Switch OFF)



Sine-wave distortion



Package Dimensions



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Renesas Technology (Shanghai) Co., Ltd.

26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

