N-channel TrenchMOS standard level FET

Rev. 02 — 21 June 2010

Product data sheet

1. Product profile

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

- Low conduction losses due to low on-state resistance
- Q101 compliant

Suitable for standard level gate drive sources

 Suitable for thermally demanding environments due to 175 °C rating

1.3 Applications

- 12 V and 24 V loads
- Automotive systems
- General purpose power switching
- Motors, lamps and solenoids



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Table 1.	Quick reference da	ta					
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	55	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; see <u>Figure 1</u> ; see <u>Figure 3</u>	<u>[1]</u>	-	-	75	A
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	-	254	W
Static cha	racteristics						
R _{DSon}	drain-source on-state resistance	$\label{eq:VGS} \begin{array}{l} V_{GS} = 10 \text{ V}; \text{ I}_{D} = 25 \text{ A}; \\ T_{j} = 25 \ ^{\circ}\text{C}; \text{ see } \overline{\text{Figure 11}}; \\ \text{see } \overline{\text{Figure 12}} \end{array}$		-	5.1	6	mΩ
Avalanche	e ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$ \begin{split} I_D &= 75 \text{ A}; V_{sup} \leq 55 \text{ V}; \\ R_{GS} &= 50 \Omega; V_{GS} = 10 \text{ V}; \\ T_{j(init)} &= 25 ^\circ\text{C}; \text{unclamped} \end{split} $		-	-	680	mJ
Dynamic o	haracteristics						
Q _{GD}	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $V_{DS} = 44 \text{ V}; T_j = 25 \text{ °C};$ see Figure 13		-	19	-	nC

1.4 Quick reference data

[1] Continuous current is limited by package.

2. Pinning information

Table 2.	Pinning	g information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		2
2	D	drain	mb	D D
3	S	source		
mb	D	mounting base; connected to drain		mbbo76 S

SOT78A (TO-220AB)

3. Ordering information

Table 3. Ordering	information		
Type number	Package		
	Name	Description	Version
BUK7506-55B	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78A
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4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	55	V
V _{DGR}	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$		-	-	55	V
V _{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	T_{mb} = 25 °C; V_{GS} = 10 V; see <u>Figure 3</u> ; see <u>Figure 1</u>	<u>[1]</u>	-	-	145	А
		T_{mb} = 100 °C; V_{GS} = 10 V; see <u>Figure 1</u>	[2]	-	-	75	А
		T_{mb} = 25 °C; V_{GS} = 10 V; see <u>Figure 1</u> ; see <u>Figure 3</u>	[2]	-	-	75	A
I _{DM}	peak drain current	T_{mb} = 25 °C; $t_p \le 10 \ \mu$ s; pulsed; see <u>Figure 3</u>		-	-	582	A
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	-	254	W
T _{stg}	storage temperature			-55	-	175	°C
Tj	junction temperature			-55	-	175	°C
Source-draii	n diode						
ls	source current	T _{mb} = 25 °C	<u>[1]</u>	-	-	145	А
			[2]	-	-	75	А
I _{SM}	peak source current	$t_p \le 10 \ \mu s$; pulsed; $T_{mb} = 25 \ ^{\circ}C$		-	-	582	А
Avalanche r	uggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$\label{eq:ld} \begin{array}{l} I_D = 75 \text{ A}; \ V_{sup} \leq 55 \text{ V}; \ R_{GS} = 50 \ \Omega; \\ V_{GS} = 10 \ \text{V}; \ T_{j(init)} = 25 \ ^\circ\text{C}; \ \text{unclamped} \end{array}$		-	-	680	mJ

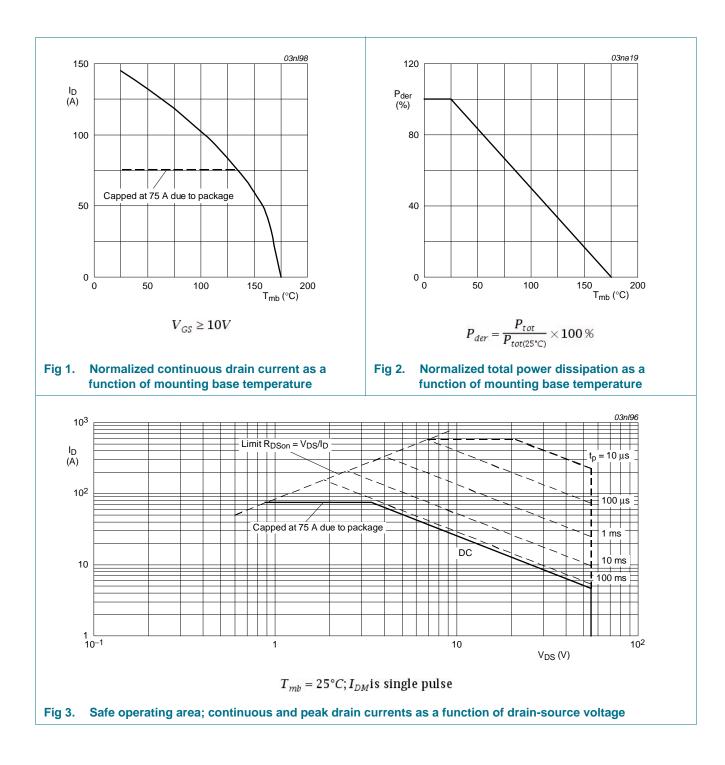
[1] Current is limited by power dissipation chip rating.

[2] Continuous current is limited by package.

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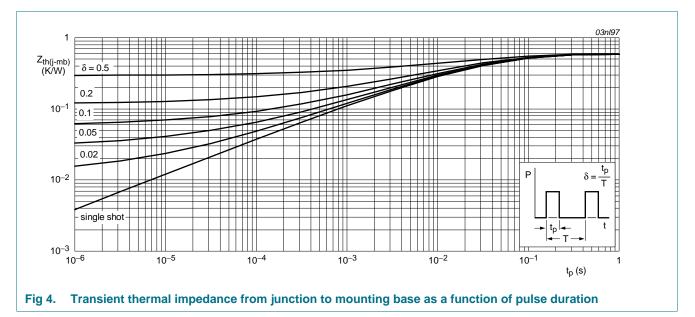
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5. Thermal characteristics

Table 5.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	see <u>Figure 4</u>	-	-	0.59	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	vertical in still air	-	-	60	K/W

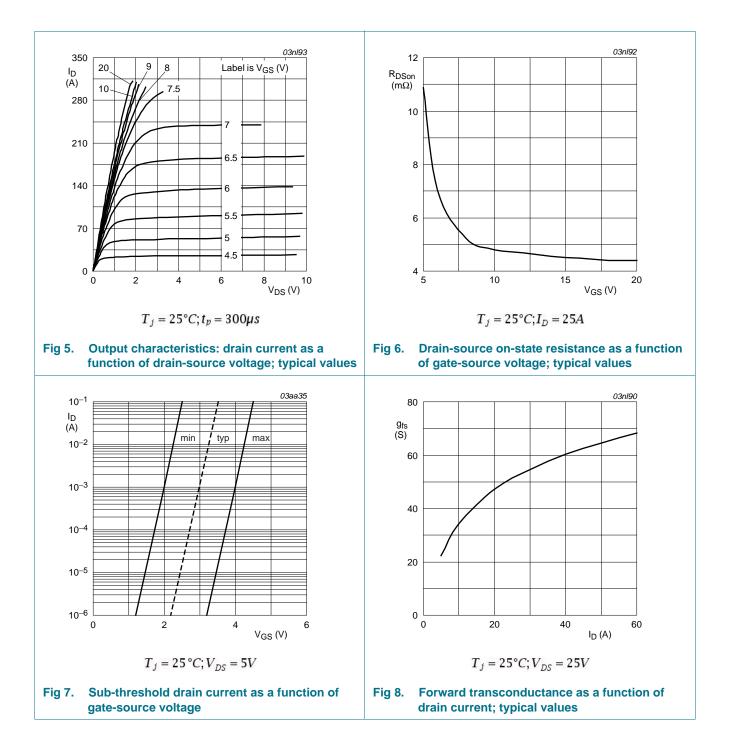


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6. Characteristics

able 6.	Characteristics			_	_	
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Static cha	aracteristics					
V _{(BR)DSS}	drain-source	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	55	-	-	V
	breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	50	-	-	V
V _{GS(th)}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ see <u>Figure 10</u>	-	-	4.4	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ see <u>Figure 10</u>	2	3	4	V
		I _D = 1 mA; V _{DS} = V _{GS} ; T _j = 175 °C; see <u>Figure 10</u>	1	-	-	V
DSS	drain leakage current	$V_{DS} = 55 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.02	1	μA
		$V_{DS} = 55 \text{ V}; V_{GS} = 0 \text{ V}; \text{ T}_{j} = 175 \text{ °C}$	-	-	500	μA
GSS	gate leakage current	$V_{DS} = 0 \text{ V}; V_{GS} = 20 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
		V_{DS} = 0 V; V_{GS} = -20 V; T_j = 25 °C	-	2	100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 175 °C; see <u>Figure 11</u> ; see <u>Figure 12</u>	-	-	12	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; see <u>Figure 11</u> ; see <u>Figure 12</u>	-	5.1	6	mΩ
Dynamic	characteristics					
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 44 \text{ V}; V_{GS} = 10 \text{ V};$	-	64	-	nC
Q _{GS}	gate-source charge	$T_j = 25 \text{ °C}; \text{ see } Figure 13$	-	14	-	nC
ସ୍ _{GD}	gate-drain charge		-	19	-	nC
C _{iss}	input capacitance	$V_{GS} = 0 V; V_{DS} = 25 V; f = 1 MHz;$	-	3825	5100	pF
C _{oss}	output capacitance	$T_j = 25 \text{ °C}; \text{ see } Figure 14$	-	783	940	pF
C _{rss}	reverse transfer capacitance		-	235	322	pF
d(on)	turn-on delay time		-	30	-	ns
r	rise time		-	46	-	ns
d(off)	turn-off delay time	$V_{DS} = 30 \text{ V}; \text{ R}_{L} = 1.2 \Omega; \text{ V}_{GS} = 10 \text{ V};$	-	85	-	ns
f	fall time	$R_{G(ext)} = 10 \ \Omega; T_j = 25 \ ^{\circ}C$	-	39	-	ns
-D	internal drain inductance	from contact screw on mounting base to centre of die; $T_j = 25 \text{ °C}$	-	3.5	-	nH
		from drain lead 6 mm from package to centre of die; $T_j = 25 \text{ °C}$	-	4.5	-	nH
-S	internal source inductance	from source lead to source bond pad ; $T_j = 25 \ ^{\circ}C$	-	7.5	-	nH
Source-d	rain diode					
V _{SD}	source-drain voltage	I _S = 25 A; V _{GS} = 0 V; T _j = 25 °C; see <u>Figure 15</u>	-	0.85	1.2	V
rr	reverse recovery time	$I_{S} = 20 \text{ A}; \text{ d}I_{S}/\text{d}t = -100 \text{ A}/\mu\text{s};$	-	73	-	ns
Q _r	recovered charge	V _{GS} = -10 V; V _{DS} = 30 V; T _j = 25 °C	-	82	-	nC
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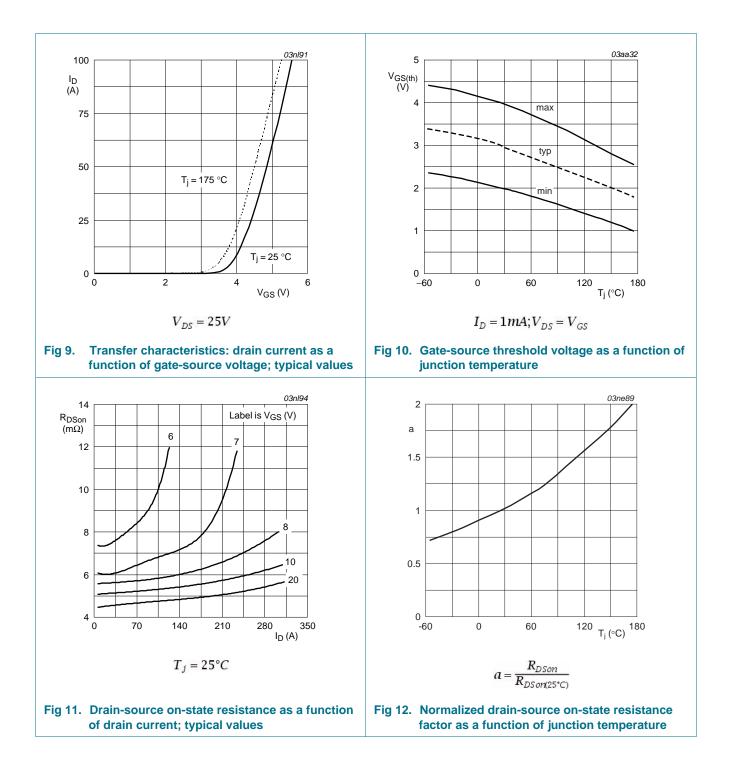


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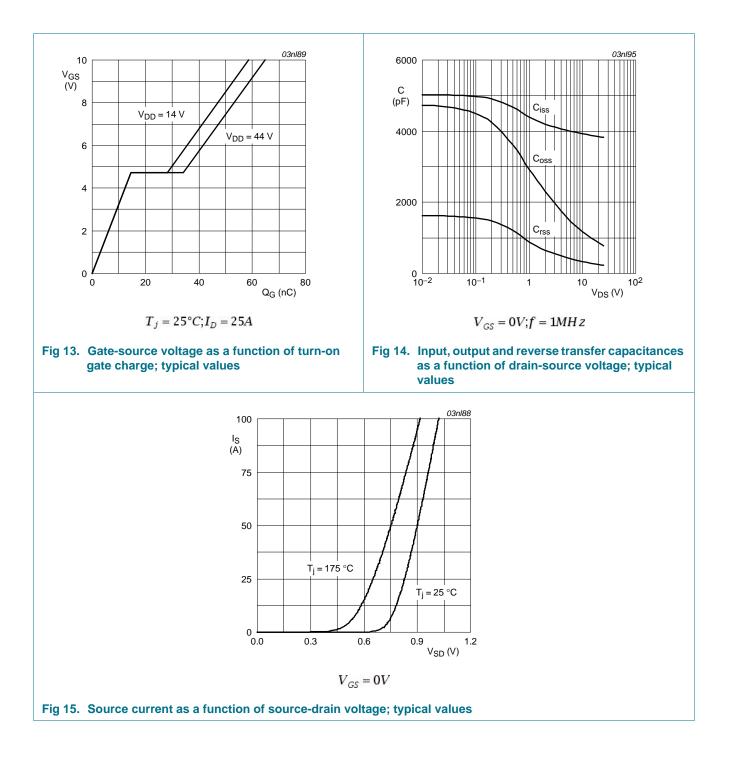
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7. Package outline

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Fig 16. Package outline SOT78A (TO-220AB)

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8. Revision history

Table 7. Revision his	tory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK7506-55B v.2	20100621	Product data sheet	-	BUK75_7606_55B v.1
Modifications:		of this data sheet has been niconductors.	redesigned to comply with	the new identity guidelines
	 Legal texts 	have been adapted to the	new company name where	appropriate.
	 Type numb 	er BUK7506-55B separate	d from data sheet BUK75_7	′606_55B v.1.
BUK75_7606_55B v.1	20030331	Product data sheet	-	-

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9. Legal information

9.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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