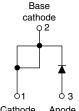


## Vishay Semiconductors

# Ultrafast Rectifier, 15 A FRED Pt®





**TO-220AC** 

I	
•	<b>*</b>
01	03

+	<b>*</b>
01	03
Cathode	Anode

PRODUCT SUMMARY					
Package	TO-220AC				
I <sub>F(AV)</sub>	15 A				
$V_{R}$	300 V				
V <sub>F</sub> at I <sub>F</sub>	1.25 V				
t <sub>rr</sub> typ.	See Recovery table				
T <sub>J</sub> max.	175 °C				
Diode variation	Single die				

### **FEATURES**

- · Ultrafast recovery time
- · Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Compliant to RoHS Directive 2002/95/EC
- · Designed and qualified for industrial level





### **DESCRIPTION**

300 V series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Repetitive peak reverse voltage	V <sub>RRM</sub>		300	V			
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 142 °C	15	Δ.			
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	140	A			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	UNITS				
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	300	-	-		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	1.05	1.25	V	
		I <sub>F</sub> = 15 A, T <sub>J</sub> = 125 °C	-	0.85	1.00		
Reverse leakage current I <sub>R</sub>		V <sub>R</sub> = V <sub>R</sub> rated	-	0.05	40		
		$T_J = 125 ^{\circ}\text{C},  V_R = V_R \text{ rated}$	-	12	400	μΑ	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 300 V	-	45	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH	

# VS-15ETH03PbF

# Vishay Semiconductors Ultrafast Rectifier, 15 A FRED Pt®



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 A$ , $dI_F/dt =$	50 A/μs, V <sub>R</sub> = 30 V	-	-	40	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 15 A dI <sub>F</sub> /dt = - 200 A/μs V <sub>R</sub> = 200 V	-	32	-	ns
		T <sub>J</sub> = 125 °C		=	45	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	2.4	-	А
		T <sub>J</sub> = 125 °C		=	6.1	-	_ A
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		=	38	-	200
		T <sub>J</sub> = 125 °C		-	137	-	nC

THERMAL - MECHANICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C	
Thermal resistance, junction to case	R <sub>thJC</sub>		-	1.02	2.0		
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.2	-		
Weight			-	2.0	-	g	
vveignt			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style TO-220AC		15E	TH03	•	



# Ultrafast Rectifier, 15 A FRED Pt® Vishay Semiconductors

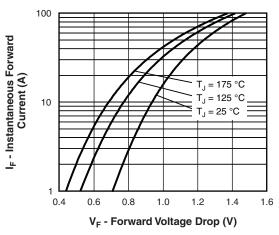


Fig. 1 - Typical Forward Voltage Drop Characteristics

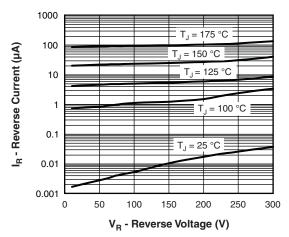


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

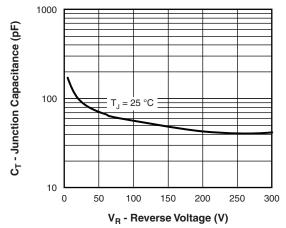


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

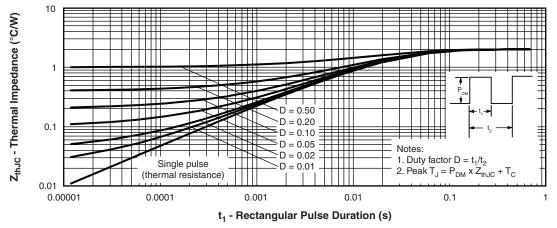


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

#### Vishay Semiconductors Ultrafast Rectifier, 15 A FRED Pt®



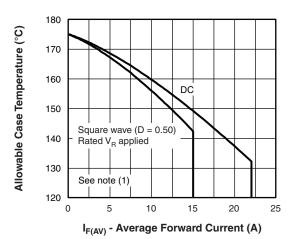


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

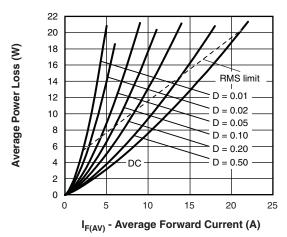


Fig. 6 - Forward Power Loss Characteristics

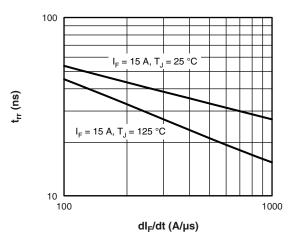


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

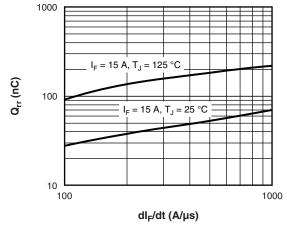


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

### Note

 $\begin{array}{ll} \mbox{(1)} & \mbox{Formula used: } T_C = T_J - (\mbox{Pd} + \mbox{Pd}_{REV}) \times R_{thJC}; \\ \mbox{Pd} = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6);} \\ \end{array}$  $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = Rated  $V_R$ 



#### Ultrafast Rectifier, 15 A FRED Pt® Vishay Semiconductors

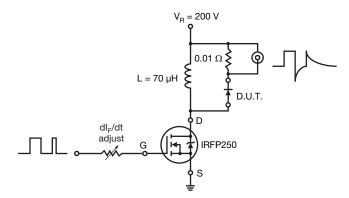
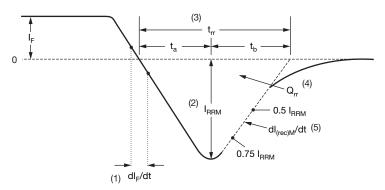


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$ extrapolated to zero current.
- (4)  $Q_{rr}$  area under curve defined by  $t_{rr}$ and I<sub>RRM</sub>

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during tb portion of tr

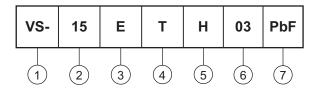
Fig. 10 - Reverse Recovery Waveform and Definitions

# Vishay Semiconductors Ultrafast Rectifier, 15 A FRED Pt®



## **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

2 - Current rating (15 = 15 A)

3 - E = Single diode

4 - Package:

T = TO-220

5 - H = Hyperfast recovery

6 - Voltage rating (03 = 300 V)

7 - PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

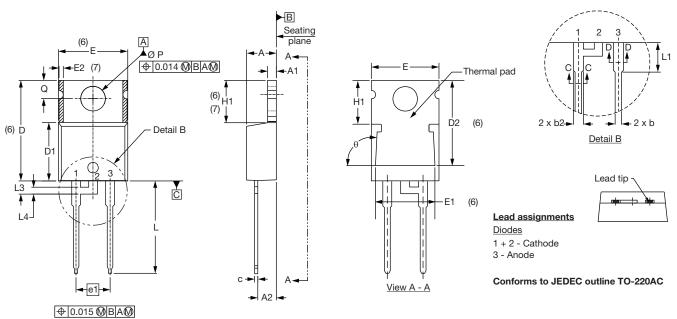
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95221			
Part marking information	www.vishay.com/doc?95224			



## Vishay Semiconductors

## **TO-220AC**

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	IETERS	INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIM	IETERS	INCHES		NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° t	o 93°	90° to 93°		

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline





Vishay

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