

2.5V Drive Nch MOSFET

RTF016N05

Structure

Silicon N-channel MOSFET

● Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TUMT3).

Application

Switching

Packaging specifications

	Package	Taping	
Type	Code	TL	
	Basic ordering unit (pieces)	3000	
RTF016N0	0		

◆ Absolute maximum ratings (Ta = 25°C)

Param	Symbol	Limits	Unit	
Drain-source voltage		V_{DSS}	45	V
Gate-source voltage		V_{GSS}	±12	V
Drain current	Continuous	I _D	±1.6	Α
	Pulsed	I _{DP} *1	±6.4	Α
Source current (Body Diode)	Continuous	I _S	0.6	Α
	Pulsed	I _{SP} *1	6.4	Α
Power dissipation		P _D *2	0.8	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

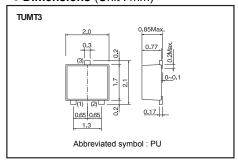
^{*1} Pw≤10µs, Duty cycle≤1%

• Thermal resistance

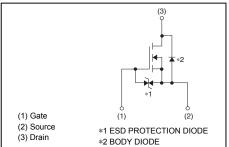
Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	156	°C/W

^{*}Mounted on a ceramic board.

Dimensions (Unit : mm)



• Inner circuit



^{*2} Mounted on a ceramic board.

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	-	-	±10	μA	V_{GS} =±12V, V_{DS} =0V
Drain-source breakdown voltage	$V_{(BR)DSS}$	45	-	-	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	1	1	1	μA	V_{DS} =45V, V_{GS} =0V
Gate threshold voltage	V _{GS (th)}	0.5	1	1.5	٧	V _{DS} =10V, I _D =1mA
Statio duoin accurac an atata		1	140	190		I _D =1.6A, V _{GS} =4.5V
Static drain-source on-state resistance	R _{DS (on)}	1	150	210	mΩ	I _D =1.6A, V _{GS} =4.0V
		ı	200	280		I _D =1.6A, V _{GS} =2.5V
Forward transfer admittance	I Y _{fs} I*	1.5	1	-	S	I _D =1.6A, V _{DS} =10V
Input capacitance	C _{iss}	1	150	-	pF	V _{DS} =10V
Output capacitance	C _{oss}	ı	40	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	1	15	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	ı	8	-	ns	I _D =0.8A, V _{DD} ≒ 25V
Rise time	t _r *	1	14	ı	ns	V _{GS} =4.5V
Turn-off delay time	t _{d(off)} *	-	16	-	ns	R_L =31.3 Ω
Fall time	t _f *	-	10	-	ns	R_G =10 Ω
Total gate charge	Q _g *	-	2.3	-	nC	I _D =1.6A, V _{DD} ≒25V
Gate-source charge	Q _{gs} *	-	0.8		nC	V _{GS} =4.5V
Gate-drain charge	Q _{gd} *	-	0.5	-	nC	

^{*}Pulsed

●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	1.2	V	I _s =1.6A, V _{GS} =0V

^{*}Pulsed

●Electrical characteristic curves (Ta=25°C)

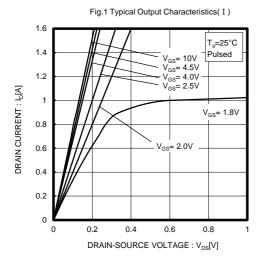


Fig.3 Typical Transfer Characteristics

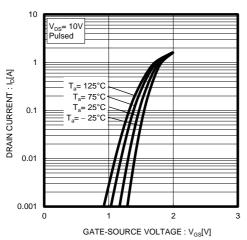


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(II)

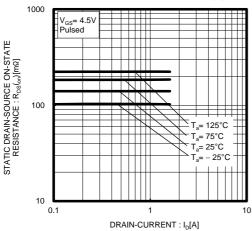


Fig.2 Typical Output Characteristics(II)

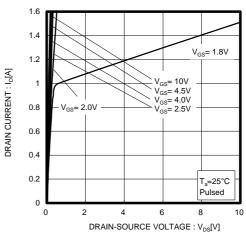


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(I)

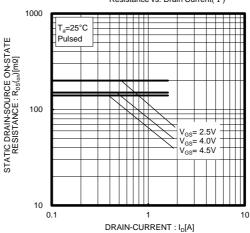
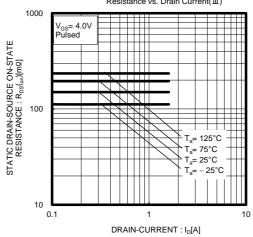
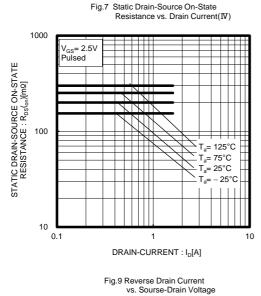
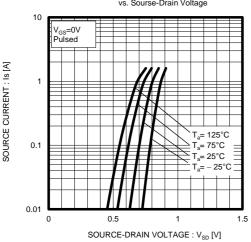
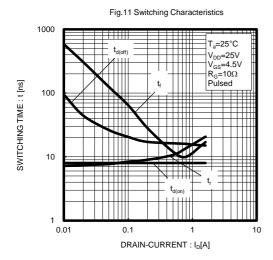


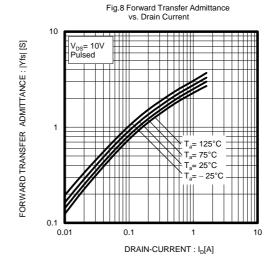
Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(Ⅲ)

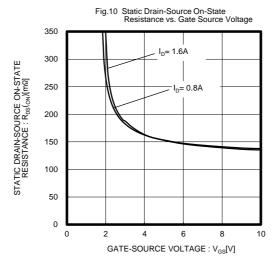


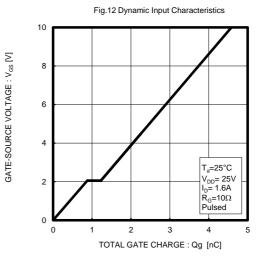












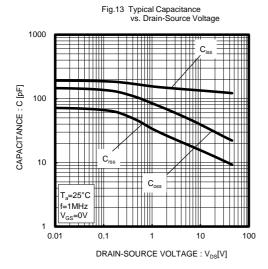
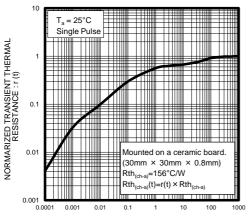
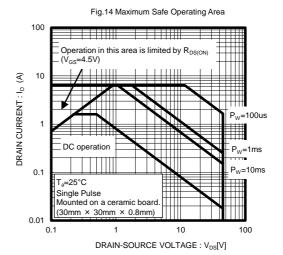


Fig.15 Normalized Transient Thermal Resistance vs. Pulse Width



PULSE WIDTH : Pw(s)



Measurement circuits

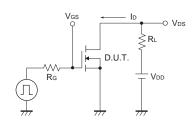


Fig.1-1 Switching Time Measurement Circuit

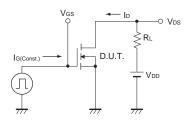


Fig.2-1 Gate Charge Measurement Circuit

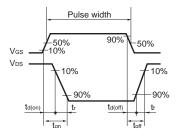


Fig.1-2 Switching Waveforms

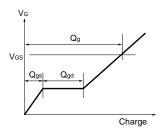


Fig.2-2 Gate Charge Waveform

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