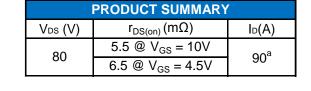
N-Channel 80-V (D-S) MOSFET

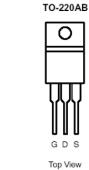
Key Features:

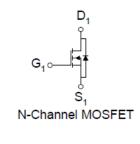
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)							
Parameter		Symbol	Limit	Units			
Drain-Source Voltage			80	V			
Gate-Source Voltage	V _{GS}	±20	V				
Continuous Drain Current ^a	T _A =25°C	I _D	90 ^a				
Pulsed Drain Current ^b		I _{DM}	350	А			
Continuous Source Current (Diode Conduction) ^a		ا _s	120				
Power Dissipation ^a	T _A =25°C	PD	300	W			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C			

ROHS COMPLIANT HALOGEN FREE

THERMAL RESISTANCE RATINGS						
Parameter			Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	R _{eja}	62.5	°C/W		
	Steady State	νθJA	0.5	C/ W		

Notes

a. Package Limited

b. Pulse width limited by maximum junction temperature

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 64 \text{ V}, V_{GS} = 0 \text{ V}$	1		1	uA	
	IDSS	$V_{DS} = 64 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	uA	
On-State Drain Current	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	45			А	
Drain-Source On-Resistance	r.	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 45 \text{ A}$			5.5	mΩ	
	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 44 \text{ A}$			6.5	11152	
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 45 \text{ A}$		22		S	
Diode Forward Voltage	V_{SD}	$I_{S} = 60 \text{ A}, V_{GS} = 0 \text{ V}$		0.9		V	
	Dynamic						
Total Gate Charge	Q_{g}			112		nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 40 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		23			
Gate-Drain Charge	Q_gd			76			
Turn-On Delay Time	t _{d(on)}			25			
Rise Time	t _r	V_{DS} = 40 V, R_L = 2 Ω , I_D = 20 A,		56		ns	
Turn-Off Delay Time	t _{d(off)}	V_{GEN} = 10 V, R_{GEN} = 6 Ω		360			
Fall Time	t _f			122			
Input Capacitance	C _{iss}			10609		pF	
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		989			
Reverse Transfer Capacitance	C _{rss}			936			

Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

3

0.6

0.8

10

1

F = 1MHz

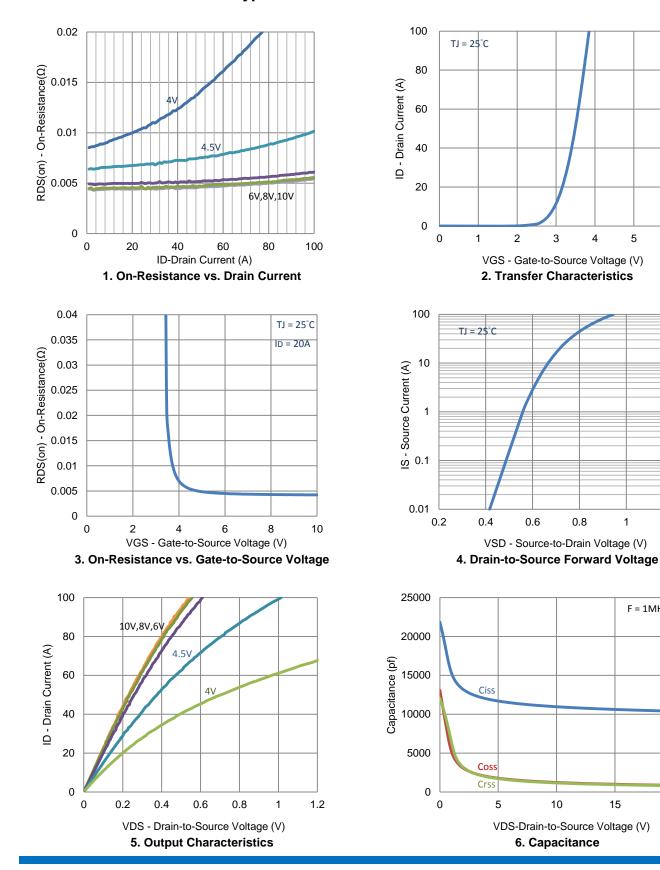
1.2

20

4

5

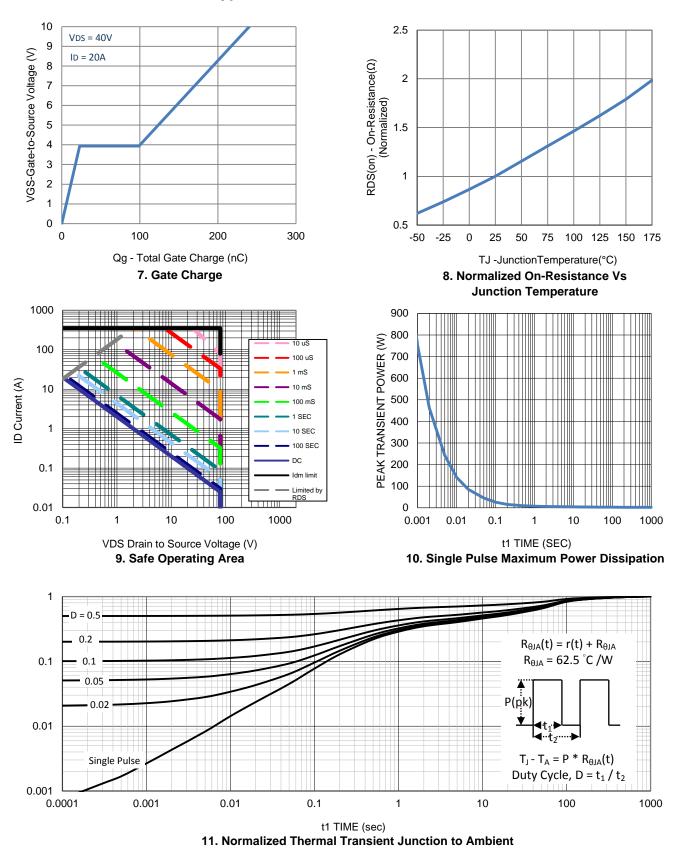
6



Typical Electrical Characteristics

Publication Order Number: DS_AM90N08-05P_1A

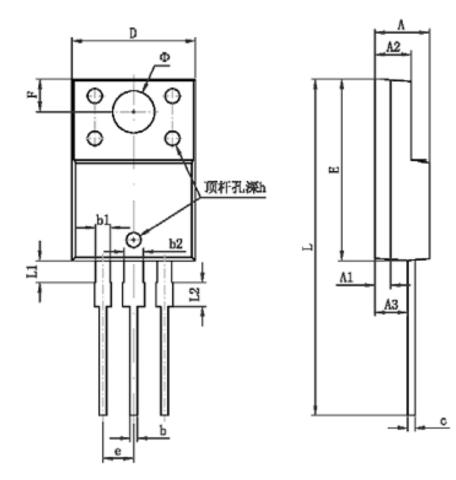
15



Typical Electrical Characteristics

© Preliminary

Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
A	4.300	4.700	0.169	0.185	
A1	1.300 REF		0.051	REF	
A2	2.800	3.200	0.110	0.126	
A3	2.500	2.900	0.098	0.114	
b	0.500	0.750	0.020	0.030	
b1	1.100	1.350	0.043	0.053	
b2	1.500	1.750	0.059	0.069	
с	0.500	0.750	0.020	0.030	
D	9.960	10.360	0.392	0.408	
E	14.800	15.200	0.583	0.598	
e	2.540 TYP		0.100) TYP	
F	2.700 REF		0.106 REF		
Φ	3.500	00 REF 0.138 REF		3 REF	
h	0.000	0.300	0.000	0.012	
L	28.000	28.400	1.102	1.118	
L1	1.700	1.900	0.067	0.075	
L2	1.900	2.100	0.075	0.083	