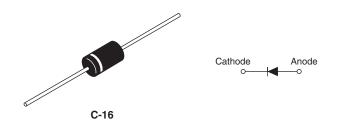


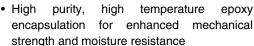
## Schottky Rectifier, 3.0 A

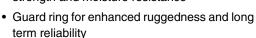


PRODUCT SUMMARY				
Package	DO-201AD (C-16)			
I <sub>F(AV)</sub>	3 A			
$V_R$	20 V			
V <sub>F</sub> at I <sub>F</sub>	See Electrical table			
I <sub>RM</sub> max.	20 mA at 100 °C			
T <sub>J</sub> max.	150 °C			
Diode variation	Single die			
E <sub>AS</sub>	See Electrical table			

#### **FEATURES**

- · Low profile, axial leaded outline
- · High frequency operation
- · Very low forward voltage drop





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for commercial level
- Halogen-free according to IEC 61249-2-21 definition (-M3 only)



### **DESCRIPTION**

The VS-1N5820... axial leaded Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	3.0	Α		
V <sub>RRM</sub>		20	V		
I <sub>FSM</sub>	$t_p = 5 \mu s \text{ sine}$	450	Α		
V <sub>F</sub>	3 Apk, T <sub>J</sub> = 25 °C	0.475	V		
TJ	Range	- 65 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-1N5820	VS-1N5820-M3	UNITS
Maximum DC reverse voltage	$V_{R}$	20	20	V
Maximum working peak reverse voltage	$V_{RWM}$	20	20	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS VALUES		UNITS	
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 114 °C, rectangular waveform With cooling fins		3.0	
Maximum peak one cycle non-repetitive surge current at T <sub>J</sub> = 25 °C		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	450	Α
		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	90	



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop V <sub>FM</sub> <sup>(1)</sup>	V <sub>FM</sub> <sup>(1)</sup>	3 A	- T <sub>J</sub> = 25 °C	0.41	0.475	· v
waximum lorward voltage drop	VFM (*)	9.4 A		0.49	0.85	
Marian was accounted to the comment	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	0.05	2.0	mA
Maximum reverse leakage current I <sub>RM</sub> (1)		T <sub>J</sub> = 100 °C	VR = nateu VR	8.1	20	IIIA
Typical junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		350	-	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		9.0	-	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> -		10 000	V/µs	

#### Note

 $^{(1)}\,$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 65 to 150	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub>	With fin 20 x 20 (0.79 x 0.79) 1.0 thick	34	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation Without cooling fin	80	C/VV
Approximate weight			1.2	g
Approximate weight			0.042	OZ.
Marking device		Case style C-16	1N5	820

### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

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## Vishay Semiconductors

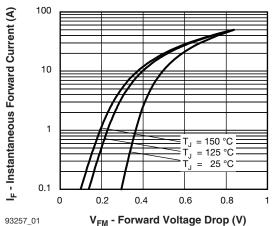


Fig. 1 - Maximum Forward Voltage Drop Characteristics

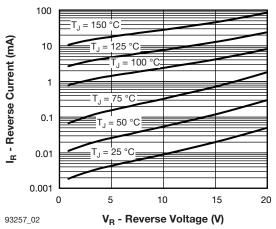


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

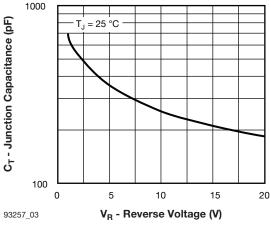
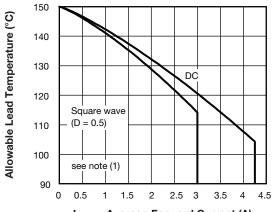


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



 $_{93257\_04}$   $I_{F(AV)}$  - Average Forward Current (A)

Fig. 4 - Typical Average Forward Current vs. Allowable Lead Temperature

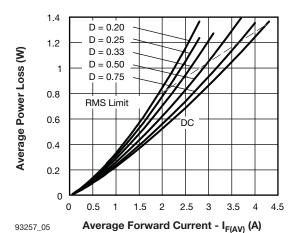
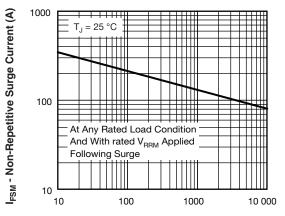


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



93257\_06  $t_p$  - Square Wave Pulse Duration ( $\mu$ s)

Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

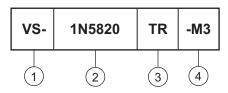
#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ; Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse$  power loss =  $V_{R1} \times I_R$  (1 - D)



### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

- Part number: 3 A, 20 V

TR = Tape and reel package

None = Bulk package

4 - Environmental digit

• None = Lead (Pb)-free and RoHS compliant

• -M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

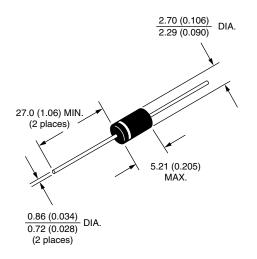
ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-1N5820	500	500	Bulk	
VS-1N5820TR	1200	1200	Tape and reel	
VS-1N5820-M3	500	500	Bulk	
VS-1N5820TR-M3	1200	1200	Tape and reel	

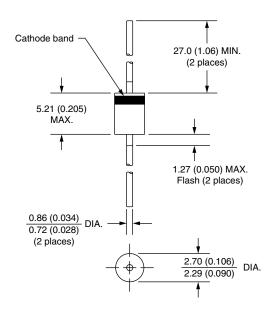
LINKS TO RELATED DOCUMENTS			
Dimensions www.vishay.com/doc?95242			
Part marking information <u>www.vishay.com/doc?95304</u>			
Packaging information	www.vishay.com/doc?95338		



# **Axial DO-204AL (DO-41)**

### **DIMENSIONS** in millimeters (inches)







## **Legal Disclaimer Notice**

Vishay

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