2SC5295J

Silicon NPN epitaxial planar type

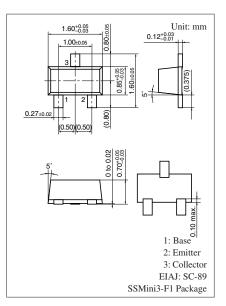
For 2 GHz band low-noise amplification

Features

- High transition frequency f_T
- \bullet Low collector output capacitance (Common base, input open circuited) C_{ob}
- SS-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	15	V	
Collector-emitter voltage (Base open)	V _{CEO}	10	V	
Emitter-base voltage (Collector open)	V _{EBO}	2	V	
Collector current	I _C	65	mA	
Collector power dissipation	P _C	125	mW	
Junction temperature	Tj	125	°C	
Storage temperature	T _{stg}	-55 to +125	°C	



Marking Symbol: 3S

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

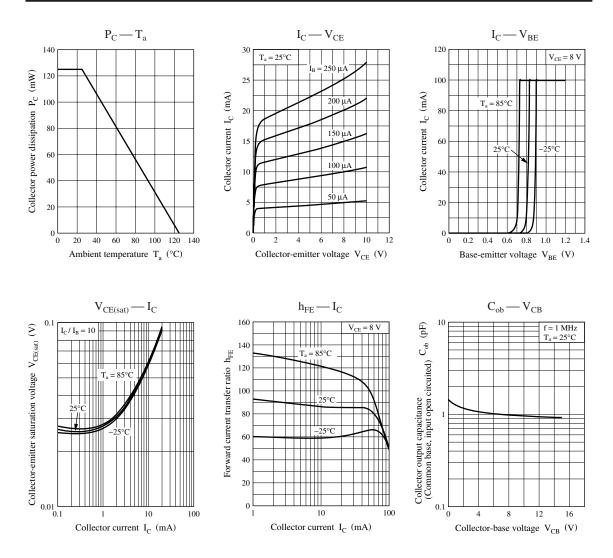
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 10 V, I_E = 0$			1	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{EB} = 1 V, I_C = 0$			1	μΑ
Forward current transfer ratio *	h _{FE}	$V_{CE} = 8 V, I_C = 20 mA$	50		170	
Transition frequency	f _T	$V_{CE} = 8 \text{ V}, I_{C} = 15 \text{ mA}, f = 1.5 \text{ GHz}$	7.0	8.5		GHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		0.6	1.0	pF
Foward transfer gain	$ S_{21e} ^2$	$V_{CE} = 8 \text{ V}, I_C = 15 \text{ mA}, f = 1.5 \text{ GHz}$	7	9		dB
Maximum unilateral power gain	G _{UM}	$V_{CE} = 8 \text{ V}, I_C = 15 \text{ mA}, f = 1.5 \text{ GHz}$		10		dB
Noise figure	NF	$V_{CE} = 8 V, I_C = 7 mA, f = 1.5 GHz$		2.2	3.0	dB

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. *: Rank classification

Rank	Q	R
$h_{\rm FE}$	50 to 120	100 to 170

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