

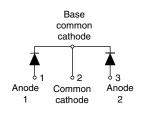
Vishay High Power Products

Schottky Rectifier

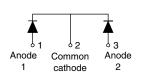
New Generation 3 D-61 Package, 2 x 40 A

80CNQ...A

D-61-8





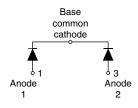


80CNQ...ASL



D-61-8-SM

D-61-8-SL



PRODUCT SUMMARY				
I _{F(AV)}	2 x 40 A			
V_{R}	35 to 45 V			

FEATURES

- 150 °C T_J operation
- · Center tap module
- · Very low forward voltage drop
- · High frequency operation
- · High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- · New fully transfer-mold low profile, small footprint, high current package
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	80	A		
V _{RRM}	Range	35 to 45	V		
I _{FSM}	t _p = 5 μs sine	5800	A		
V _F	40 Apk, T _J = 125 °C (per leg)	0.47	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	80CNQ035A	80CNQ040A	80CNQ045A	UNITS
Maximum DC reverse voltage	V_R	35	40	45	V
Maximum working peak reverse voltage	V_{RWM}	35			V

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	L TEST CONDITIONS VA		VALUES	UNITS
Maximum average forward current per leg		50 % duty cycle at T _C = 114 °C, rectangular waveform		40	
See fig. 5 per device			, rectangular wavelonn	80	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	5800	A
non-repetitive surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	750	
Non-repetitive avalanche energy per leg E_{AS} $T_{J} = 25 ^{\circ}\text{C}$, $I_{AS} = 8 \text{A}$, $L = 1.7 \text{mH}$		nΗ	54	mJ	
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum V_A = 1.5 x V_R typical		8	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	40 A	T _J = 25 °C	0.52	V
		80 A		0.66	
		40 A	T _J = 125 °C	0.47	
		80 A		0.61	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	5	mA
See fig. 2		T _J = 125 °C		250	
Threshold voltage	V _{F(TO)}	$T_{J} = T_{J}$ maximum		0.26	V
Forward slope resistance	r _t			3.93	mΩ
Maximum junction capacitance per leg	Ст	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C 26		2600	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.5		nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000			V/µs

Note

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 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %





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PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T_J , T_{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	0.85		
Maximum thermal resistance, junction to case per package		R_{thJC}	DC operation	0.42	°C/W	
Typical thermal resistance, case to heatsink (D-61-8 only)		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
A managina ata wai alat				7.8	g	
Approximate weight				0.28	oz.	
Mounting torque	minimum			40 (35)	kgf · cm	
(D-61-8 only) m	maximum			58 (50)	(lbf · in)	
				80CNQ035A		
			Case style D-61-8	80CNQ040A		
				80CNQ045A		
			800		CNQ035ASM	
Marking device			Case style D-61-8-SM	80CNQ040ASM		
		80CNQ045ASM				
				80CNQ035ASL		
			Case style D-61-8-SL	80CNQ040ASL		
				80CNQ	045ASL	

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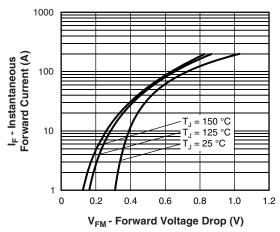


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

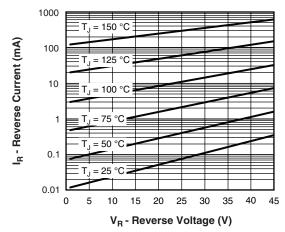


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

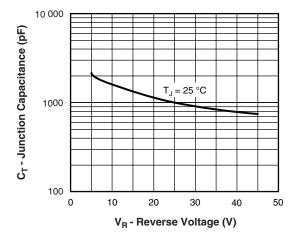


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

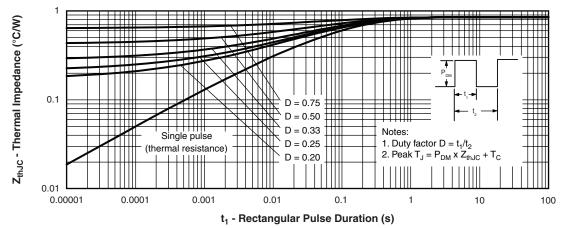


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



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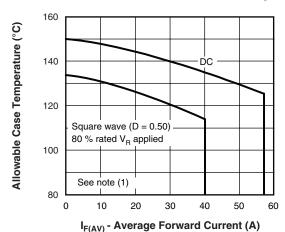


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

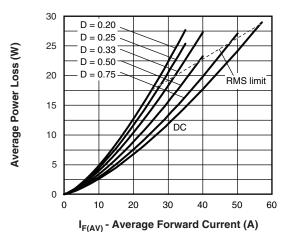


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

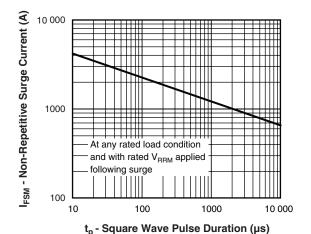


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

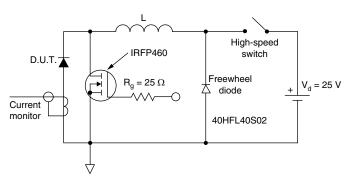


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

80CNQ...A Series

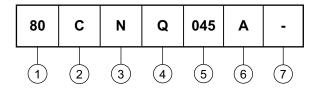
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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (80 A)
- 2 Circuit configuration:
 - C = Common cathode
- Package:
 - N = D-61
- 4 Schottky "Q" series

035 = 35 V

5 - Voltage ratings

040 = 40 V 045 = 45 V

- **6** Package style:
 - A = D-61-8
 - ASM = D-61-8-SM
 - ASL = D-61-8-SL
- 7 • None = Standard production
 - PbF = Lead (Pb)-free (D-61-8 only)

Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95354			
Part marking information	http://www.vishay.com/doc?95356			

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