TOSHIBA Intelligent Power Device Silicon Monolithic Power MOS Integrated Circuit

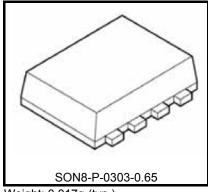
TPD7211F

Power MOSFET Gate Driver for half-bridge

TPD7211F is a Power MOSFET gate driver for half-bridge circuit. BiCD process is applied on this product.

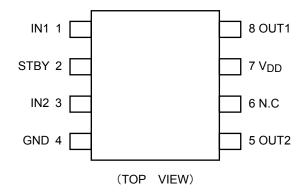
Features

- Power MOSFET gate driver for half-bridge
- High-side can operate P channel MOSFET, Low-side can operate N channel MOSFET
- Housed in the PS-8 package and supplied in embossed carrier tape.

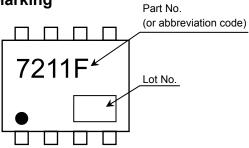


Weight: 0.017g (typ.)

Pin Assignment (top view)

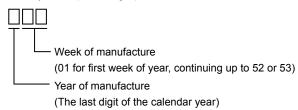


Marking



on the lower left of the marking indicates Pin 1

*Weekly code: (Three digits)

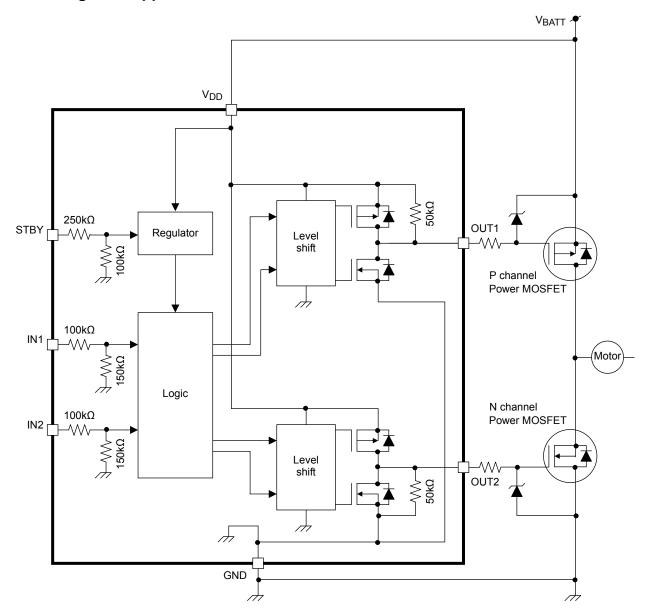


Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain

This product has a MOS structure and is sensitive electrostatic discharge.

Block Diagram / Application Circuit



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Pin Description

Pin No.	Symbol	Pin Description					
1	IN1	Input pin for high-side output (OUT1) control. The IN1 pin has an internal pull-down resistor. Thus, even if the input is open-circuit, the OUT1 never turns on ("L") inadvertently.					
2	STBY	Standby pin:By driving this pin "L", supply current is $10\mu\text{A}$ or less and all outputs can be turned off regardless of input signals. By driving this pin "H", all outputs are switching normally. The STBY pin has an internal pull-down resistor. When input is open circuit, this IC becomes the same operation as "L".					
3	IN2	Input pin for low-side output (OUT2) control. The IN2 pin has an internal pull-down resistor. Thus, even if the input is open-circuit, the OUT2 never turns on ("H") inadvertently.					
4	GND	Ground pin.					
5	OUT2	Drives the low-side N channel power MOSFET.					
6	N.C	No-Connect pin.					
7	V _{DD}	Power supply pin.					
8	OUT1	Drives the high-side P channel power MOSFET.					

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Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Pin	Rating	Unit	Remarks	
Power supply voltage	V _{DD}	V _{DD}	-0.3 to 35	V	When V_{DD} range is 30V or more, Pulse width ≤ 0.3 s	
	V _{IN}	IN1, IN2	-0.3 to 6	V	-	
Input voltage	V _{STBY}	STBY	-0.3 to 35	V	When V_{DD} range is 30V or more, Pulse width $\leq 0.3s$	
Output voltage	V _{OUT}	OUT1, OUT2	-0.3 to V _{DD} +0.3	V	Absolute Maximum Ratings is 35V or less. When V_{DD} range is 30V or more, Pulse width \leq 0.3s	
Output current	Гоит	OUT1, OUT2	±500	mA	-	
Dower dissination(Note 2)	P _{D(1)}	-	0.7	W	Refer to Note 2a	
Power dissipation(Note 2)	P _{D(2)}	-	0.35	W	Refer to Note 2b	
Operating temperature	T _{opr}	-	-40 to 125	°C	-	
Junction temperature	Tj	-	150	°C	-	
Storage temperature	T _{stg}	-	-40 to 150	°C	-	

Note1: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

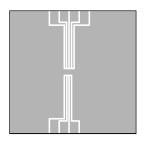
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Resistance

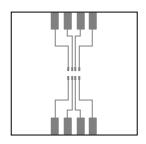
Characteristic	Symbol	Rating	Unit	
Junction to ambient thermal resistance	Pu c	178.6 (Note 2a)	°C / W	
Junction to ambient thermal resistance	R _{th (j−a)}	357.2 (Note 2b)		

Note 2:

(a)Mounted on glass epoxy board



Glass epoxy board Material : FR-4 25.4mm×25.4mm×0.8mm (b) Mounted on glass epoxy board



Glass epoxy board Material : FR-4 25.4mm×25.4mm×0.8mm

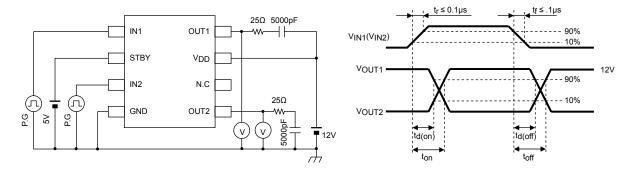
Electrical Characteristics (Unless otherwise specified, T_j = - 40 to 125 °C, V_{DD} = 5 to 18 V, V_{STBY} = 5 V)

Characteristics	Symbol	Pin	Condition	Min	Тур.	Max	Unit	
Operating supply voltage	V _{DD(opr)}	V_{DD}	-	5	12	18	V	
Supply current	I _{DD1}	V _{DD}	V _{STBY} =0V, V _{DD} =12V, Output pin is open.	-	-	10	μA	
Зирргу сине т	I _{DD2}	V _{DD}	V _{STBY} =5V, V _{DD} =12V, V _{IN1,2} =0V, Output pin is open.	-	-	3	mA	
High lovel input voltage	V _{IH1}	IN1,IN2		3.5	-	-	V	
High level input voltage	V _{IH2}	STBY		3.5	-	-	V	
Low lovel input veltage	V _{IL1}	IN1,IN2	-	-	-	1.5	V	
Low level input voltage	V _{IL2}	STBY		-	-	0.8	V	
High level input current	l _{IH1}	IN1,IN2	V _{IN1,2} =5V, per one input.	-	20	50	μΑ	
nigir level lilput current	I _{IH2}	STBY	V _{STBY} =5V	-	15	50	μΑ	
Low lovel input ourrent	I _{IL1}	IN1,IN2	V _{IN1,2} =0V, per one input.	-0.2	-	+0.2	μΑ	
Low level input current	I _{IL2}	STBY	V _{STBY} =0V	-0.2	-	+0.2	μΑ	
High-side(OUT1) high-level output voltage	V _{O1H}	OUT1	V _{IN1} =0V, I ₀ =-10mA	V _{DD} -0.2	-	-	٧	
High-side(OUT1) low-level output voltage	V _{O1L}	OUT1	V _{IN1} =5V, I _o =+10mA	-	-	0.2	٧	
Low-side(OUT2) high-level output voltage	V _{O2H}	OUT2	V _{IN2} =5V, I _o =-10mA	V _{DD} -0.2	-	-	٧	
Low-side(OUT2) low-level output voltage	V _{O2L}	OUT2	V _{IN2} =0V, I _o =+10mA	-	-	0.2	٧	
Output ON Pagistance	RDS(ON)[SOURCE]	OUT1, OUT2	T _j =25°C, I ₀ =-250mA	-	4	8	- Ω	
Output ON Resistance	R _{DS(ON)[SINK]}	OUT1, OUT2	T _j =25°C, I ₀ =+250mA	-	3	6		
	^t d(on)1	OUT1		-	0.25	1		
	t _{ON1}			-	0.5	2	μs	
	t _{d(off)1}	0011		-	0.25	1		
Switching times	t _{OFF1}		V _{DD} =12V,	-	0.5	2		
Switching times	t _{d(on)2}		$R_0=25\Omega$, $C_0=5000pF$	-	0.25	1		
	t _{ON2}	OUT2		-	0.5	2		
	t _{d(off)2}			-	0.25	1		
	t _{OFF2}			-	0.5	2		
Dood times	t _{dead1}	OUT1, OUT2	$t_{d(off)1}-t_{d(on)2}, t_{d(off)2}-t_{d(on)1}$	-	-	1		
Dead times	t _{dead2}	OUT1, OUT2	^t d(off)1 ^{-t} d(on)1, ^t d(off)2 ^{-t} d(on)2	-	-	1	- µs	

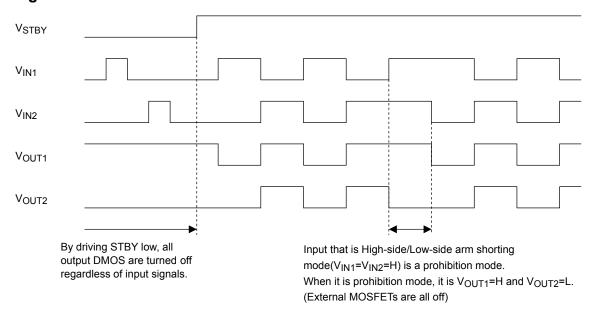
^{*}Please set the deadtime of the input signal after considering the switching time of external power MOSFET.

^{*}The condition of the typical value is $T_j = 25 ^{\circ} \text{C}, \ V_{DD} = 12 \text{V}.$

Switching times test circuit

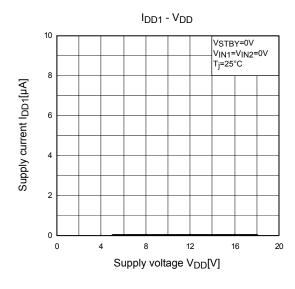


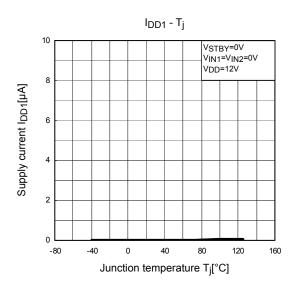
Timing chart

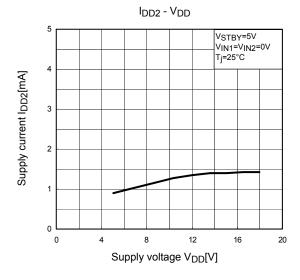


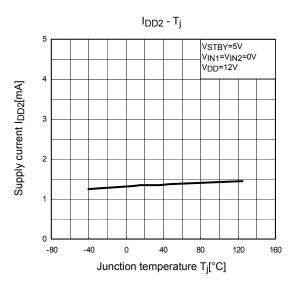
Truth Table

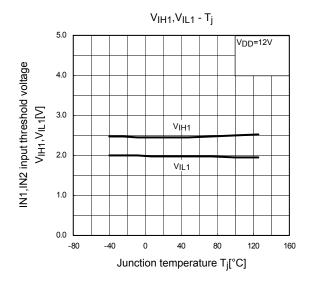
STBY signal	IN1 signal	IN2 signal	V _{OUT1}	V _{OUT2}	Remarks		
L	L	L	Н	L			
L	Н	L	Н	L	Standby mode		
L	L	Н	Н	L	(Output is all off)		
L	Н	Н	Н	L			
Н	L	L	Н	L	OUT1 and OUT2 are off mode. (External MOSFETs are all off mode)		
Н	Н	L	L	L	OUT1 is on mode. (External high side MOSFET is on mode)		
Н	L	Н	Н	Н	OUT2 is on mode. (External low side MOSFET is on mode)		
Н	Н	Н	Н	L	High-side/Low-side arm shorting mode.		

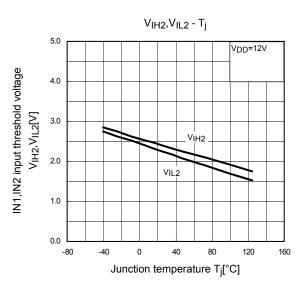


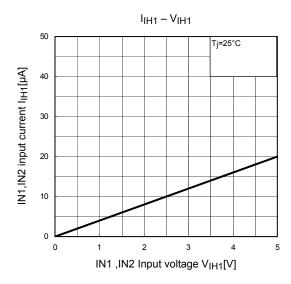


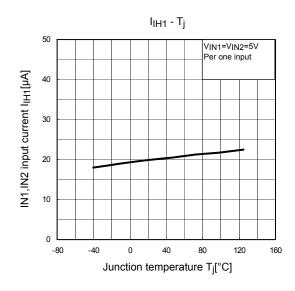


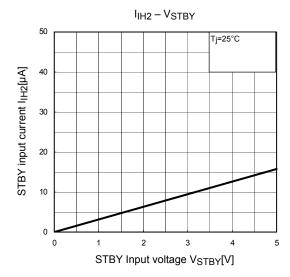


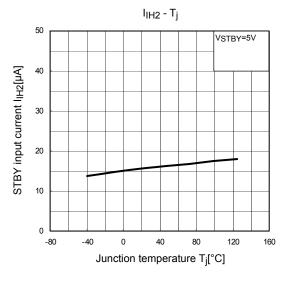


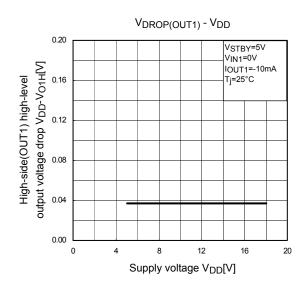


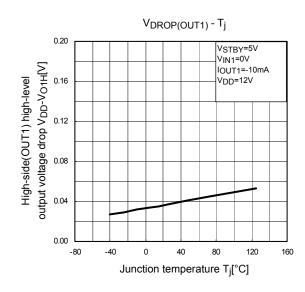




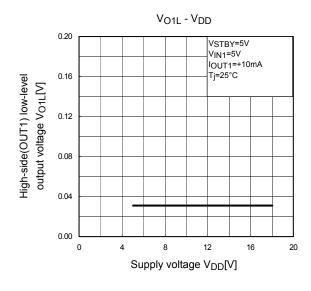


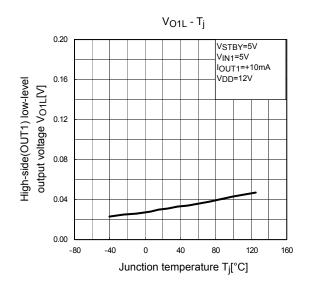


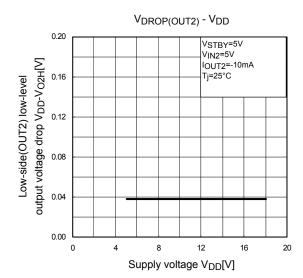


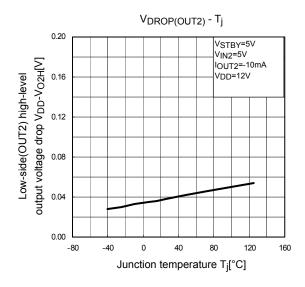


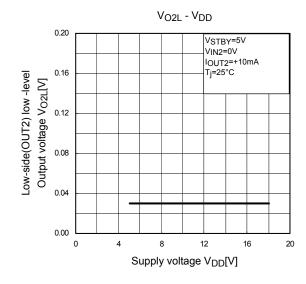
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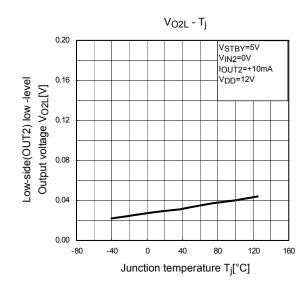




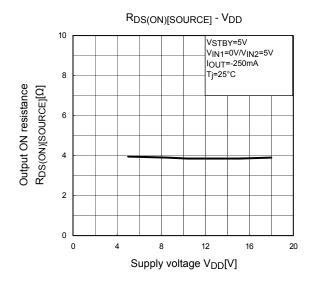


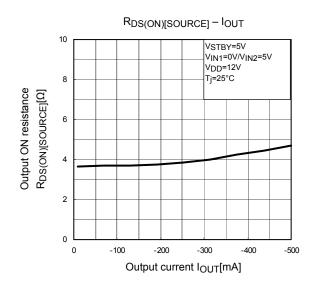


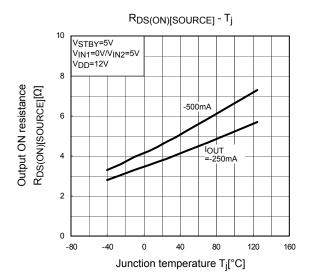


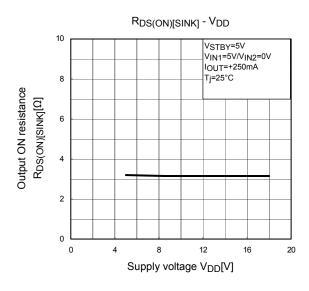


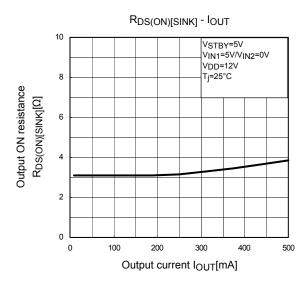
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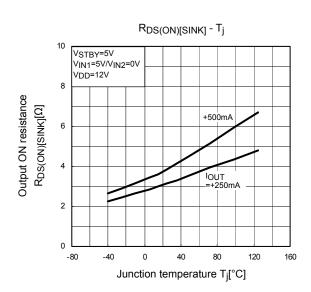


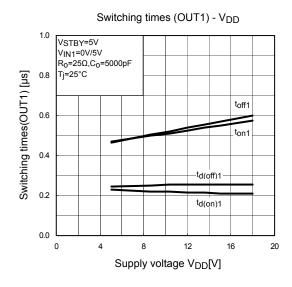


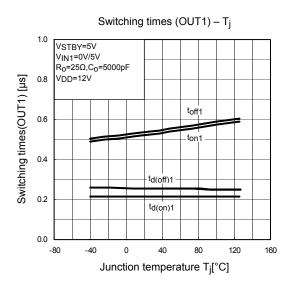


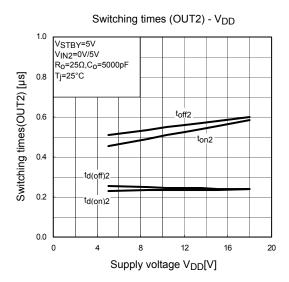


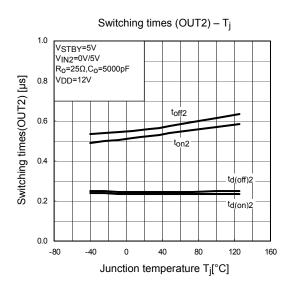


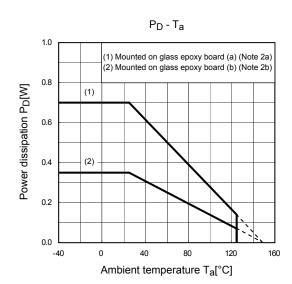






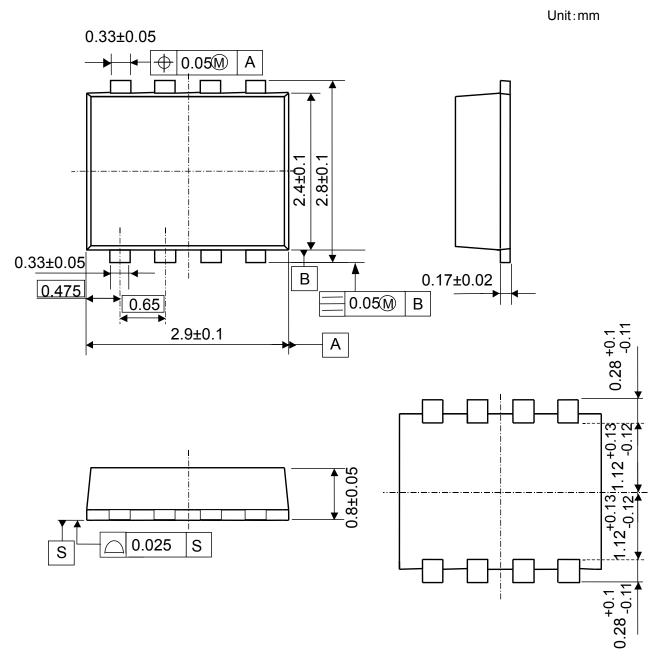






Package Dimensions

SON8-P-0303-0.65



Weight: 0.017g(typ.)

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