Preferred Device

Monolithic Dual Switching Diode Common Anode

MAXIMUM RATINGS (EACH DIODE)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	70	Vdc
Forward Current	IF	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc
Non-Repetitive Peak Forward Current t = 1 μs (Note 3)	I _{FSM}	4	А

THERMAL CHARACTERISTICS

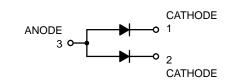
Characteristic	Symbol	Max	Unit	
Total Device Dissipation FR-5 Board (Note 1) T _A = 25°C	P _D	225	mW	
Derate above 25°C		1.8	mW/°C	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W	
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C Derate above 25°C	P _D	300 2.4	mW mW/°C	
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	417	°C/W	
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C	

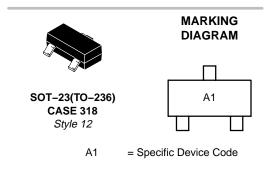
- 1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in.
- 2. Alumina = 0.4 \times 0.3 \times 0.024 in. 99.5% alumina.
- 3. Square Wave; $T_i = 25^{\circ}C$.



ON Semiconductor®

http://onsemi.com





ORDERING INFORMATION

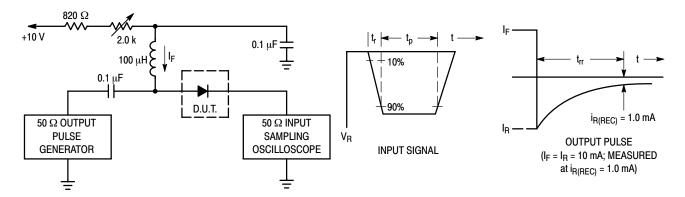
Device	Package	Shipping [†]
BAW56LT1	SOT-23	3000 / Tape & Reel
BAW56LT3	SOT-23	10,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Reverse Breakdown Voltage (I _(BR) = 100 μAdc)	V _(BR)	70	-	Vdc
Reverse Voltage Leakage Current $(V_R = 25 \text{ Vdc}, T_J = 150^{\circ}\text{C})$ $(V_R = 70 \text{ Vdc})$ $(V_R = 70 \text{ Vdc}, T_J = 150^{\circ}\text{C})$	I _R	- - -	30 2.5 50	μAdc
Diode Capacitance (V _R = 0, f = 1.0 MHz)	C _D	-	2.0	pF
Forward Voltage $(I_F = 1.0 \text{ mAdc})$ $(I_F = 10 \text{ mAdc})$ $(I_F = 50 \text{ mAdc})$ $(I_F = 150 \text{ mAdc})$	V _F	- - - -	715 855 1000 1250	mVdc
Reverse Recovery Time (I _F = I _R = 10 mAdc, I _{R(REC)} = 1.0 mAdc) (Figure 1) R_L = 100 Ω	t _{rr}	-	6.0	ns



Notes: 1. A 2.0 $k\Omega$ variable resistor adjusted for a Forward Current (I_F) of 10 mA.

Figure 1. Recovery Time Equivalent Test Circuit

^{2.} Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.

^{3.} $t_p \gg t_{rr}$

Curves Applicable to Each Cathode

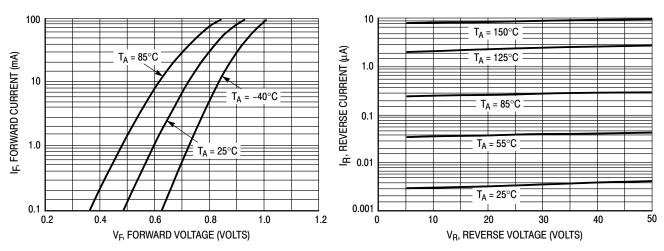


Figure 2. Forward Voltage

Figure 3. Leakage Current

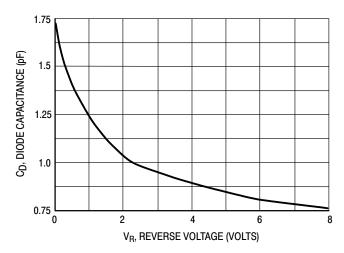


Figure 4. Capacitance

PACKAGE DIMENSIONS

SOT-23-3 (TO-236) CASE 318-08 **ISSUE AH**

R

н₫

NOTES:

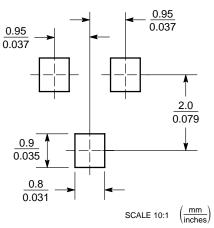
- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE
- 318-03 AND -07 OBSOLETE, NEW STANDARD 318-08.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.1102	0.1197	2.80	3.04
В	0.0472	0.0551	1.20	1.40
С	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
Н	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
٧	0.0177	0.0236	0.45	0.60

STYLE 12:

- CATHODE CATHODE 2.
- ANODE

SOLDERING FOOTPRINT



SOT-23-3

ON Semiconductor and was are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its partnif rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.