

# DATA SHEET

## GENERAL PURPOSE CHIP RESISTORS

RC0402 (Pb Free)  
5%, 1%



SCOPE

This specification describes RC0402 series chip resistors with lead-free terminations made by thick film process.

ORDERING INFORMATION

Part number is identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

**YAGEO ORDERING CODE**

**CTC CODE**

**RC0402 X X X XX XXXX L**  
 (1) (2) (3) (4) (5) (6)

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**(1) TOLERANCE**

F = ±1%  
 J = ±5%

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**(2) PACKAGING TYPE**

R = Paper/PE taping reel

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**(3) TEMPERATURE COEFFICIENT OF RESISTANCE**

– = Base on spec

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**(4) TAPING REEL**

07 = 7 inch dia. Reel  
 10 = 10 inch dia. Reel (not preferred)  
 13 = 13 inch dia. Reel

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**(5) RESISTANCE VALUE**

5R6, 56R, 560R, 56K, 10M.

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**(6) RESISTOR TERMINATIONS**

L = Lead free terminations (pure Tin)

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**ORDERING EXAMPLE**

The ordering code of a RC0402 chip resistor, value 56 Ω with ±1% tolerance, supplied in 7-inch tape reel is: RC0402FR-0756RL.

**NOTE**

1. The “L” at the end of the code is only for ordering. On the reel label, the standard CTC will be mentioned an additional stamp “LFP”= lead free production.
2. Products with lead in terminations fulfil the same requirements as mentioned in this datasheet.
3. Products with lead in terminations will be phased out in the coming months (before July 1st, 2006)

**MARKING**

RC0402



Fig. 1

No marking

**CONSTRUCTION**

The resistors are constructed out of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by a resistive paste. The composition of the paste is adjusted to give the approximate required resistance and laser cutting of this resistive layer that achieves tolerance trims the value. The resistive layer is covered with a protective coat.

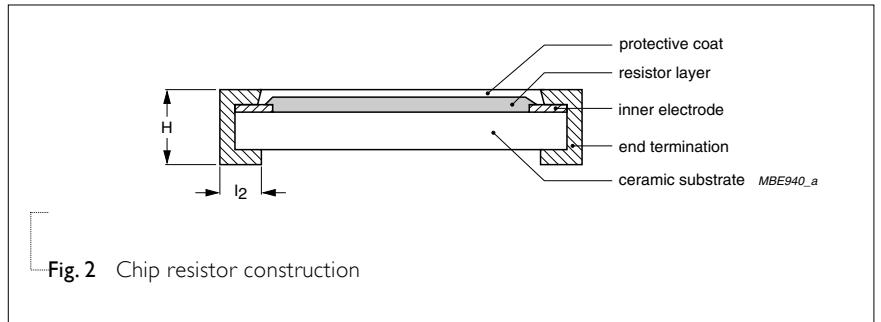


Fig. 2 Chip resistor construction

Finally, the two external terminations (pure Tin) are added. See fig. 2.

**DIMENSIONS**

Table I

TYPE	RC0402
L (mm)	1.00 ±0.05
W (mm)	0.50 ±0.05
H (mm)	0.35 ±0.05
l <sub>1</sub> (mm)	0.20 ±0.10
l <sub>2</sub> (mm)	0.25 ±0.10

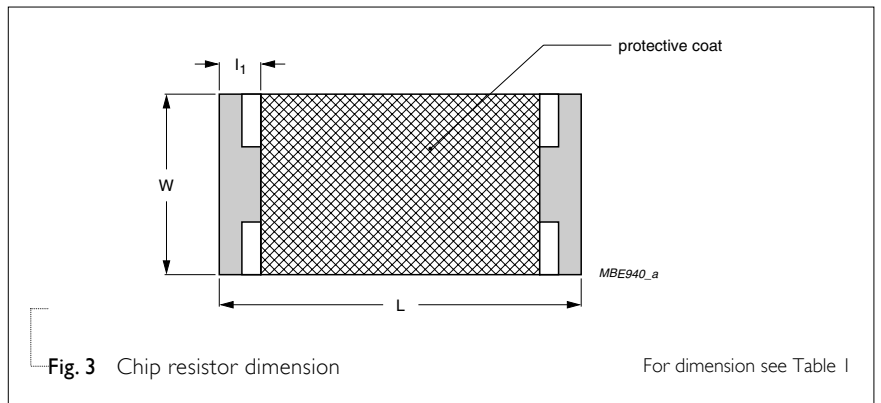


Fig. 3 Chip resistor dimension

For dimension see Table I

ELECTRICAL CHARACTERISTICS

Table 2

CHARACTERISTICS		RC0402 1/16 W
Operating Temperature Range		-55 °C to +155 °C
Maximum Working Voltage		50 V
Maximum Overload Voltage		100 V
Dielectric Withstanding Voltage		100 V
Resistance Range	5% (E24)	1 Ω to 10 MΩ
	1% (E96)	1 Ω to 10 MΩ
	Zero Ohm Jumper	< 0.05 Ω
Temperature Coefficient	10 Ω < R ≤ 10 MΩ	±100 ppm/°C
	1 Ω < R ≤ 10 Ω	±200 ppm/°C
Jumper Criteria	Rated Current	1.0 A
	Maximum Current	2.0 A

FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet “Chip resistors mounting”.

ENVIRONMENTAL DATA

For material declaration information (IMDS-data) of the products, please see the separated info “Environmental data”.

PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

PRODUCT TYPE	PACKING STYLE	REEL DIMENSION	QUANTITY PER REEL
RC0402	Paper / PE Taping Reel (R)	7" (178 mm)	10,000 units
		10" (254 mm) / not preferred	20,000 units
		13" (330 mm)	50,000 units

**NOTE**

1. For Paper/PE tape and reel specification/dimensions, please see the special data sheet “Packing” document.

**FUNCTIONAL DESCRIPTION**

**POWER RATING**

RC0402 rated power at 70°C is 1/16 W

**RATED VOLTAGE**

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

$$V = \sqrt{P \times R}$$

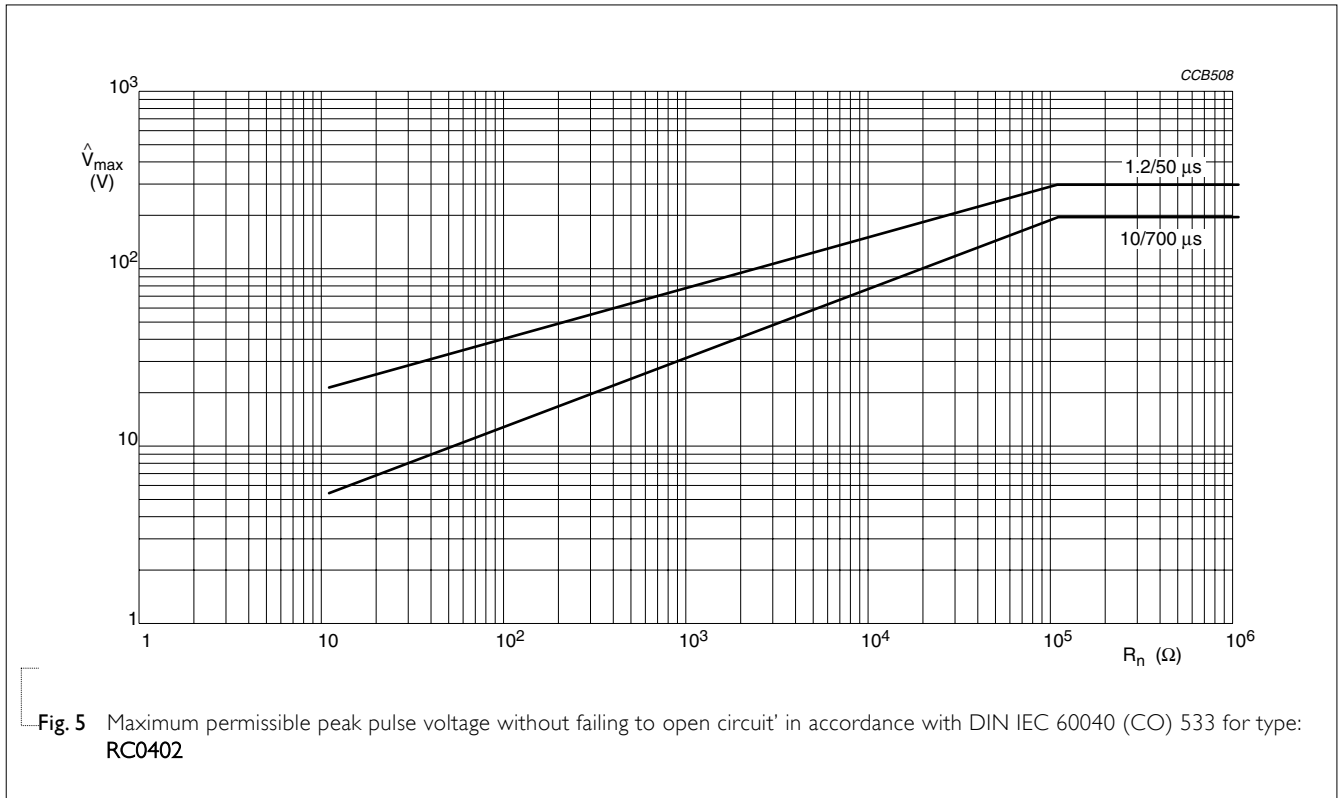
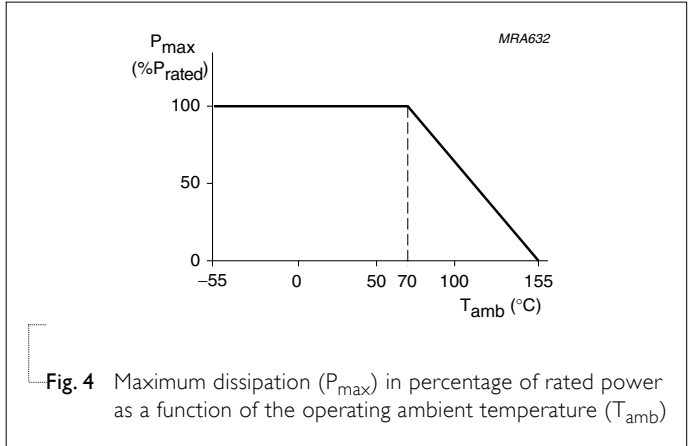
Where

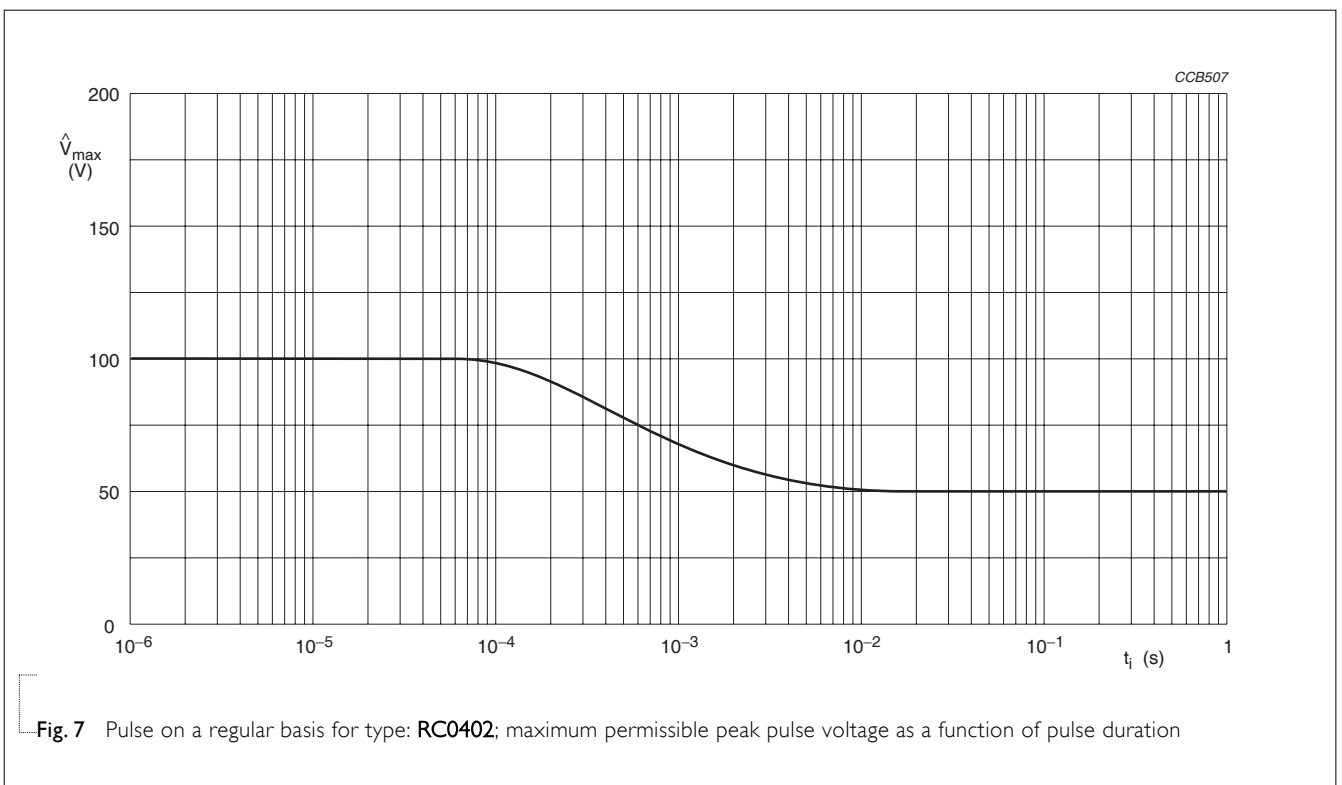
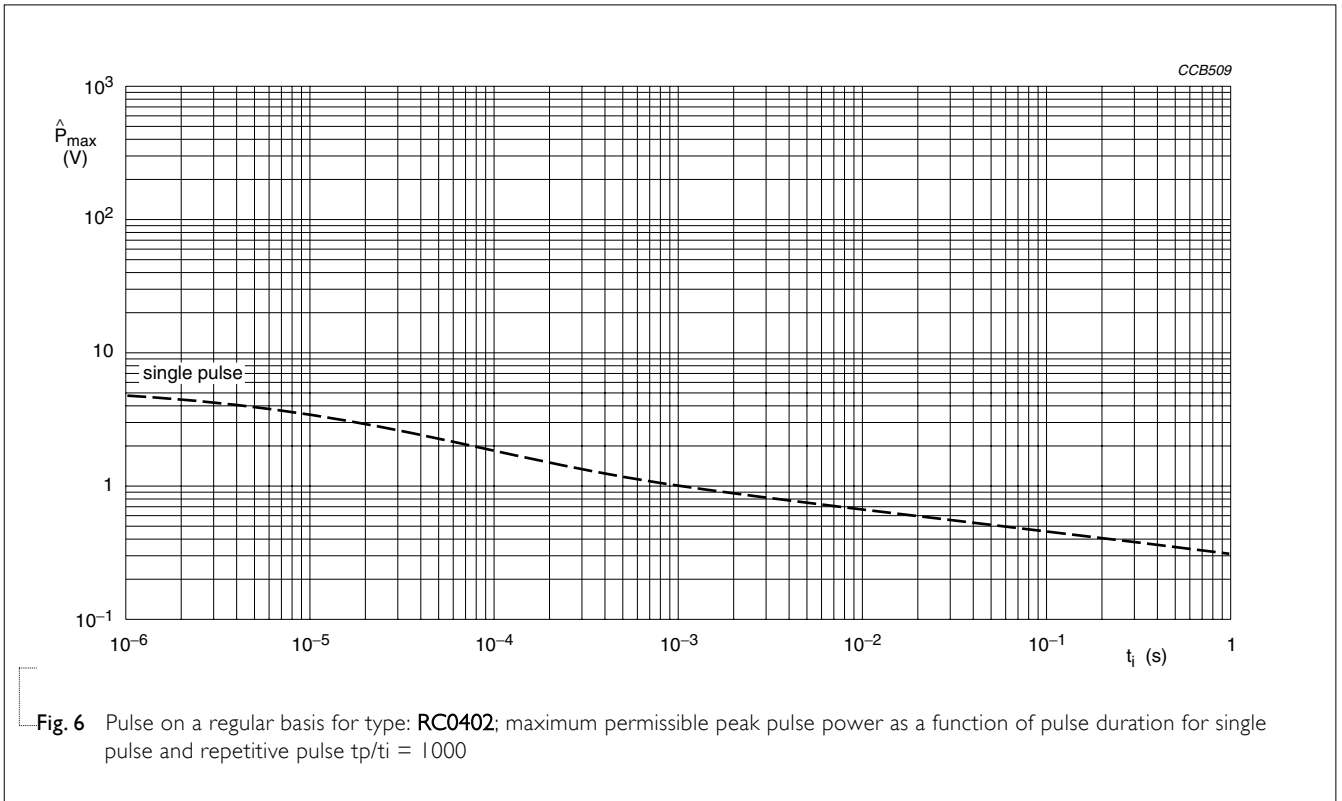
V=Continuous rated DC or AC (rms) working voltage (V)

P=Rated power (W)

R=Resistance value ( $\Omega$ )

**PULSE LOADING CAPABILITIES**





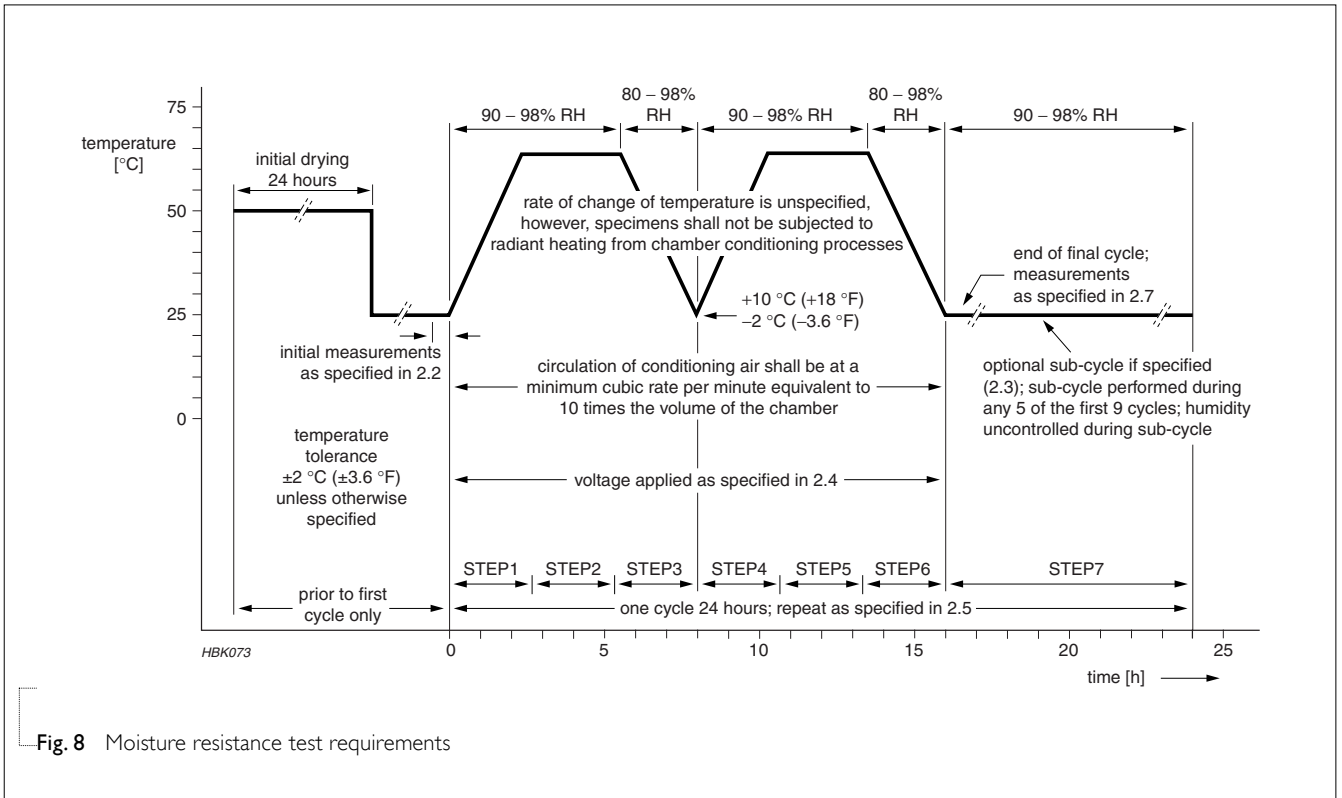
**TESTS AND REQUIREMENTS**

Table 4 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS				
Temperature Coefficient of Resistance (T.C.R.)	MIL-STD-202F-method 304;	At +25/-55 °C and +25/+125 °C	Refer to table 2				
	JIS C 5202-4.8	<p><b>Formula:</b></p> $T.C.R = \frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)}$ <p>Where  <math>t_1 = +25 \text{ °C}</math> or specified room temperature  <math>t_2 = -55 \text{ °C}</math> or <math>+125 \text{ °C}</math> test temperature  <math>R_1 =</math>resistance at reference temperature in ohms  <math>R_2 =</math>resistance at test temperature in ohms</p>					
Thermal Shock	MIL-STD-202F-method 107G; IEC 60115-1 4.19	At -65 (+0/-10) °C for 2 minutes and at +155 (+10/-0) °C for 2 minutes; 25 cycles	±(0.5%+0.05 Ω) for 1% tol. ±(1.0%+0.05 Ω) for 5% tol.				
Low Temperature Operation	MIL-R-55342D-Para 4.7.4	At -65 (+0/-5) °C for 1 hour; RCWV applied for 45 (+5/-0) minutes	±(0.5%+0.05 Ω) for 1% tol . ±(1.0%+0.05 Ω) for 5% tol. No visible damage				
Short Time Overload	MIL-R-55342D-Para 4.7.5; IEC 60115-1 4.13	2.5 × RCWV applied for 5 seconds at room temperature	±(1.0%+0.05 Ω) for 1% tol. ±(2.0%+0.05 Ω) for 5% tol. No visible damage				
Insulation Resistance	MIL-STD-202F-method 302; IEC 60115-1 4.6.1.1	RCOV for 1 minute  <table border="1"> <tr> <td><b>Type</b></td> <td><b>RC0402</b></td> </tr> <tr> <td><b>Voltage (DC)</b></td> <td>100 V</td> </tr> </table>	<b>Type</b>	<b>RC0402</b>	<b>Voltage (DC)</b>	100 V	≥10 GΩ
<b>Type</b>	<b>RC0402</b>						
<b>Voltage (DC)</b>	100 V						
Dielectric Withstand Voltage	MIL-STD-202F-method 301; IEC 60115-1 4.6.1.1	Maximum voltage ( $V_{rms}$ ) applied for 1 minute  <table border="1"> <tr> <td><b>Type</b></td> <td><b>RC0402</b></td> </tr> <tr> <td><b>Voltage (AC)</b></td> <td>100 <math>V_{rms}</math></td> </tr> </table>	<b>Type</b>	<b>RC0402</b>	<b>Voltage (AC)</b>	100 $V_{rms}$	No breakdown or flashover
<b>Type</b>	<b>RC0402</b>						
<b>Voltage (AC)</b>	100 $V_{rms}$						
Resistance to Soldering Heat	MIL-STD-202F-method 210C; IEC 60115-1 4.18	Unmounted chips; 260 ±5 °C for 10 ±1 seconds	±(0.5%+0.05 Ω) for 1% tol. ±(1.0%+0.05 Ω) for 5% tol. No visible damage				
Life	MIL-STD-202F-method 108A; IEC 60115-1 4.25.1	At 70±2 °C for 1,000 hours; RCWV applied for 1.5 hours on and 0.5 hour off	±(1%+0.05 Ω) for 1% tol. ±(3%+0.05 Ω) for 5% tol.				

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS														
Solderability	MIL-STD-202F-method 208A; IEC 60115-1 4.17	Solder bath at 245±3 °C Dipping time: 2±0.5 seconds	Well tinned (≥95% covered) No visible damage														
Bending Strength	JIS C 5202.6.14; IEC 60115-1 4.15	Resistors mounted on a 90 mm glass epoxy resin PCB (FR4) Bending: 5 mm	±(1.0%+0.05 Ω) for 1% tol. ±(1.0%+0.05 Ω) for 5% tol. No visible damage														
Resistance to Solvent	MIL-STD-202F-method 215; IEC 60115-1 4.29	Isopropylalcohol (C <sub>3</sub> H <sub>7</sub> OH) or dichloromethane (CH <sub>2</sub> Cl <sub>2</sub> ) followed by brushing	No smeared														
Noise	JIS C 5202 5.9; IEC 60115-1 4.12	Maximum voltage (V <sub>rms</sub> ) applied.	<table border="1"> <thead> <tr> <th>Resistors range</th> <th>Value</th> </tr> </thead> <tbody> <tr> <td>R &lt; 100 Ω</td> <td>10 dB</td> </tr> <tr> <td>100 Ω ≤ R &lt; 1 KΩ</td> <td>20 dB</td> </tr> <tr> <td>1 KΩ ≤ R &lt; 10 KΩ</td> <td>30 dB</td> </tr> <tr> <td>10 KΩ ≤ R &lt; 100 KΩ</td> <td>40 dB</td> </tr> <tr> <td>100 KΩ ≤ R &lt; 1 MΩ</td> <td>46 dB</td> </tr> <tr> <td>1 MΩ ≤ R ≤ 22 MΩ</td> <td>48 dB</td> </tr> </tbody> </table>	Resistors range	Value	R < 100 Ω	10 dB	100 Ω ≤ R < 1 KΩ	20 dB	1 KΩ ≤ R < 10 KΩ	30 dB	10 KΩ ≤ R < 100 KΩ	40 dB	100 KΩ ≤ R < 1 MΩ	46 dB	1 MΩ ≤ R ≤ 22 MΩ	48 dB
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Humidity (steady state)	JIS C 5202 7.5; IEC 60115-8 4.24.8	1,000 hours; 40±2 °C; 93(+2/-3)% RH RCWV applied for 1.5 hours on and 0.5 hour off	±(0.5%+0.05 Ω) for 1% tol. ±(2.0%+0.05 Ω) for 5% tol.														
Leaching	EIA/IS 4.13B; IEC 60115-8 4.18	Solder bath at 260±5 °C Dipping time: 30±1 seconds	No visible damage														
Intermittent Overload	JIS C 5202 5.8	At room temperature; 2.5 × RCWV applied for 1 second on and 25 seconds off; total 10,000 cycles	±(1.0%+0.05 Ω) for 1% tol. ±(2.0%+0.05 Ω) for 5% tol.														
Resistance to Vibration	On request	On request															
Moisture Resistance Heat	MIL-STD-202F-method 106F; IEC 60115-1 4.24.2	42 cycles; total 1,000 hours Shown as Fig. 8	±(0.5%+0.05Ω) for 1% tol. ±(2.0%+0.05Ω) for 5% tol. No visible damage														





**Fig. 8** Moisture resistance test requirements

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 2	Sep 03, 2004	-	- Test method and procedure updated - PE tape added (paper tape will be replaced by PE tape)