

Analog Switches

Virtual Ground JFETs (P-Channel)

Switch Configuration				Features	Special Features	r _{DS} (on) (Ω Max.)	Package Number of Pins*
SPST	Dual SPST	Triple SPST	Quad SPST				
IH5022	IH5017 IH5018			4 channels analog gating eliminate external drivers. - Odd number devices drive directly from TTL open-collector logic (15V). - Each channel simulates SPDT switch. $t_{on}/t_{off} = 500\text{ns}$, $I_D (\text{off}) = 0.2\text{nA}$	Common Output	100 150	DD, JD, PA
		IH5014	IH5009 IH5010				DD, JD, PD
IH5024	IH5019 IH5020				Separate Output	100 150	DE, JE, PA
		IH5016	IH5011 IH5012				PA, PE

T_A Range: 0°C to +70°C, -55 to +125°C

* See interpretation guide and packaging section

RF/Video Switches, CMOS

Switch Configuration		Features	r _{DS} (on) (Ω Max.)	I _D (off) (nA Max.)	t _{on} (ns Max.)	t _{off} (ns. Max.)	Package Number of Pins*
Dual SPST	Quad SPST						
IH5341		- Superior I/O isolation enhances video, rf performance - Switch attenuation varies <3dB from dc to 100MHz	75	1.0	300	150	PD, TW
	IH5352						

T_A Range: 0°C to +70°C, -25 to +85°C, -55 to +125°C

* See interpretation guide and packaging section

Separate Driver/Switch Combinations

IH6201	TTL level translator/driver	N-JFET	30	0.5	50 (Typ)	150 (Typ)	15p-p (Min)
IH401/A	Low charge injection switch		50	0.5	50 (Typ)	150 (Typ)	20p-p (Min)

Drivers for FET Switches

Monolithic bipolar drivers convert low-level positive logic to high-level positive and negative voltages necessary to drive FET switches.

Type	Number of Channels	Output Swing		t _{ON} ns Max	t _{OFF} ns Max
		Positive (V Max)	Negative (V Max)		
IH6201	2 TTL	+14.0	-14.0	400	300