

# Technische Information / Technical Information

IGBT-Module  
IGBT-Modules

## FF150R12KS4

eupec



### Höchstzulässige Werte / Maximum rated values

#### Elektrische Eigenschaften / Electrical properties

Kollektor-Emitter-Sperrspannung collector-emitter voltage	$T_{vj} = 25^\circ\text{C}$	$V_{CES}$	1200	V
Kollektor-Dauergleichstrom DC-collector current	$T_C = 70^\circ\text{C}$	$I_{C,nom.}$	150	A
	$T_C = 25^\circ\text{C}$	$I_C$	225	A
Periodischer Kollektor Spitzenstrom repetitive peak collector current	$t_p = 1\text{ ms}, T_C = 80^\circ\text{C}$	$I_{CRM}$	300	A
Gesamt-Verlustleistung total power dissipation	$T_C = 25^\circ\text{C}, \text{ Transistor}$	$P_{tot}$	1,2	kW
Gate-Emitter-Spitzenspannung gate-emitter peak voltage		$V_{GES}$	+/- 20V	V
Dauergleichstrom DC forward current		$I_F$	150	A
Periodischer Spitzenstrom repetitive peak forw. current	$t_p = 1\text{ ms}$	$I_{FRM}$	300	A
Grenzlastintegral der Diode $I^2t$ - value, Diode	$V_R = 0V, t_p = 10\text{ms}, T_{vj} = 125^\circ\text{C}$	$I^2t$	9	$\text{k A}^2\text{s}$
Isolations-Prüfspannung insulation test voltage	RMS, $f = 50\text{ Hz}, t = 1\text{ min.}$	$V_{ISOL}$	2,5	kV

### Charakteristische Werte / Characteristic values

#### Transistor / Transistor

			min.	typ.	max.	
Kollektor-Emitter Sättigungsspannung collector-emitter saturation voltage	$I_C = 150\text{A}, V_{GE} = 15\text{V}, T_{vj} = 25^\circ\text{C}$	$V_{CE\text{ sat}}$	-	3,2	3,7	V
	$I_C = 150\text{A}, V_{GE} = 15\text{V}, T_{vj} = 125^\circ\text{C}$		-	3,85	-	V
Gate-Schwellenspannung gate threshold voltage	$I_C = 6\text{mA}, V_{CE} = V_{GE}, T_{vj} = 25^\circ\text{C}$	$V_{GE(th)}$	4,5	5,5	6,5	V
Gateladung gate charge	$V_{GE} = -15\text{V} \dots +15\text{V}$	$Q_G$	-	1,6	-	$\mu\text{C}$
Eingangskapazität input capacitance	$f = 1\text{MHz}, T_{vj} = 25^\circ\text{C}, V_{CE} = 25\text{V}, V_{GE} = 0\text{V}$	$C_{ies}$	-	11	-	nF
Rückwirkungskapazität reverse transfer capacitance	$f = 1\text{MHz}, T_{vj} = 25^\circ\text{C}, V_{CE} = 25\text{V}, V_{GE} = 0\text{V}$	$C_{res}$	-	0,5	-	nF
Kollektor-Emitter Reststrom collector-emitter cut-off current	$V_{CE} = 1200\text{V}, V_{GE} = 0\text{V}, T_{vj} = 25^\circ\text{C}$	$I_{CES}$	-	-	5	mA
Gate-Emitter Reststrom gate-emitter leakage current	$V_{CE} = 0\text{V}, V_{GE} = 20\text{V}, T_{vj} = 25^\circ\text{C}$	$I_{GES}$	-	-	400	nA

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### Charakteristische Werte / Characteristic values

#### Transistor / Transistor

			min.	typ.	max.	
Einschaltverzögerungszeit (ind. Last) turn on delay time (inductive load)	$I_C = 150A, V_{CE} = 600V$	$t_{d,on}$	-	0,10	-	$\mu s$
	$V_{GE} = \pm 15V, R_G = 6,8\Omega, T_{vj} = 25^\circ C$		-	0,13	-	$\mu s$
Anstiegszeit (induktive Last) rise time (inductive load)	$I_C = 150A, V_{CE} = 600V$	$t_r$	-	0,09	-	$\mu s$
	$V_{GE} = \pm 15V, R_G = 6,8\Omega, T_{vj} = 125^\circ C$		-	0,10	-	$\mu s$
Abschaltverzögerungszeit (ind. Last) turn off delay time (inductive load)	$I_C = 150A, V_{CE} = 600V$	$t_{d,off}$	-	0,53	-	$\mu s$
	$V_{GE} = \pm 15V, R_G = 6,8\Omega, T_{vj} = 125^\circ C$		-	0,59	-	$\mu s$
Fallzeit (induktive Last) fall time (inductive load)	$I_C = 150A, V_{CE} = 600V$	$t_f$	-	0,06	-	$\mu s$
	$V_{GE} = \pm 15V, R_G = 6,8\Omega, T_{vj} = 125^\circ C$		-	0,07	-	$\mu s$
Einschaltverlustenergie pro Puls turn-on energy loss per pulse	$I_C = 150A, V_{CE} = 600V, V_{GE} = \pm 15V$ $R_G = 6,8\Omega, T_{vj} = 125^\circ C, L_G = 60nH$	$E_{on}$	-	14,5	-	mJ
Abschaltverlustenergie pro Puls turn-off energy loss per pulse	$I_C = 150A, V_{CE} = 600V, V_{GE} = \pm 15V$ $R_G = 6,8\Omega, T_{vj} = 125^\circ C, L_G = 60nH$	$E_{off}$	-	11	-	mJ
Kurzschlußverhalten SC Data	$t_p \leq 10\mu s, V_{GE} \leq 15V, R_G = 6,8\Omega$ $T_{vj} \leq 125^\circ C, V_{CC} = 900V, V_{CEmax} = V_{CES} - L_{\sigma CE} \cdot di/dt$	$I_{SC}$	-	950	-	A
Modulinduktivität stray inductance module	Anschlüsse / terminals 2-3	$L_{\sigma CE}$	-	20	-	nH
Modul Leitungswiderstand, Anschlüsse – Chip module lead resistance, terminals – chip	pro Zweig / per arm, $T_c = 25^\circ C$	$R_{CC+EE}$	-	0,7	-	m $\Omega$

### Charakteristische Werte / Characteristic values

#### Diode / Diode

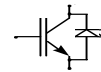
			min.	typ.	max.	
Durchlaßspannung forward voltage	$I_F = 150A, V_{GE} = 0V, T_{vj} = 25^\circ C$	$V_F$	-	2,0	2,4	V
	$I_F = 150A, V_{GE} = 0V, T_{vj} = 125^\circ C$		-	1,7	-	V
Rückstromspitze peak reverse recovery current	$I_F = 150A, -di_F/dt = 1500A/\mu s$	$I_{RM}$	-	105	-	A
	$V_R = 600V, V_{GE} = -15V, T_{vj} = 25^\circ C$ $V_R = 600V, V_{GE} = -15V, T_{vj} = 125^\circ C$		-	165	-	A
Sperrverzögerungsladung recovered charge	$I_F = 150A, -di_F/dt = 1500A/\mu s$	$Q_r$	-	11,5	-	$\mu C$
	$V_R = 600V, V_{GE} = -15V, T_{vj} = 25^\circ C$ $V_R = 600V, V_{GE} = -15V, T_{vj} = 125^\circ C$		-	30,0	-	$\mu C$
Abschaltenergie pro Puls reverse recovery energy	$I_F = 150A, -di_F/dt = 1500A/\mu s$	$E_{rec}$	-	6	-	mJ
	$V_R = 600V, V_{GE} = -15V, T_{vj} = 25^\circ C$ $V_R = 600V, V_{GE} = -15V, T_{vj} = 125^\circ C$		-	14,5	-	mJ

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### Thermische Eigenschaften / Thermal properties

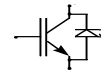
			min.	typ.	max.	
Innerer Wärmewiderstand thermal resistance, junction to case	Transistor / transistor, DC, pro Modul / per module	$R_{thJC}$	-	-	0,050	K/W
	Transistor / transistor, DC, pro Zweig / per arm		-	-	0,100	K/W
	Diode / Diode, DC, pro Modul / per module		-	-	0,125	K/W
	Diode / Diode, DC, pro Zweig / per arm		-	-	0,250	K/W
Übergangs-Wärmewiderstand thermal resistance, case to heatsink	pro Modul / per module	$R_{thCK}$	-	0,010	-	K/W
	pro Zweig / per arm		-	0,020	-	K/W
	$\lambda_{Paste} = 1 \text{ W/m}^2\text{K} / \lambda_{grease} = 1 \text{ W/m}^2\text{K}$					
Höchstzulässige Sperrschichttemperatur maximum junction temperature		$T_{vj\ max}$	-	-	150	°C
Betriebstemperatur operation temperature		$T_{vj\ op}$	-40	-	125	°C
Lagertemperatur storage temperature		$T_{stg}$	-40	-	125	°C

### Mechanische Eigenschaften / Mechanical properties

Gehäuse, siehe Anlage case, see appendix						
Innere Isolation internal insulation				$Al_2O_3$		
Kriechstrecke creepage distance				20		mm
Luftstrecke clearance distance				11		mm
CTI comperative tracking index				425		
Anzugsdrehmoment f. mech. Befestigung mounting torque	Schraube / screw M6	M	3,0	-	6,0	Nm
Anzugsdrehmoment f. elektr. Anschlüsse terminal connection torque	Anschlüsse / terminals M6	M	2,5	-	5,0	Nm
Gewicht weight		G		340		g

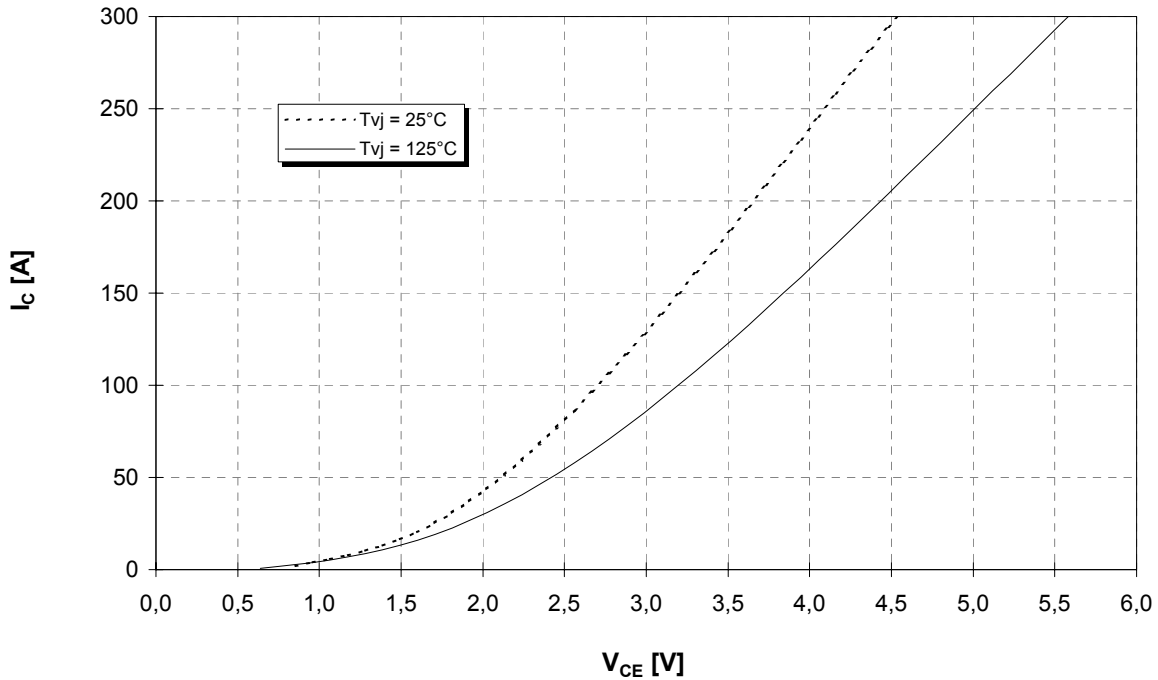
Mit dieser technischen Information werden Halbleiterbauelemente spezifiziert, jedoch keine Eigenschaften zugesichert. Sie gilt in Verbindung mit den zugehörigen Technischen Erläuterungen.

This technical information specifies semiconductor devices but promises no characteristics. It is valid in combination with the belonging technical notes.



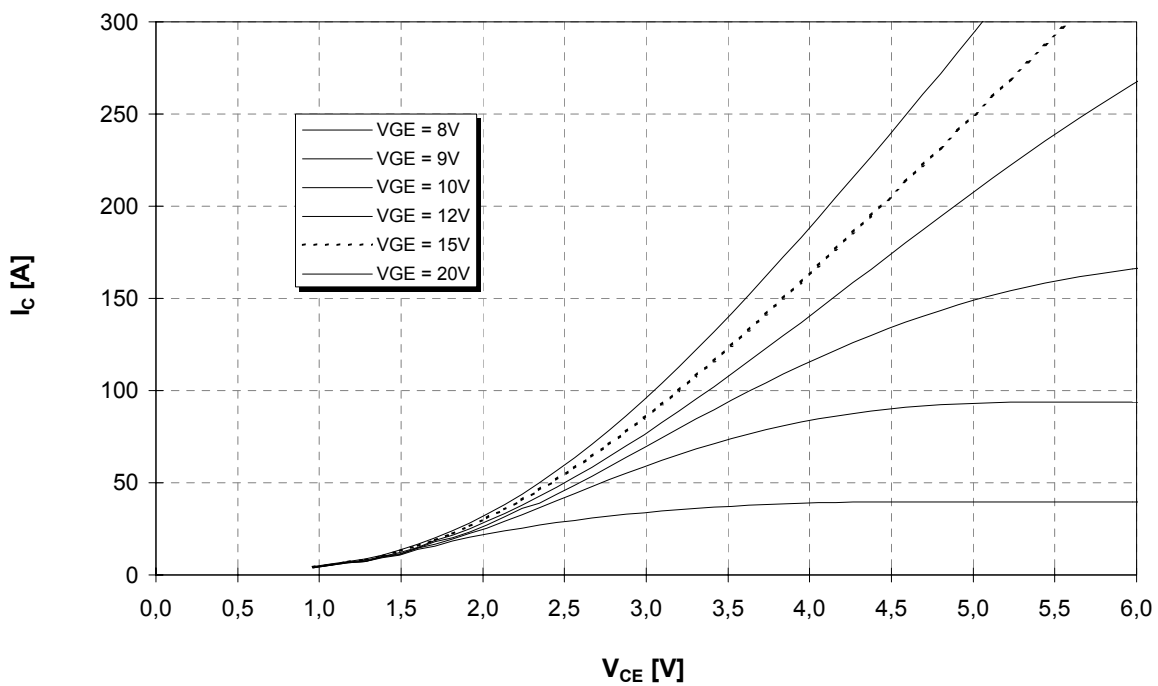
**Ausgangskennlinie (typisch)**  
**Output characteristic (typical)**

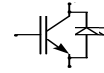
**$I_C = f(V_{CE})$**   
 **$V_{GE} = 15V$**



**Ausgangskennlinienfeld (typisch)**  
**Output characteristic (typical)**

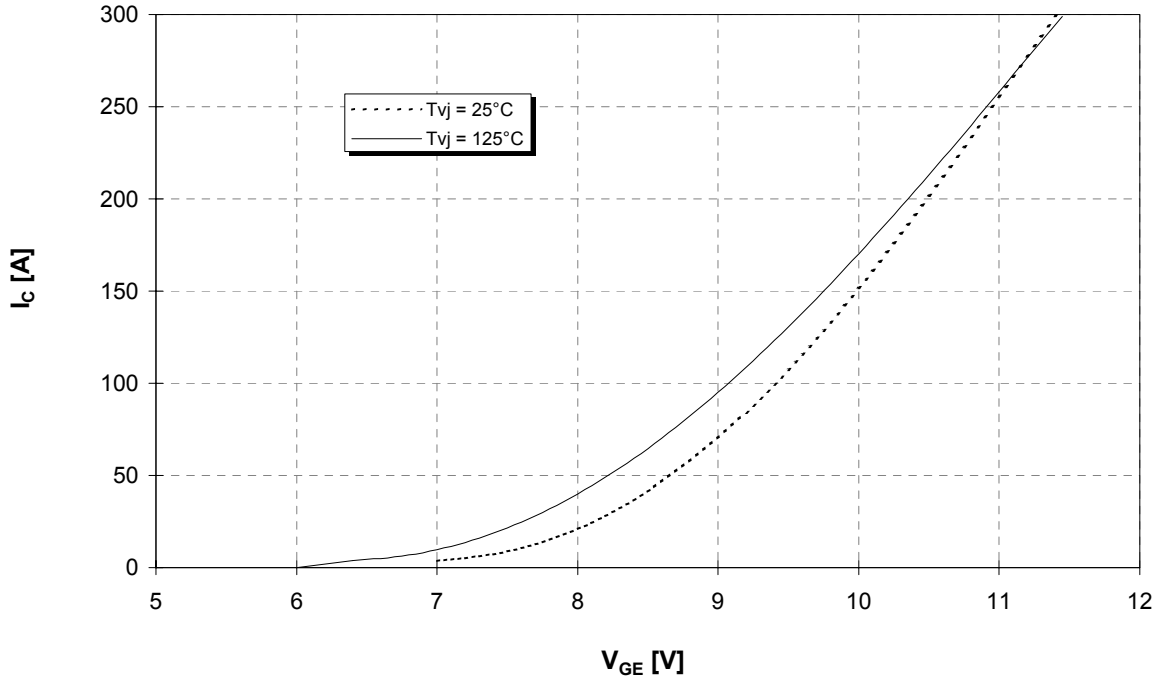
**$I_C = f(V_{CE})$**   
 **$T_{vj} = 125^\circ C$**





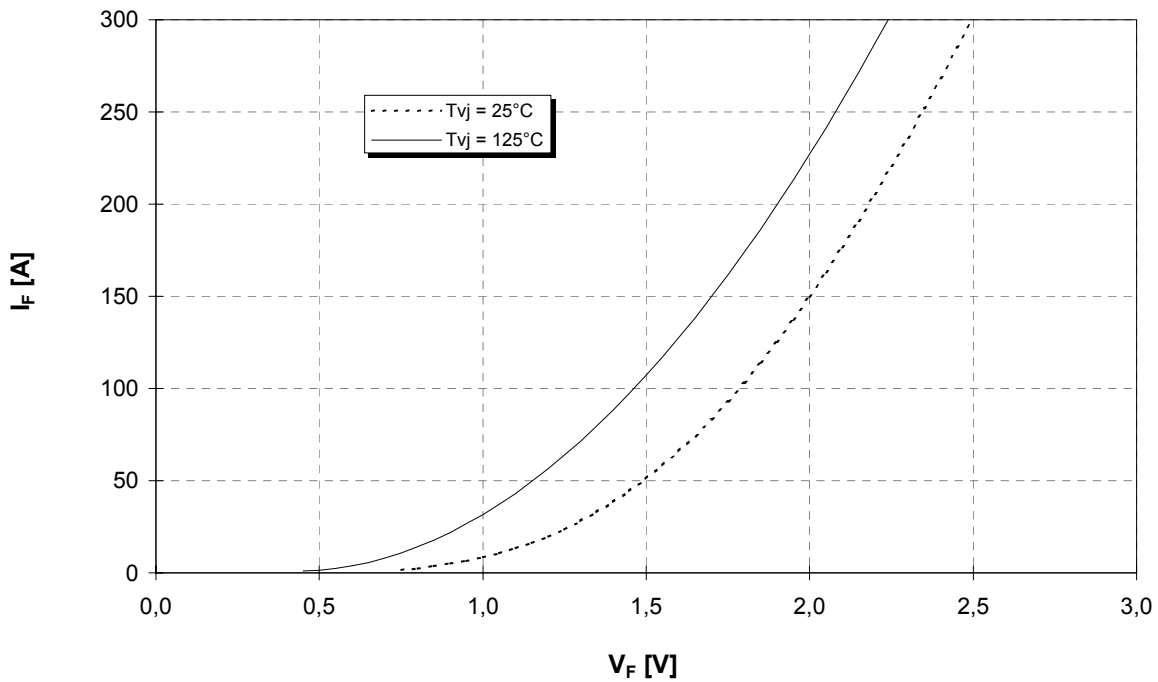
Übertragungscharakteristik (typisch)  
Transfer characteristic (typical)

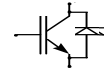
$I_C = f(V_{GE})$   
 $V_{CE} = 20V$



Durchlaßkennlinie der Inversdiode (typisch)  
Forward characteristic of inverse diode (typical)

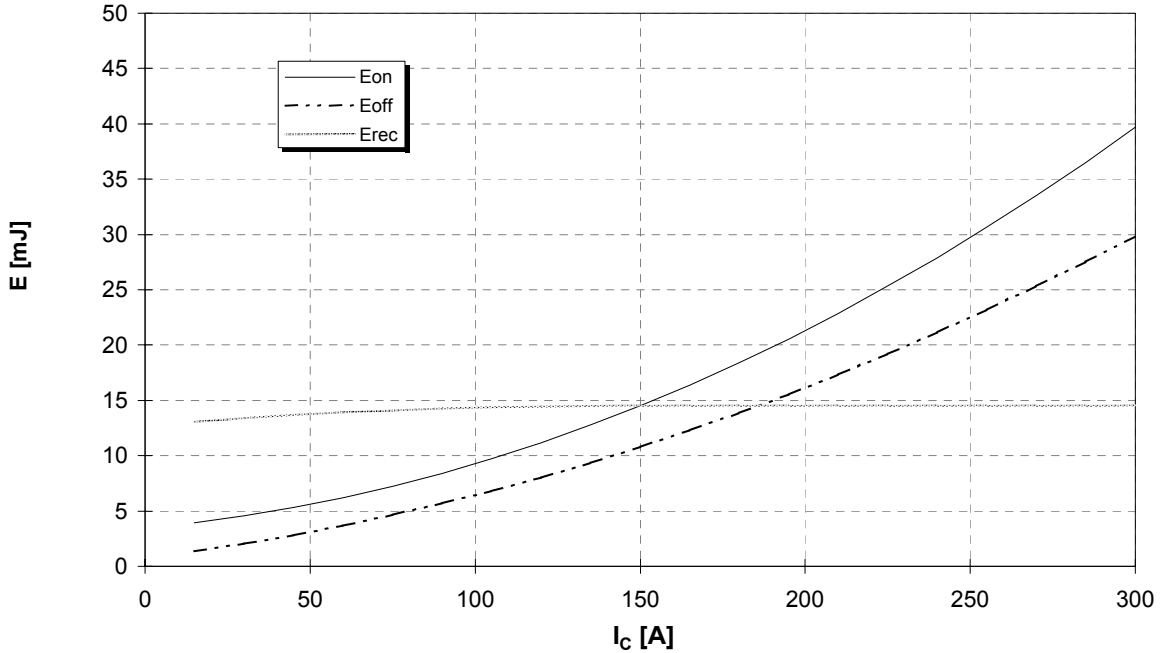
$I_F = f(V_F)$





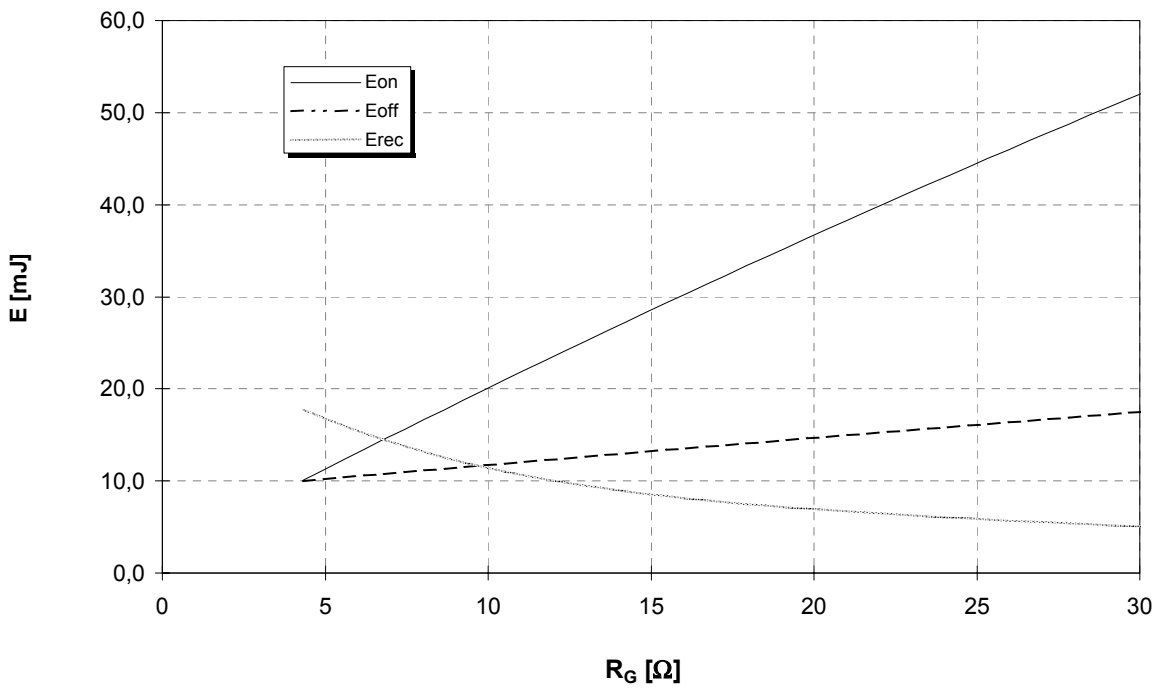
Schaltverluste (typisch)  $E_{on} = f(I_C)$ ,  $E_{off} = f(I_C)$ ,  $E_{rec} = f(I_C)$

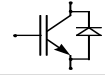
Switching losses (typical)  $V_{GE} = \pm 15V$ ,  $R_G = 6,8 \Omega$ ,  $V_{CE} = 600V$ ,  $T_{vj} = 125^\circ C$



Schaltverluste (typisch)  $E_{on} = f(R_G)$ ,  $E_{off} = f(R_G)$ ,  $E_{rec} = f(R_G)$

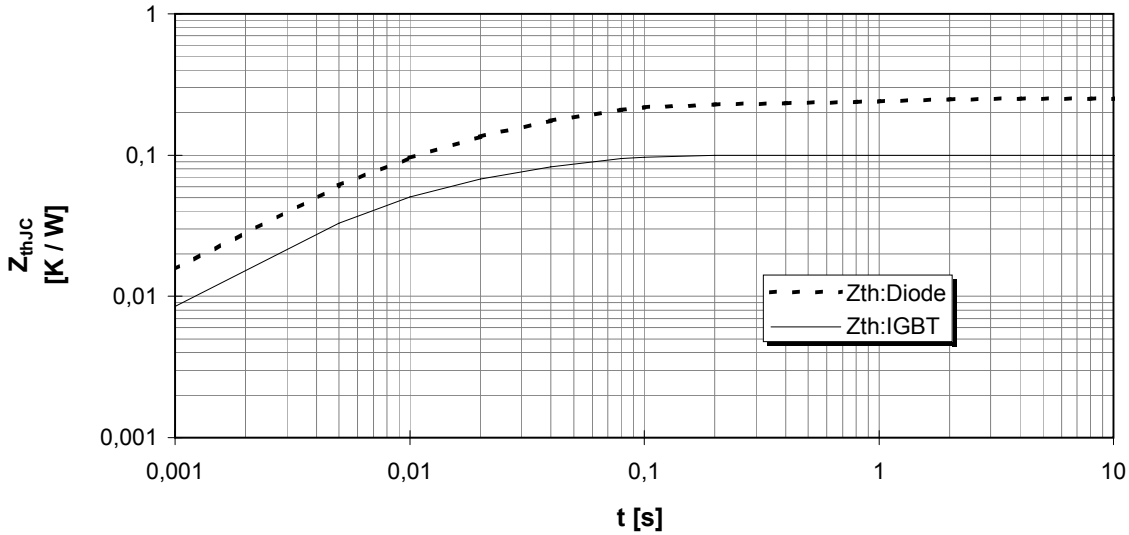
Switching losses (typical)  $V_{GE} = \pm 15V$ ,  $I_C = 150A$ ,  $V_{CE} = 600V$ ,  $T_{vj} = 125^\circ C$





**Transienter Wärmewiderstand**  
**Transient thermal impedance**

$Z_{thJC} = f(t)$

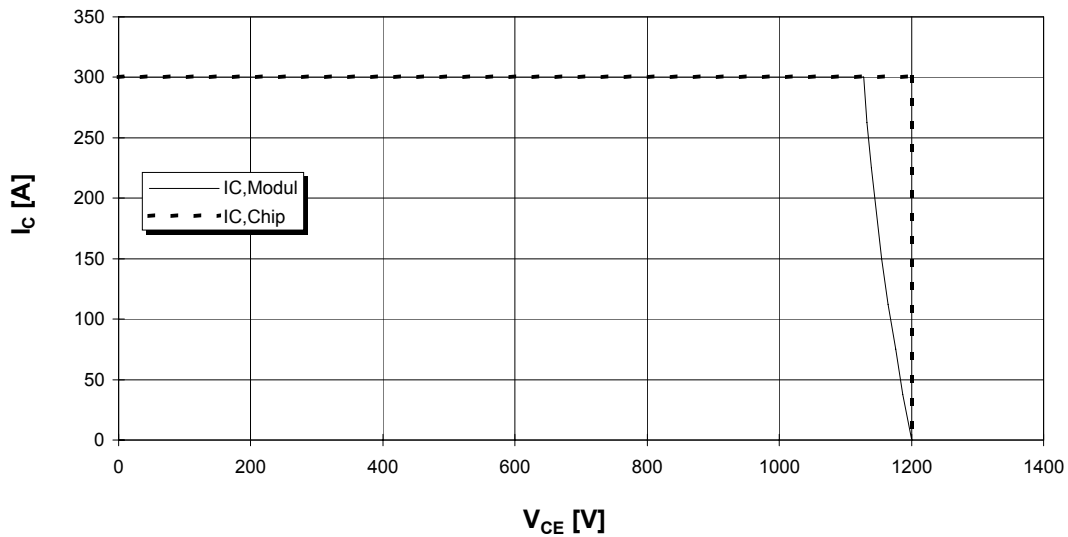


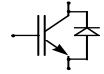
i	1	2	3	4
$r_i$ [K/kW] : IGBT	44,54	33,9	21,52	0,04
$\tau_i$ [s] : IGBT	0,006	0,029	0,043	1,014
$r_i$ [K/kW] : Diode	68,24	101,68	52,66	27,42
$\tau_i$ [s] : Diode	0,006	0,035	0,033	0,997

**Sicherer Arbeitsbereich (RBSOA)**

**Reverse bias safe operation area (RBSOA)**

$V_{GE} = \pm 15V, R_G = 6,8 \Omega, T_{vj} = 125^\circ C$





Gehäusemaße / Schaltbild  
Package outline / Circuit diagram

