

UNISONIC TECHNOLOGIES CO., LTD

7N10 Power MOSFET

7A, 100V N-CHANNEL POWER MOSFET

DESCRIPTION

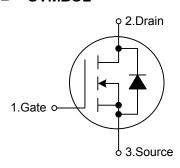
The UTC 7N10 is an N-Channel enhancement mode power MOSFET, providing customers with excellent switching performance and minimum on-state resistance. The UTC 7N10 uses planar stripe and DMOS technology to provide perfect quality. This device can also withstand high energy pulse in the avalanche and the commutation mode.

The UTC 7N10 is generally applied in low voltage applications, such as DC motor controls, audio amplifiers and high efficiency switching DC/DC converters,.

FEATURES

- * Low Gate Charge: 5.8nC (TYP.)
- * Low C_{RSS:} 10 pF (TYP.)
- * $R_{DS(ON)}$ = 0.35 Ω @ V_{GS} = 10 V
- * Fast Switching
- * Improved dv/dt Capability

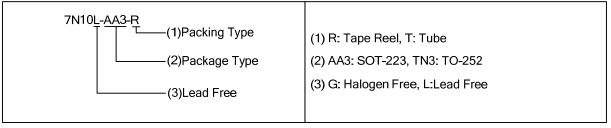
SYMBOL



ORDERING INFORMATION

Ordering Number		Dookone	Pin Assignment			Deakins	
Lead Free	Halogen Free	Package	1	2	3	Packing	
7N10L-AA3-R	7N10G-AA3-R	SOT-223	G	D	S	Tape Reel	
7N10L-TN3-R	7N10G-TN3-R	TO-252	G	D	S	Tape Reel	
7N10L-TN3-T	7N10G-TN3-T	TO-252	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



SOT-223 TO-252

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■ **ABSOLUTE MAXIMUM RATINGS** (T_C =25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain -Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	±25	V
0 11 0 1	T _C =25°C	I _D	7	Α
Continuous Drain Current	$T_C = 70^{\circ}C$	I _D	6.8	Α
Pulsed Drain Current (Note 2)		I _{DM}	16	Α
Avalanche Current (Note 2)		I _{AR}	7	Α
Repetitive Avalanche Energy (Note 2)		E _{AR}	0.2	mJ
Single Pulsed Avalanche Energy (Note 3)		E _{AS}	50	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	6.0	V/ns
Power Dissipation	SOT-223		2.0	10/
	TO-252	D	2.5	W
Derate above 25°C	SOT-223	P _D	0.016	\\\\\\\\
	TO-252		0.02	W/°C
Operating Junction Temperature		TJ	-55 ~ +150	°C
Storage Temperature		T _{STG}	-55 ~ + 150	°C

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L =26mH, I_{AS} =1.7A, V_{DD} =25V, R_{G} =25 Ω Starting T_{J} =25 $^{\circ}$ C
- 4. $I_{SD} \le 7.3A$, di/dt $\le 300A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	0	62.5	° C / A /
	TO-252	$\Theta_{ m JA}$	50	°C/W

Note: When mounted on the minimum pad size recommended (PCB Mount)

■ ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS} = 0V, I_D = 250 \mu A$	100			V	
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_{J}$	Reference to 25°C, I _D =250µA		0.1		V/°C	
Drain-Source Leakage Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	μA	
		V _{DS} =80V, T _C =125°C			10	μΑ	
Gate-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 25V$, $V_{DS} = 0V$			±100	nA	
ON CHARACTERISTICS							
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V	
Static Drain-Source On-Resistance	R _{DS(ON)}	$V_{GS} = 10V, I_D = 3.5A$		0.28	0.35	Ω	
Forward Transconductance	g FS	V _{DS} =40V, I _D =0.85A (Note 1)		1.85		S	
DYNAMIC PARAMETERS							
Input Capacitance	C _{ISS}			190	250	pF	
Output Capacitance	Coss	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		60	75	pF	
Reverse Transfer Capacitance	C _{RSS}			10	13	pF	

■ ELECTRICAL CHARACTERISTICS (Cont.)

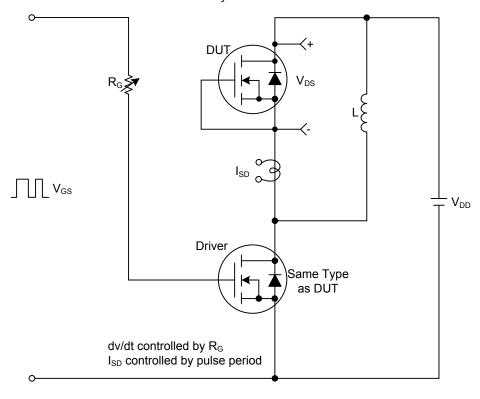
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
SWITCHING PARAMETERS							
Total Gate Charge	Q_G	1/ 10// 1/ 00// 1 7 04		5.8	7.5	nC	
Gate Source Charge	Q_{GS}	V_{GS} =10V, V_{DS} =80V, I_{D} =7.3A		1.4		nC	
Gate Drain Charge	Q_{GD}	(Note 1,2)		2.5		nC	
Turn-ON Delay Time	t _{D(ON)}			7	25	ns	
Turn-ON Rise Time	t _R	V_{DD} =50V, I_{D} =7.3A, R_{G} =25 Ω		24	60	ns	
Turn-OFF Delay Time	t _{D(OFF)}	(Note 1,2)		13	35	ns	
Turn-OFF Fall-Time	t _F			19	50	ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Continuous Drain-Source Diode					7	^	
Forward Current	I _S				1	Α	
Maximum Pulsed Drain-Source Diode					16	Α	
Forward Current	I _{SM}				10	A	
Drain-Source Diode Forward Voltage	V_{SD}	I _S =7A, V _{GS} =0V			1.5	V	
Reverse Recovery Time	t _{rr}	V_{GS} =0V, I_{S} =7.3A,		70		ns	
Reverse Recovery Charge	Q_{RR}	di _F /dt=100A/µs		150		nC	

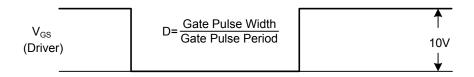
Notes: 1. Pulse Test : Pulse width ≤ 300µs, Duty cycle ≤ 2%

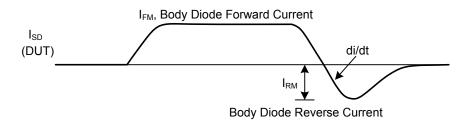
2. Essentially independent of operating temperature

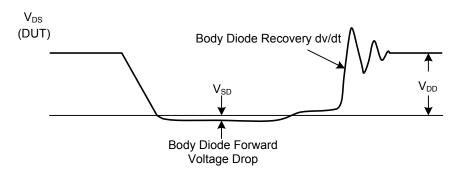
■ TEST CIRCUITS AND WAVEFORMS

Peak Diode Recovery dv/dt Test Circuit & Waveforms

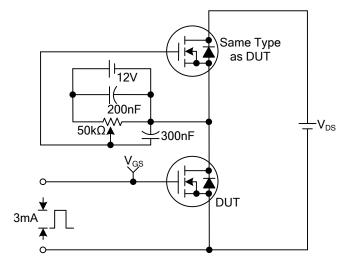








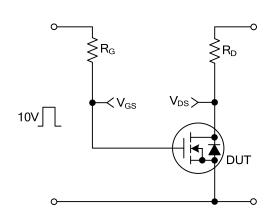
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



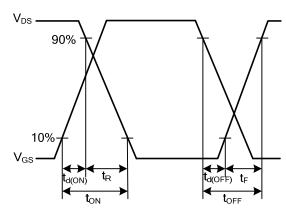
 V_{GS} Q_{G} Q_{G} Q_{GD} Q_{GD} Q_{GD} Q_{GD}

Gate Charge Test Circuit

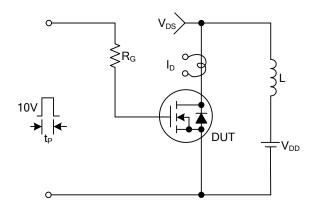
Gate Charge Waveforms



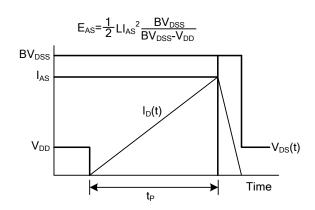
Resistive Switching Test Circuit



Resistive Switching Waveforms



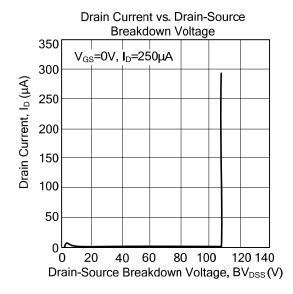
Unclamped Inductive Switching Test Circuit

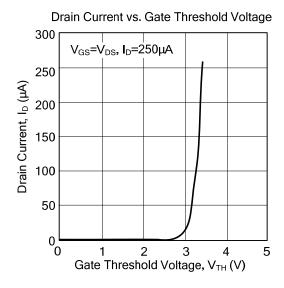


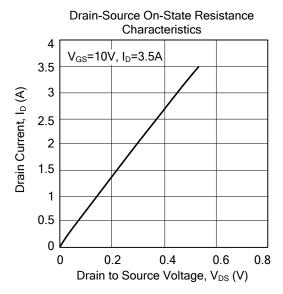
Unclamped Inductive Switching Waveforms

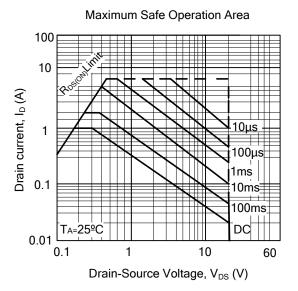
7N10 Power MOSFET

■ TYPICAL CHARACTERISTICS









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