

SANYO Semiconductors DATA SHEET

LV8019V — Forward/Reverse Motor Driver

Overview

The LV8019V is a forward/reverse motor driver.

Features

- One H-bridge driver channel
- Provides a constant current output
- Built-in thermal shutdown circuit

Specifications

Maximum Ratings at Ta = 25°C and SGND = PGND = 0V

Parameter	Symbol	Conditions Ratings		Unit
Output block supply voltage	VM max		-0.5 to 8.4	٧
Control block supply voltage	V _{CC} max		-0.5 to 7.0	V
Constant current output block supply voltage	VRG max		-0.5 to 6.0	V
Maximum output current	I _O max		1.0	А
	I _O peak1	$t \le 200 ms, f = 2 Hz$	3	Α
	I _O peak2	t ≤ 10ms, f = 2Hz	5	А
Input signal voltage	V _{IN} max		-0.5 to V _{CC} +0.5	А
Allowable power dissipation	Pd max	When mounted on a circuit board *1	0.8	W
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

^{*1} Specified circuit board : $114.3 \times 76.1 \times 1.6$ mm³, glass epoxy

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LV8019V

Recommended Operating Conditions at Ta = 25°C and SGND = PGND = 0V

Parameter	Symbol	Conditions	Ratings	Unit
Output block supply voltage	VM		3.0 to 7.4	V
Control block supply voltage	Vcc		2.7 to 6.0	V
Constant current output block supply voltage	VRGIN		1.5 to V _{CC}	V
Input signal voltage	VIN		0 to V _{CC}	V
Maximum input signal frequency	f _{max}	Duty = 50%	100	kHz

Electrical Characteristics Ta = 25 °C, $V_{CC} = VM = 5V$, and SGND = PGND = 0V unless otherwise specified.

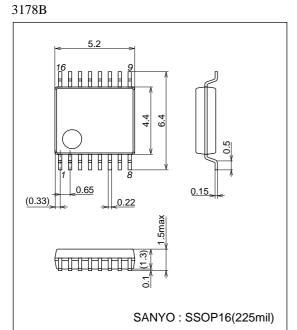
Parameter		Cumbal	Symbol Conditions		Ratings			
		Symbol Conditions		min	typ	max	Unit	
Standby mode output block current consumption		IMO	EN = 0V, IN1 = IN2 = ICTRL = 0V			1.0	μΑ	
Control block Standby current mode		Icco	EN = 0V, IN1 = IN2 = ICTRL = 0V		0	1.0	μА	
consumption	Operation mode	ICC	EN = 5V		0.8	1.3	mA	
High-level input vol	tage	V _{IN} H		2.5		VCC	V	
Low-level input volt	age	V _{IN} L		0		0.8	V	
High-level input cur	rent	I _{IN} H				1.0	μΑ	
Low-level input curr	rent	I _{IN} L		-1.0			μА	
High-level EN pin c	High-level EN pin current		EN pin	15	25	35	μА	
Low-level EN pin cu	Low-level EN pin current		EN pin			1.0	μА	
Output on	1	R _{ON} 1	VM = 5V, sink + source		0.45	0.55	Ω	
resistance	2	R _{ON} 2	VM = 3V, sink + source		0.60	0.75	Ω	
ISET setting resistance		RSET	Between ISET pin and SGND	80			Ω	
ISET pin voltage	ISET pin voltage		RSET > 80Ω	0.90	1.05	1.20	V	
CC pin output satur	ation voltage	VCSAT	RSET > 150Ω *1			1.5	V	
CC pin output leaka	age current	ICONL	CTRL = 0V			1.0	μА	
Low voltage shutdown operation voltage		VLVD	V _{CC} pin voltage detection	2.10	2.35	2.60	V	
High-level output turn-on time		ТОН	The transition from 10% to 90% of the output amplitude *2		0.1	1.0	μs	
Low-level output turn-on time		TOL	The transition from 90% to 10% of the output amplitude *2		0.2	2.0	μS	
Thermal shutdown temperature		TSD	*2	150	180		°C	
Thermal shutdown hysteresis		ΔTSD	*2		40		°C	

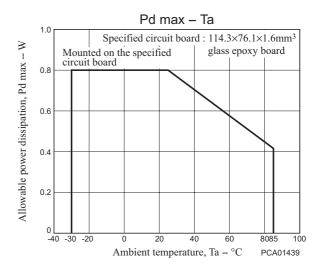
 $^{^{\}star}1$: Voltage between CC pin and ISET pin

 $[\]ensuremath{^{*}2}$: Design guarantee: These characteristics are not measured.

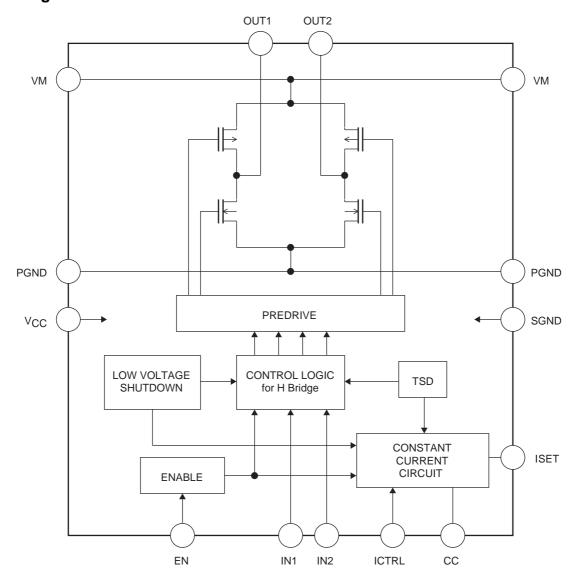
Package Dimensions

unit: mm (typ)





Block Diagram



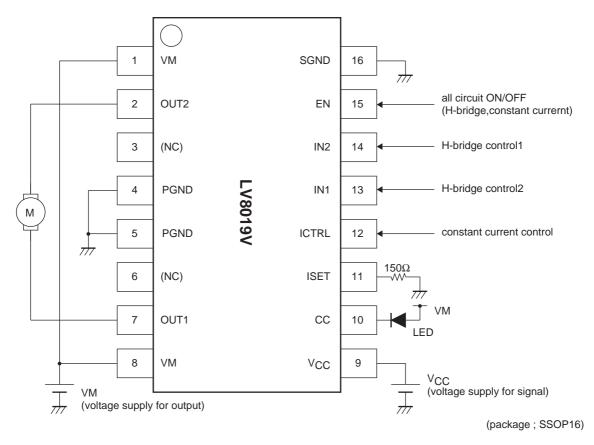
Truth Table

EN	IN1	IN2	ICTRL	OUT1	OUT2	CC	Mode	
Н	Н	Н	X	L	L	X	Break	
Н	Н	L	X	Н	L	X	Forward	
Н	L	Н	X	L	Н	X	Reverse	
Н	L	L	X	Z	Z	X	Standby	
L	X	X	X	L	L	L	Standby	
Н	Х	Х	L	Х	Х	Z	Constant current output off	
Н	Х	Х	Н	X	X	ON	Constant current output on	

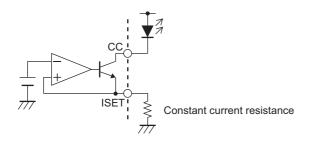
H : High level
L : Low level
Z : Hi-impedance

X : Don't care

Pin Assignment and Application Example



Constant current output



LV8019V

Pin Functions

Pin No.	Pin	Description	Equivalent circuit
13 14	IN1 IN2	Logic input 1 Logic input 2 The output is set by the combination of the input 1 and 2 states. See the truth table for details.	V_{CC} I_{N2} I_{N2} I_{N2} I_{N2}
12	ICTRL	Controls the output on/off state of the constant current block.	ICTRL S-GND
15	EN	EN pin. Controls the on/off state of the H-bridge output (OUT1 and OUT2) and the constant current output. See the truth table for details.	V_{CC} $10k\Omega$ $10k\Omega$ $200k\Omega$
7 2	OUT1 OUT2	Output 1. Output 2. The source side is a p-channel transistor and sink side is an n-channel transistor.	VM OUT* PGND
10 11	CC ISET	Constant current output. Constant current setting. The output current (CC) is set by connecting a resistor between the ISET pin and ground.	VCC CC SGND SGND SGND
9	VCC	Signal system power supply.	Vcc
8	VM	Power system power supply.	VM
16	SGND	Signal system ground.	SGND ()———
4,5	PGND	Power system ground.	PGND O

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