



APT1001RBVFR

1000V 11A 1.000Ω

POWER MOS V

FREDFET

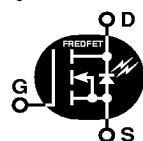
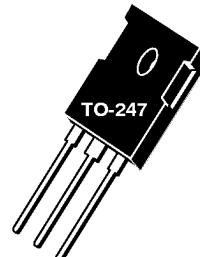
Power MOS V is a new generation of high voltage N-Channel enhancement mode power MOSFETs. This new technology minimizes the JFET effect, increases packing density and reduces the on-resistance. Power MOS V also achieves faster switching speeds through optimized gate layout.

- Fast Recovery Body Diode
- Lower Leakage
- Faster Switching
- 100% Avalanche Tested
- Popular TO-247 Package

MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	APT1001RBVFR	UNIT
V_{DSS}	Drain-Source Voltage	1000	Volts
I_D	Continuous Drain Current @ $T_C = 25^\circ\text{C}$	11	Amps
I_{DM}	Pulsed Drain Current ①	44	
V_{GS}	Gate-Source Voltage Continuous	± 30	Volts
V_{GSM}	Gate-Source Voltage Transient	± 40	
P_D	Total Power Dissipation @ $T_C = 25^\circ\text{C}$	280	Watts
	Linear Derating Factor	2.24	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	$^\circ\text{C}$
T_L	Lead Temperature: 0.063" from Case for 10 Sec.	300	
I_{AR}	Avalanche Current ① (Repetitive and Non-Repetitive)	11	Amps
E_{AR}	Repetitive Avalanche Energy ①	30	
E_{AS}	Single Pulse Avalanche Energy ④	1210	mJ



STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage ($V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$)	1000			Volts
$I_{D(on)}$	On State Drain Current ② ($V_{DS} > I_{D(on)} \times R_{DS(on)}$ Max, $V_{GS} = 10\text{V}$)	11			Amps
$R_{DS(on)}$	Drain-Source On-State Resistance ② ($V_{GS} = 10\text{V}$, $0.5 I_{D[Cont.]}$)			1.00	Ohms
I_{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = V_{DSS}$, $V_{GS} = 0\text{V}$)			250	μA
	Zero Gate Voltage Drain Current ($V_{DS} = 0.8 V_{DSS}$, $V_{GS} = 0\text{V}$, $T_C = 125^\circ\text{C}$)			1000	
I_{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 30\text{V}$, $V_{DS} = 0\text{V}$)			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 1.0\text{mA}$)	2		4	Volts



CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

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DYNAMIC CHARACTERISTICS

APT1001RBVFR

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1\text{ MHz}$		3050		pF
C_{oss}	Output Capacitance			270		
C_{rss}	Reverse Transfer Capacitance			125		
Q_g	Total Gate Charge ③	$V_{GS} = 10V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D[\text{Cont.}] @ 25^\circ C$		150		nC
Q_{gs}	Gate-Source Charge			16		
Q_{gd}	Gate-Drain ("Miller") Charge			78		
$t_{d(on)}$	Turn-on Delay Time	$V_{GS} = 15V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D[\text{Cont.}] @ 25^\circ C$		12		ns
t_r	Rise Time			11		
$t_{d(off)}$	Turn-off Delay Time			60		
t_f	Fall Time	$R_G = 1.6\Omega$		15		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
I_S	Continuous Source Current (Body Diode)			11	Amps
I_{SM}	Pulsed Source Current ① (Body Diode)			44	
V_{SD}	Diode Forward Voltage ② ($V_{GS} = 0V$, $I_S = -I_D[\text{Cont.}]$)			1.3	Volts
dV/dt	Peak Diode Recovery dV/dt ⑤			5	
t_{rr}	Reverse Recovery Time ($I_S = -I_D[\text{Cont.}]$, $dI/dt = 100A/\mu s$)	$T_j = 25^\circ C$		200	ns
		$T_j = 125^\circ C$		350	
Q_{rr}	Reverse Recovery Charge ($I_S = -I_D[\text{Cont.}]$, $dI/dt = 100A/\mu s$)	$T_j = 25^\circ C$		0.7	μC
		$T_j = 125^\circ C$		1.5	
I_{RRM}	Peak Recovery Current ($I_S = -I_D[\text{Cont.}]$, $dI/dt = 100A/\mu s$)	$T_j = 25^\circ C$		11	Amps
		$T_j = 125^\circ C$		16	

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.45	$^\circ C/W$
$R_{\theta JA}$	Junction to Ambient			40	

① Repetitive Rating: Pulse width limited by maximum junction temperature.

③ See MIL-STD-750 Method 3471

④ Starting $T_j = +25^\circ C$, $L = 20mH$, $R_G = 25\Omega$, Peak $I_L = 11A$

② Pulse Test: Pulse width < 380 μs , Duty Cycle < 2%

⑤ $I_S \leq -I_D[\text{Cont.}]$, $dI/dt = 100A/\mu s$, $V_{DD} \leq V_{DSS}$, $T_j \leq 150^\circ C$, $R_G = 2.0\Omega$, $V_R = 200V$.

APT Reserves the right to change, without notice, the specifications and information contained herein.

TO-247AD Package Outline

