

C²MOS DIGITAL INTEGRATED CIRCUIT
SILICON MONOLITHIC

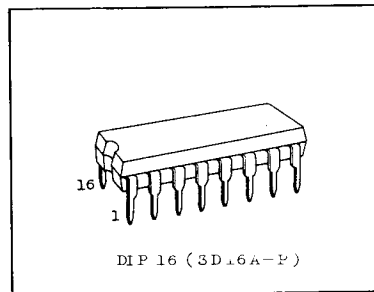
TC5002BP, TC5022BP

TC5002BP, TC5022BP BCD TO 7-SEGMENT DECODER/DRIVER

TC5002BP and TC5022BP are decoders to convert BCD code input to the driving signal for 7-segment display element and equipped with NPN transistors as the output buffers enabling direct driving of common cathode type LED.

When BI input is set at "H" level, all the segment outputs are turned "OFF" (not illumination) regardless of other inputs.

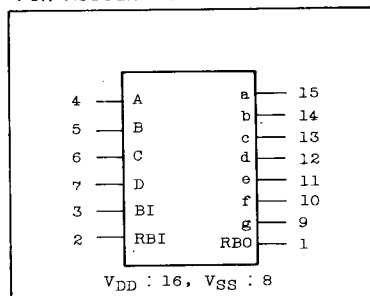
RBI input is to turn the output "OFF" and RBO input is to generate "H" level output only for "0" code input and these are used for leading zero suppress when connected in cascade.



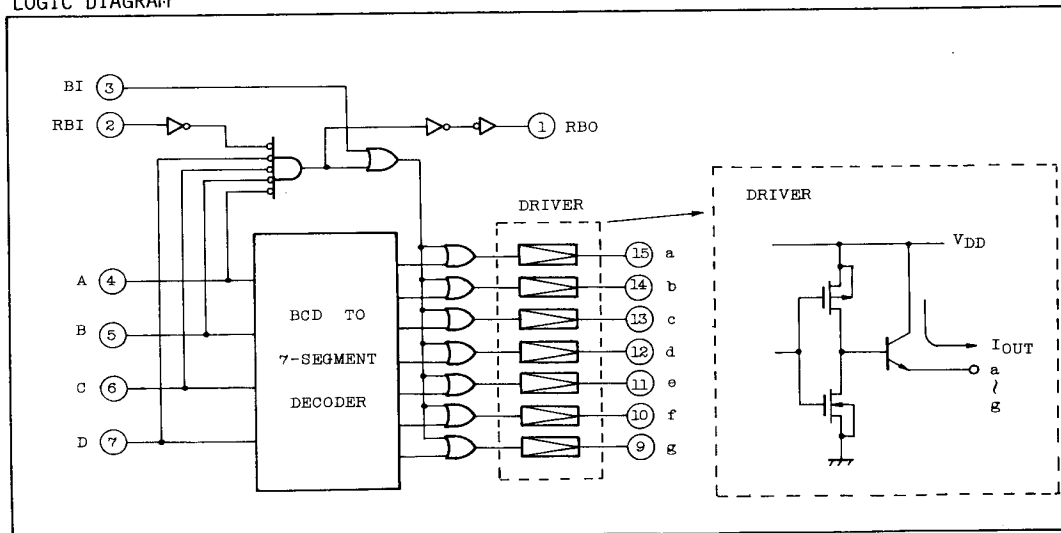
ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V _{DD}	V _{SS} -0.5 ~ V _{SS} +20	V
Input Voltage	V _{IN}	V _{SS} -0.5 ~ V _{DD} +0.5	V
Output Voltage	V _{OUT}	V _{SS} -0.5 ~ V _{DD} +0.5	V
DC Input Current	I _{IN}	±10	mA
Power Dissipation	P _D	300	mW
Storage Temperature Range	T _{stg}	-65 ~ 150	°C
Lead Temp./Time	T _{sol}	260°C · 10sec	

PIN ASSIGNMENT



LOGIC DIAGRAM



TC5002BP, TC5022BP

RECOMMENDED OPERATING CONDITIONS (VSS= 0V)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	VDD	3	-	18	V
Input Voltage	VIN	0	-	VDD	V
Operating Temp.	Topr	-40	-	85	°C

ELECTRICAL CHARACTERISTICS (VSS=0V)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	VDD (V)	-40°C		25°C			85°C		UNIT
				MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	
High Level Output Voltage (RBO)	VOH	IOUT < 1μA VIN=VSS, VDD	5	4.95	-	4.95	5.00	-	4.95	-	V
			10	9.95	-	9.95	10.00	-	9.95	-	
			15	14.95	-	14.95	15.00	-	14.95	-	
Low Level Output Voltage (RBO)	VOL	IOUT < 1μA VIN=VSS, VDD	5	-	0.05	-	0.00	0.05	-	0.05	V
			10	-	0.05	-	0.00	0.05	-	0.05	
			15	-	0.05	-	0.00	0.05	-	0.05	
High Level Output Voltage (a - g)	VOH	IOUT < 1μA VIN=VSS, VDD	5	4.0	-	4.0	4.5	-	4.0	-	V
			10	9.0	-	9.0	9.5	-	9.0	-	
			15	14.0	-	14.0	14.5	-	14.0	-	
High Level Output Current (RBO)	IOH	VOH=4.6V VOH=9.5V VOH=13.5V VIN=VSS, VDD	5	-0.2	-	-0.16	-	-	-0.12	-	mA
			10	-0.5	-	-0.4	-	-	-0.3	-	
			15	-1.4	-	-1.2	-	-	-1.0	-	
			5	0.52	-	0.44	-	-	0.36	-	
Low Level Output Current (RBO)	IOL	VOL=0.4V VOL=0.5V VOL=1.5V VIN=VSS, VDD	10	1.3	-	1.1	-	-	0.9	-	mA
			15	3.6	-	3.0	-	-	2.4	-	
			5	-20	-	-20	-	-	-15	-	
			10	-25	-	-25	-	-	-20	-	
High Level Output Current (a - g)	IOH	VOH=3.5V VOH=8.5V VOH=13.5V VIN=VSS, VDD	15	-30	-	-30	-	-	-25	-	mA
			5	3.5	-	3.5	2.75	-	3.5	-	
			10	7.0	-	7.0	5.5	-	7.0	-	
High Level Input Voltage	VIH **	VOUT=0.5V, 4.0V VOUT=1.0V, 9.0V VOUT=1.5V, 13.5V IOUT < 1μA	5	11.0	-	11.0	8.25	-	11.0	-	V
			10	7.0	-	7.0	5.5	-	7.0	-	
			15	11.0	-	11.0	8.25	-	11.0	-	
Low Level Input Voltage	VIL **	VOUT=0.5V, 4.0V VOUT=1.0V, 9.0V VOUT=1.5V, 13.5V IOUT < 1μA	5	-	1.5	-	2.25	1.5	-	1.5	V
			10	-	3.0	-	4.5	3.0	-	3.0	
			15	-	4.0	-	6.75	4.0	-	4.0	
Disable Current (a - g)	IDL	VOL=0V	18	-	-3.0	-	-10 ⁻⁴	-3.0	-	-30	μA
Input "H" Level Current	I _{IH}	V _{IH} =18V	18	-	0.3	-	10 ⁻⁵	0.3	-	1.0	μA
	I _{IL}	V _{IL} =0V	18	-	-0.3	-	-10 ⁻⁵	-0.3	-	-1.0	
Quiescent Current Consumption	IDD	VIN=VSS, VDD *	5	-	20	-	0.005	20	-	150	μA
			10	-	40	-	0.010	40	-	300	
			15	-	80	-	0.015	80	-	600	

* All valid input combinations. Outputs open.

** Required pull down register RL = 20 kΩ (a ~ g outputs).

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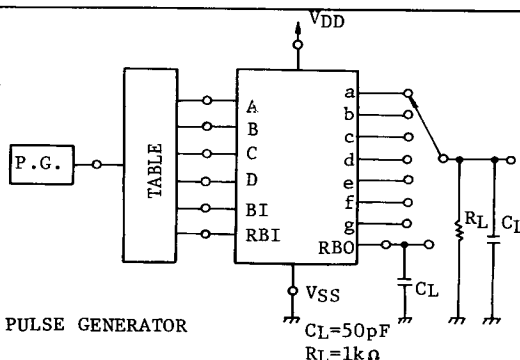
SWITCHING CHARACTERISTICS (Ta=25°C, VSS=0V, CL=50pF)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	VDD (V)	MIN.	TYP.	MAX.	UNIT
Output Rise Time (SEGMENT OUT)	tTLH	RL=1 kΩ	5	-	100	200	ns
			10	-	50	100	
			15	-	40	80	
Output Rise Time (RBO)	tTLH		5	-	130	400	
			10	-	65	200	
			15	-	50	160	
Output Fall Time (RBO)	tTHL		5	-	100	200	
			10	-	50	100	
			15	-	40	80	
(LOW-HIGH) Propagation Delay Time (A,B,C,D-SEGMENT OUT)	tpLH	RL=1 kΩ	5	-	500	1000	ns
			10	-	150	400	
			15	-	120	300	
(HIGH-LOW) Propagation Delay Time (A,B,C,D-SEGMENT OUT)	tpHL	RL=1 kΩ	5	-	1000	2000	
			10	-	450	1000	
			15	-	320	700	
(LOW-HIGH) Propagation Delay Time (A,B,C,D - RBO)	tpLH		5	-	1000	2000	ns
			10	-	370	1000	
			15	-	250	750	
(HIGH-LOW) Propagation Delay Time (A,B,C,D - RBO)	tpHL		5	-	500	1000	
			10	-	200	500	
			15	-	140	300	
(LOW-HIGH) Propagation Delay Time (RBI - RBO)	tpLH		5	-	800	1600	ns
			10	-	270	700	
			15	-	190	500	
(HIGH-LOW) Propagation Delay Time (RBI - RBO)	tpHL		5	-	180	700	
			10	-	70	350	
			15	-	50	250	
Propagation Delay Time (BI - SEGMENT OUT)	tpLH tpHL	RL=1 kΩ	5	-	500	1500	ns
			10	-	200	600	
			15	-	150	500	
Input Capacity	CIN			-	5	7.5	pF

SWITCHING TIME TEST CIRCUIT

TABLE (tpLH, tpHL Test Condition)

TEST	P.G.	"H"	"L"	OUTPUT	Wave-form
A,B,C,D - SEGMENT OUT	A	-	Other Inputs	a	1
A,B,C,D - RBO	A	RBI	Other Inputs	RBO	2
RBI - RBO	RBI	-	Other Inputs	RBO	3
BI - SEGMENT OUT	BI	A,B	Other Inputs	a	4



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SWITCHING TIME TEST WAVEFORMS

