www.DataSheet4U.cppSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

# SM1G43,SM1J43

AC POWER CONTROL APPLICATIONS

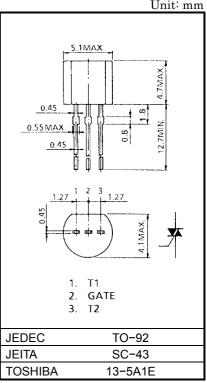
- Repetitive Peak Off-State Voltage : V<sub>DRM</sub> = 400, 600V
- R.M.S On–State Current

TOSHIBA

- : IT (RMS) = 1A
- Higt Commutating (dv / dt)

#### **MAXIMUM RATINGS**

CHARACTER	ISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage	SM1G43	\/·	400	V	
	SM1J43	VDRM	600	v	
R.M.S On-State Current (Full Sine Waveform Tc = 74°C)		I <sub>T (RMS)</sub>	1.0	А	
Peak One Cycle Surge On-State Current (Non-Repetitive)		le a c	8 (50Hz)	А	
		ITSM	8.8 (60Hz)	~	
I <sup>2</sup> t Limit Value		l <sup>2</sup> t	0.32	A <sup>2</sup> s	
Peak Gate Power Dissipation		P <sub>GM</sub>	1	W	
Average Gate Power Dissipation		P <sub>G (AV)</sub>	0.1	W	
Peak Gate Voltage		V <sub>GM</sub>	6	V	
Peak Gate Current		I <sub>GM</sub>	0.5	А	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature F	Range	T <sub>stg</sub>	-40~125	°C	



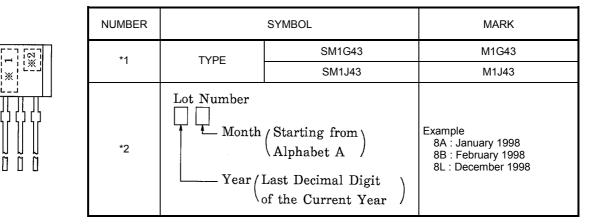
Weight: 0.2g

Unit: mm

#### www.DataShaet411.com ELECTRICAL CHARACTERISTICS (Ta = 25°C)

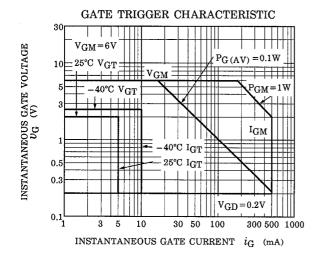
CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current		I <sub>DRM</sub>	V <sub>DRM</sub> = Rated		_	_	10	μA
Gate Trigger Voltage	Ι	V <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	T2 (+) , Gate (+)	-	_	2	V
	П			T2 (+) , Gate (−)	_	_	2	
	Ш			T2 (−) , Gate (−)	_	_	2	
	IV			T2 (-) , Gate (+)	_	2	_	
Gate Trigger Current	I	I <sub>GT</sub>	V <sub>D</sub> = 12V, R <sub>L</sub> = 20Ω	T2 (+) , Gate (+)		_	5	- mA
	П			T2 (+) , Gate (−)		_	5	
	Ш			T2 (−) , Gate (−)		_	5	
	IV			T2 (-) , Gate (+)		10	_	
Peak On-State Voltage		V <sub>TM</sub>	I <sub>TM</sub> = 1.5A		-	_	1.5	V
Gate Non-Trigger Voltage		V <sub>GD</sub>	V <sub>D</sub> = Rated, Tc = 125°C		0.2	_	_	V
Holding Current		Ι <sub>Η</sub>	V <sub>D</sub> = 12V, I <sub>TM</sub> = 1A		_	_	10	mA
Thermal Resistance		R <sub>th (j−c)</sub>	Junction to Case, AC		_	_	40	°C/W
Thermal Resistance		R <sub>th (j−a)</sub>	Junction to Ambient, AC		_	_	180	°C/W

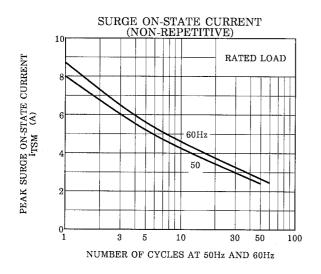
### MARKING

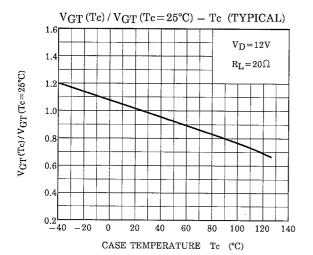


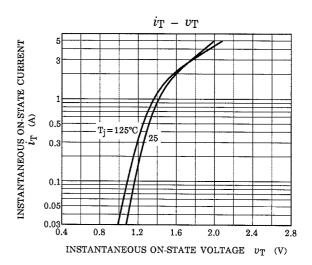
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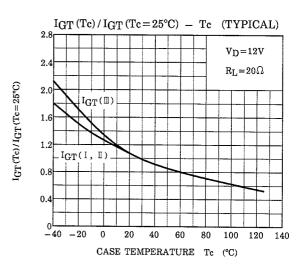
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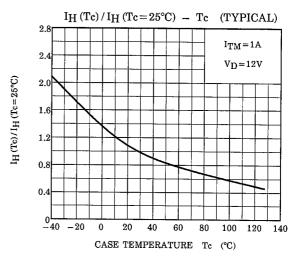






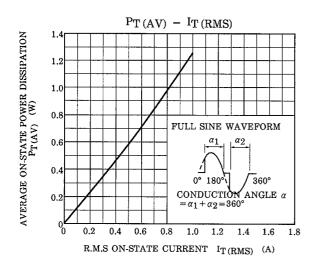


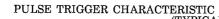


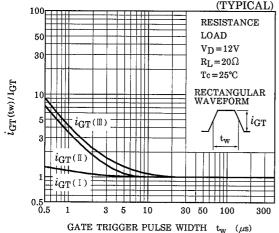


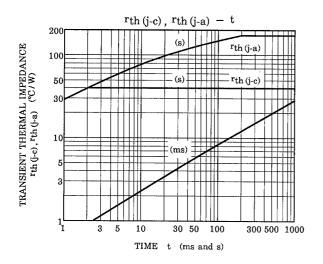
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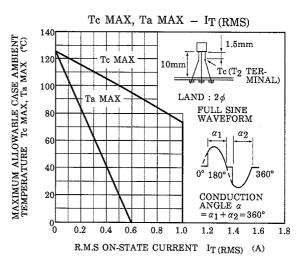
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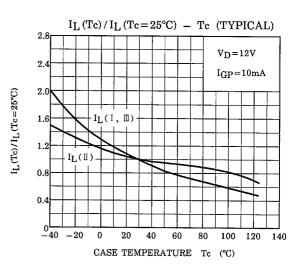














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