

UNISONIC TECHNOLOGIES CO., LTD

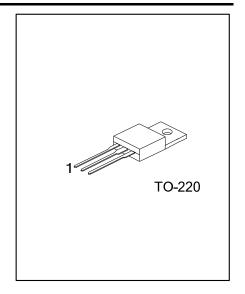
UF1404 Preliminary Power MOSFET

162A, 40V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **UF1404** is a N-channel enhancement power MOSFET using UTC's advanced technology to provide the customers with perfect $R_{\text{DS(ON)}}$ and high switching speed.

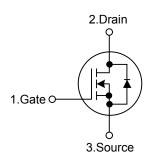
The UTC **UF1404** is suitable for all commercial-industrial applications at power dissipation levels to approximately 50 watts, etc.



■ FEATURES

- * $R_{DS(ON)}$ = 4m Ω @ V_{GS} =10V, I_D =95A
- * High Switching Speed

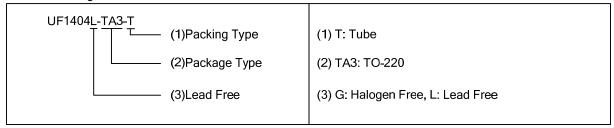
■ SYMBOL



■ ORDERING INFORMATION

| Ordering | Dookogo | Pin | Doolsing | | | | |
|---------------|---------------|---------|----------|---|---|---------|--|
| Lead Free | Halogen Free | Package | 1 | 2 | 3 | Packing | |
| UF1404L-TA3-T | UF1404G-TA3-T | TO-220 | G | D | S | Tube | |

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ **ABSOLUTE MAXIMUM RATINGS** (T_J=25°C, unless otherwise specified)

| PARAMETER | | SYMBOL | RATINGS | UNIT | | |
|---|-----------------------|-----------------------------------|----------------------|-----------------|--------------|----|
| Drain-Source Voltage | | V_{DSS} | 40 | V | | |
| Gate-Source Voltage | | V_{GSS} | ±20 | V | | |
| Drain Current | Continuous ()/ -10\/) | | T _C =25°C | l _D | 162 (Note 5) | Α |
| | Continu | Continuous (V _{GS} =10V) | | | 115 (Note 5) | Α |
| | Pulsed (| Note 2) | T _C =25°C | I _{DM} | 650 | Α |
| Avalanche Current (Note 2) | | I _{AR} | 95 | Α | | |
| Avalanche Energy Single Pulsed (Note 3) Repetitive (Note 2) | | E _{AS} | 519 | mJ | | |
| | | Repetitive (Note 2) | | E _{AR} | 20 | mJ |
| Peak Diode Recovery dv/dt (Note 3) | | dv/dt | 5.0 | V/ns | | |
| Power Dissipation (T _C =25°C) | | P_{D} | 200 | W | | |
| Junction Temperature | | T_J | +150 | °C | | |
| Storage Temperature | | T _{STG} | -55~+150 | °C | | |

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive rating: pulse width limited by maximum junction temperature
- 3. Starting T_J =25°C, L=0.12mH, R_G =25 Ω , I_{AS} =95A
- 4. $I_{SD} \le 95A$, di/dt $\le 150A/\mu s$, $V_{DD} \le BV_{DSS}$, $T_J \le 175$ °C
- 5. Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A

■ THERMAL CHARACTERISTICS

| PARAMETER | SYMBOL | RATINGS | UNIT | |
|---------------------|------------------|---------|------|--|
| Junction to Ambient | θ_{JA} | 62 | °C/W | |
| Junction to Case | $\theta_{ m JC}$ | 0.625 | °C/W | |

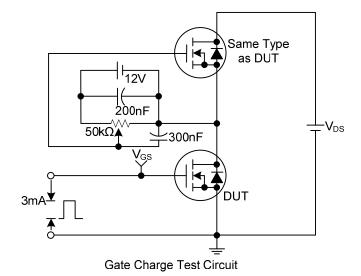
■ **ELECTRICAL CHARACTERISTICS** (T_J=25°C, unless otherwise specified)

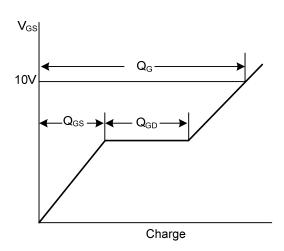
| PARAMETER | SYMBOL | TEST CONDITIONS | | TYP | MAX | UNIT | | | |
|--|---------------------|---|-----|-------|------|------|--|--|--|
| OFF CHARACTERISTICS | | | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =250μA | | | | V | | | |
| Breakdown Voltage Temperature Coefficien | | • | | 0.036 | | V/°C | | | |
| David On and Landau On and | , | V _{DS} =40V, V _{GS} =0V | | | 20 | μA | | | |
| Drain-Source Leakage Current | I _{DSS} | V _{DS} =32V, V _{GS} =0V, T _J =150°C | | | 250 | μA | | | |
| Coto Course Lockers Current Forward | | V _{GS} =+20V | | | +200 | nA | | | |
| Gate- Source Leakage Current Reverse | I _{GSS} | V _{GS} =-20V | | | -200 | nA | | | |
| ON CHARACTERISTICS | | | | | | | | | |
| Gate Threshold Voltage | $V_{GS(TH)}$ | $V_{DS}=V_{GS}$, $I_{D}=250\mu A$ | 2.0 | | 4.0 | V | | | |
| Static Drain-Source On-State Resistance | R _{DS(ON)} | V _{GS} =10V, I _D =95A (Note 2) | | 3.5 | 4 | mΩ | | | |
| DYNAMIC PARAMETERS | | | | | | | | | |
| Input Capacitance | C _{ISS} | | | 7.36 | | nF | | | |
| Output Capacitance | Coss | V _{GS} =0V, V _{DS} =25V, f=1.0MHz | | 1.68 | | nF | | | |
| Reverse Transfer Capacitance | C _{RSS} | | | 0.24 | | nF | | | |
| SWITCHING PARAMETERS | | | | | | | | | |
| Total Gate Charge | Q_{G} | I _D =95A, V _{DS} =32V, V _{GS} =10V (Note 2) | | 160 | 200 | nC | | | |
| Gate to Source Charge | Q_{GS} | | | 35 | | nC | | | |
| Gate to Drain Charge | Q_{GD} | (Note 2) | | 42 | 60 | nC | | | |
| Turn-ON Delay Time | t _{D(ON)} | | | 17 | | ns | | | |
| Rise Time | t _R | V_{DD} =20V, I_{D} =95A, R_{G} =2.5 Ω , | | 140 | | ns | | | |
| Turn-OFF Delay Time | t _{D(OFF)} | R _D =0.21Ω (Note 2) | | 72 | | ns | | | |
| Fall-Time | t _F | | | 26 | | ns | | | |
| SOURCE- DRAIN DIODE RATINGS AND O | HARACTERI | | | | | | | | |
| Internal Drain Inductance | L _D | Between lead, 6 mm |) | 4.5 | | nΗ | | | |
| Internal Source Inductance | L _S | (0.25in.) from package and center of die contact |) | 7.5 | | nΗ | | | |
| Maximum Body-Diode Continuous Current (Note 4) | I _S | MOSFET symbol showing | | | 162 | Α | | | |
| Maximum Body-Diode Pulsed Current (Note 1) | I _{SM} | the integral reverse p-n g G | 3 | | 650 | Α | | | |
| Drain-Source Diode Forward Voltage | V _{SD} | I _S =95A, V _{GS} =0V, T _J =25°C (Note 2) | | | 1.3 | ٧ | | | |
| Body Diode Reverse Recovery Time | t _{RR} | I _F =95A, di/dt=100A/μs, | | 71 | 110 | ns | | | |
| Body Diode Reverse Recovery Charge | Q _{RR} | T _J =25°C (Note 2) | | 180 | 270 | μC | | | |

Notes: 1. Repetitive rating: pulse width limited by maximum junction temperature

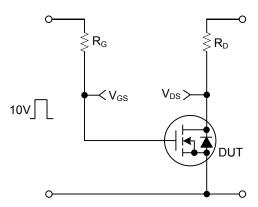
- 2. Pulse width≤300µs, Duty cycle≤2%
- 3. C_{OSS} eff. is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 80% V_{DSS}
- 4. Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A

TEST CIRCUITS AND WAVEFORMS

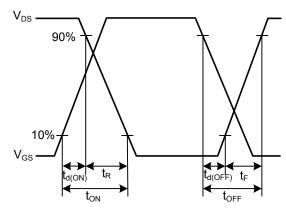




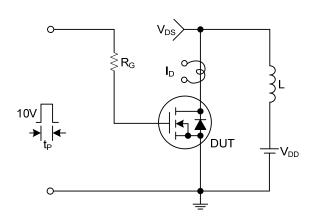
Gate Charge Waveforms



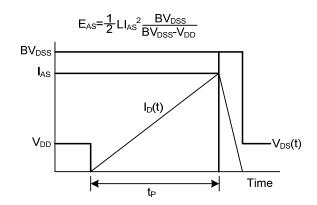
Resistive Switching Test Circuit



Resistive Switching Waveforms

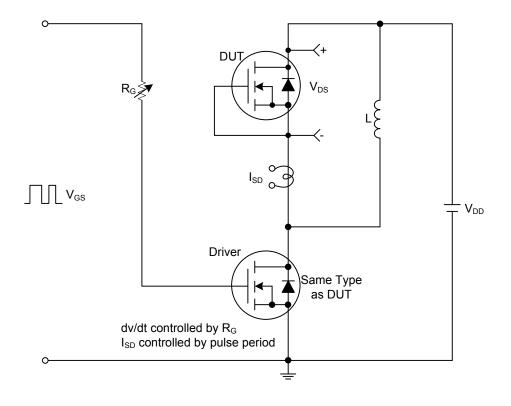


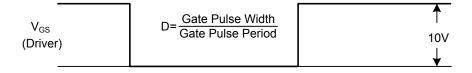
Unclamped Inductive Switching Test Circuit

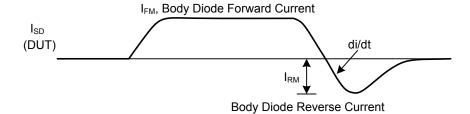


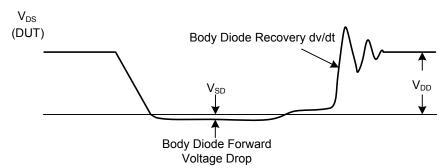
Unclamped Inductive Switching Waveforms

■ TEST CIRCUITS AND WAVEFORMS(Cont.)









Peak Diode Recovery dv/dt Test Circuit and Waveforms

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