

Description

The SE2148 positive, linear regulators feature low quiescent current (45µA typ.) with low dropout voltage, making them ideal for battery applications. These rugged devices have both Thermal Shutdown and Current Fold-back to prevent device failure under the "Worst" of operating conditions. The SE2148 is stable with an output capacitance of 4.7µF or greater.

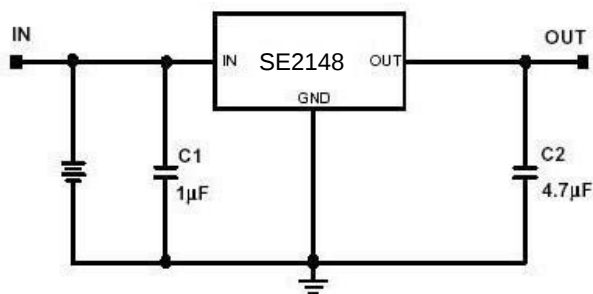
Features

- * Low Temperature Coefficient
- * Over-Temperature Shutdown
- * Very Low Dropout Voltage
- * High Accurate $\pm 1.5\%$
- * Short Circuit Current Fold-back
- * Guaranteed 1.5A output
- * Current Limiting

Applications

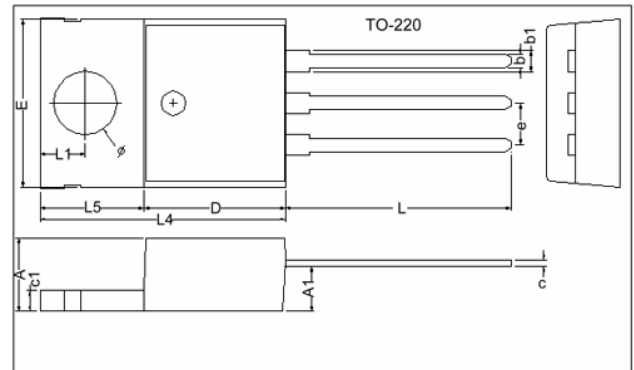
- * PC Peripherals
- * Wireless Devices
- * Portable Electronics
- * Battery Powered Widgets
- * Instrumentation

Typical Circuit

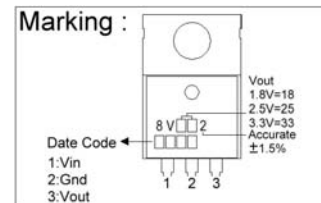


Ordering Information(contd.)

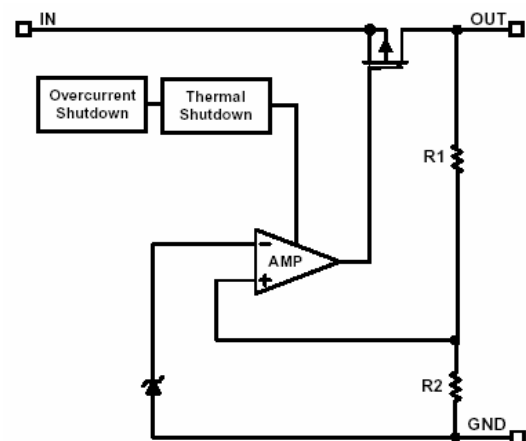
Part Number	Marking	Output Voltage	Part Number	Marking	Output Voltage
SE2148-15	8V152 XXXX	1.5V	SE2148-18	8V182 XXXX	1.8V
SE2148-19	8V192 XXXX	1.9V	SE2148-25	8V252 XXXX	2.5V
SE2148-33	8V332 XXXX	3.3V	SE2148-47	8V472 XXXX	4.75V
SE2148-50	8V502 XXXX	5.0V			



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.40	4.80	c1	1.25	1.45
b	0.76	1.00	b1	1.17	1.47
c	0.36	0.50	L	13.25	14.25
D	8.60	9.00	e	2.54 REF.	
E	9.80	10.4	L1	2.60	2.89
L4	14.7	15.3	Ø	3.71	3.96
L5	6.20	6.60	A1	2.60	2.80



Functional Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Input Max Voltage	V _{IN}	8	V
Output Current	I _{OUT}	P _D /(V _{IN} - V _O)	A
Output Voltage	V _{OUT}	1.5~5.0	V
Operating Ambient Temperature	T _{opr}	-40 ~ +85	°C
Junction Temperature	T _j	-40 ~ +125	°C
Maximum Junction Temperature	T _j Max	150	°C
Thermal Resistance	θ _{jc} (Conductive Epoxy)*	6	°C/W
	θ _{ja}	55	°C/W
Internal Power Dissipation	PD	2.2	W
EDS Classification		B	

*Measure θ_{jc} on backside center of tab.

Electrical Characteristics V_{IN}=V_{OUT}(T)+2V, T_a=25°C unless otherwise noted

Parameter	Symbol	Condition	Min	TYP	Max	Unit
Output Voltage	V _{OUT} (E) (Note1)	I _O =1mA	-1.5%	V _{OUT} (T) (Note2)	1.5%	V
Output Current	I _O	V _O >1.2V	1.5	-	-	A
Current Limit	I _{LIM}	V _O >1.2V	1.5	2.0	-	A
Load Regulation	REG _{LOAD}	V _{IN} =V _{OUT} (T)+2V, I _O =1mA to 1.5A	-1	0.2	1	%
Dropout Voltage	V _{DROPOUT}	I _O =1.5A V _O =V _{OUT} (E)-2%	1.4V<V _{OUT} (T)≤2.0V	-	1300	mV
			2.0V<V _{OUT} (T)≤2.8V	-	800	
			2.8V<V _{OUT} (T)	-	600	
Quiescent Current	I _Q	V _{IN} =V _{OUT} (T)+2V, I _O =0mA	-	45	70	μA
Ground Pin Current	I _{GND}	V _{IN} =V _{OUT} (T)+2V, I _O =1mA~1.5A	-	45	-	μA
Line Regulation	REG _{LINE}	I _O =1mA V _{IN} =V _{OUT} V _{OUT} (T)+2	V _{OUT} (T)<2.0V	-0.15	0.15	%
			2.0V≤V _{OUT} (T)<4.0V	-0.1	0.02	
			4.0V≤V _{OUT} (T)	-0.4	0.4	
Input Voltage	V _{IN}		Note3	-	7	V
Over Temperature Shutdown	OTS		-	150	-	°C
Over Temperature Hysteresis	OTH		-	30	-	°C
Output Voltage Temperature Coefficient	TC		-	30	-	ppm/°C
Short Circuit Current	I _{SC}	V _{IN} =V _{OUT} (T)+1V, V _{OUT} <0.4V	-	750	-	mA
Power Supply Rejection	PSRR	I _O =100mA C _O =4.7μF	f=100Hz	-	70	dB
			f=1kHz	-	50	
			f=10kHz	-	20	
Output Voltage Noise	e _N	f=10Hz~100kHz, I _O =10mA, C _O =4.7μF	-	30	-	μV _{rms}

Note 1: V_{OUT}(E) =Effective Output Voltage (i.e. the output voltage when "V_{OUT}(T) + 2.0V" is provided at the V_{IN} pin while maintaining a certain I_{OUT} value).

2: V_{OUT}(T) =Specified Output Voltage

3: V_{IN}(MIN) =V_{OUT}+V_{DROPOUT}

Detailed Description

The SE2148 of COMS regulators contain a PMOS pass transistor, voltage reference, error amplifier, over-current protection and thermal shutdown. The P-channel pass transistor receives data from the error amplifier, over-current shutdown, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. Over-current and Thermal shutdown circuits become active when the junction temperature exceeds 150°C , or the current exceeds 2.2A. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops below 120°C . The SE2148 behaves like a current source when the load reaches 2.2A. However, if the load impedance drops below 0.3Ω , the current drops back to 600mA to prevent excessive power dissipation. Normal operation is restored when the load resistance exceeds of 0.75Ω .

External Capacitors

The SE2148 is stable with an output capacitance to ground of 4.7uF or greater. Ceramic capacitors have the lowest ESR, and will offer the best AC performance. Conversely, Aluminum Electrolytic capacitors exhibit the highest ESR, resulting in the poorest AC response. Unfortunately, large value ceramic capacitors are comparatively expensive. One option is to parallel a 0.1uF ceramic capacitor with a 10uF Aluminum Electrolytic. The benefit is low ESR, high capacitance, and low overall cost. A second capacitor is recommended between the input and ground to stabilize V_{IN} . The input capacitor should be at least 0.1uF to have a beneficial effect. All capacitors should be placed in closed proximity to the pins. A "Quiet" ground termination is desirable. This can be achieved with a "Star" connection.

Characteristics Curve

