2SD1330

Silicon NPN epitaxial planer type

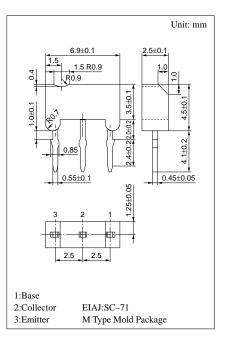
For low-voltage output amplification For muting For DC-DC converter

Features

- Low collector to emitter saturation voltage V_{CE(sat)}.
- Low ON resistance Ron.
- High foward current transfer ratio h_{FE} .
- M type package allowing easy automatic and manual insertion as well as stand-alone fixing to the printed circuit board.

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	25	V
Collector to emitter voltage	V _{CEO}	20	V
Emitter to base voltage	V _{EBO}	12	V
Peak collector current	I _{CP}	1	А
Collector current	I _C	0.5	А
Collector power dissipation	P _C	600	mW
Junction temperature	Tj	150	°C
Storage temperature	T _{stg}	-55 ~ +150	°C

Absolute Maximum Ratings (Ta=25°C)

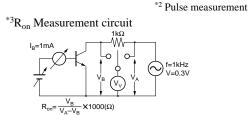


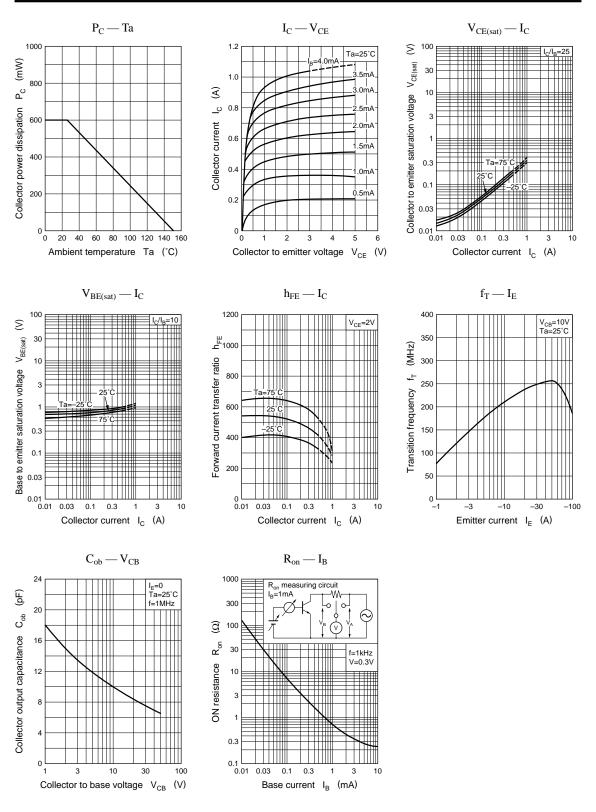
Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = 25V, I_C = 0$			100	nA
Collector to base voltage	V _{CBO}	$I_C = 10\mu A, I_E = 0$	25			v
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 1$ mA, $I_{\rm B} = 0$	20			V
Emitter to base voltage	V _{EBO}	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	12			v
Forward current transfer ratio	h _{FE1} *1	$V_{CE} = 2V, I_C = 0.5A^{*2}$	200		800	
	h _{FE2}	$V_{CE} = 2V, I_C = 1A^{*2}$	60			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 0.5 {\rm A}, I_{\rm B} = 20 {\rm mA}$		0.13	0.4	V
Base to emitter saturation voltage	V _{BE(sat)}	$I_{\rm C} = 0.5 A, I_{\rm B} = 50 m A$			1.2	v
Transition frequency	f _T	$V_{CB} = 10V, I_E = -50mA, f = 200MHz$		200		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10V, I_E = 0, f = 1MHz$		10		pF
ON resistanse	R _{on} *3			1.0		Ω

*1hFE1 Rank classification

Rank	R	S	Т
h _{FE1}	200 ~ 350	300 ~ 500	400 ~ 800





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