

UNISONIC TECHNOLOGIES CO., LTD

4N70

4.4A, 700V N-CHANNEL **POWER MOSFET**

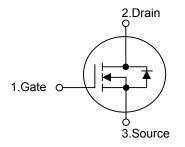
DESCRIPTION

The UTC 4N70 is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche. This high speed switching power MOSFET is usually used in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)}$ = 2.8 Ω @V_{GS} = 10 V
- * Ultra Low Gate Charge (Typical 15nC)
- * Low Reverse Transfer Capacitance (C_{RSS} = Typical 8.0 pF)
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness

SYMBOL



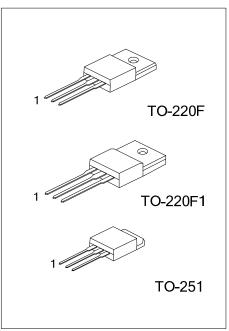
ORDERING INFORMATION

Ordering Number		Daakaga	Pin Assignment			Dooking		
Lead Free	Halogen Free	Package	1	2	3	Packing		
4N70L-TF1-T	4N70G-TF1-T	TO-220F1	G	D	S	Tube		
4N70L-TF3-T	4N70G-TF3-T	TO-220F	G	D	S	Tube		
4N70L-TM3-T 4N70G-TM3-T		TO-251	G	D	S	Tube		
Nate: Din Assignment: C: Cate D: Drain C: Source								

Note: Pin Assignment: G: Gate D: Drain S: Source

	ing Type age Type Free (3) G: Halogen Free, L: Lead Free	
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Power MOSFET



PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	700	V
Gate-Source Voltage		V _{GSS}	±30	V
Avalanche Current (Note 2)		I _{AR}	4.4	А
Daraia Quanta at	Continuous	Ι _D	4.4	А
Drain Current	Pulsed (Note 2)	I _{DM}	17.6	А
	Single Pulsed (Note 3)	E _{AS}	260	mJ
Avalanche Energy	Repetitive (Note 2)	E _{AR}	10.6	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Dower Dissignation	TO-220F/TO-220F1	D	36	10/
Power Dissipation	TO-251	PD	49	W
Junction Temperature		TJ	+150	°C
Operating Temperature		T _{OPR}	-55 ~ +150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

■ ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

- Absolute maximum ratings are stress ratings only and functional device operation is not implied.
- 2. Repetitive Rating : Pulse width limited by maximum junction temperature
- 3. L = 26.9mH, I_{AS} = 4.4A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C 4. I_{SD} 4.4A, di/dt ≤200A/µs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220F/TO-220F1	0	62.5	°C/M	
	TO-251	θ _{JA}	110	°C/W	
Junction to Case	TO-220F/TO-220F1	0	3.47	°C 1.1	
	TO-251	θ _{Jc}	2.55	°C/W	

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	ΜΑΧ	UNIT	
PARAMETER SYMBOL TEST CONDITIONS MIN TYP MAX UNIT OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0 V, I _D = 250 µA	700			V	
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 700 V, V _{GS} = 0 V			10	μA	
	Forward	- I _{GSS}	V _{GS} = 30 V, V _{DS} = 0 V			100		
Gate-Source Leakage Current	Reverse		V _{GS} = -30 V, V _{DS} = 0 V			-100	nA	
Breakdown Voltage Temperature	Coefficient	$\bigtriangleup BV_{DSS} / \bigtriangleup T_J$	$I_D = 250 \mu A$, Referenced to $25^{\circ}C$		0.6		V/°C	
ON CHARACTERISTICS								
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250 μA	2.0		4.0	V	
Static Drain-Source On-State Res	sistance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 2.2 A		2.6	2.8	Ω	
DYNAMIC CHARACTERISTICS							-	
Input Capacitance		CISS	V _{DS} = 25 V, V _{GS} = 0 V,		520	670	рF	
Output Capacitance		C _{OSS}	f = 1MHz		70	90	рF	
Reverse Transfer Capacitance		C _{RSS}			8	11	рF	
SWITCHING CHARACTERISTICS								
Turn-On Delay Time		t _{D(ON)}			13	35	ns	
Turn-On Rise Time		t _R	$V_{DD} = 350V, I_D = 4.4A, R_G = 25\Omega$ (Note 1, 2)		45	100	ns	
Turn-Off Delay Time		t _{D(OFF)}			25	60	ns	
Turn-Off Fall Time		t _F			35	80	ns	
Total Gate Charge		Q_G	V _{DS} = 560V, I _D = 4.4A, V _{GS} = 10 V (Note 1, 2)		15	20	nC	
Gate-Source Charge		Q_{GS}			3.4		nC	
Gate-Drain Charge		Q_{GD}			7.1		nC	



■ ELECTRICAL CHARACTERISTICS(Cont.)

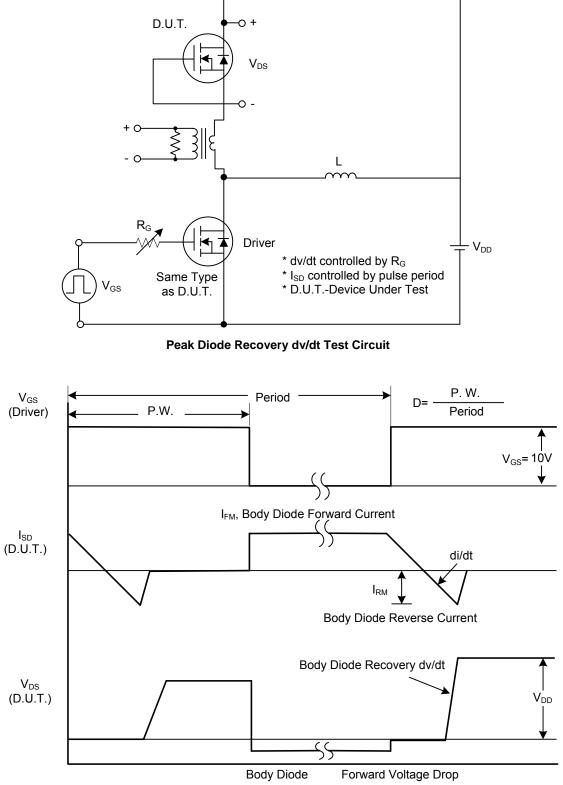
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Drain-Source Diode Forward Voltage	V_{SD}	V_{GS} = 0 V, I _S = 4.4 A			1.4	V		
Maximum Continuous Drain-Source Diode Forward Current	I _S				4.4	А		
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}				17.6	А		
Reverse Recovery Time	t _{rr}	$V_{GS} = 0 V, I_S = 4.4 A,$		250		ns		
Reverse Recovery Charge	Q _{RR}	dl/dt = 100 A/µs (Note 1)		1.5		μC		

Notes: 1. Pulse Test: Pulse width≤300µs, Duty cycle≤2%

2. Essentially independent of operating temperature



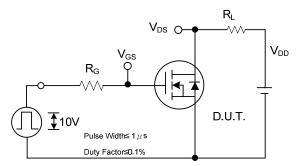
■ TEST CIRCUITS AND WAVEFORMS



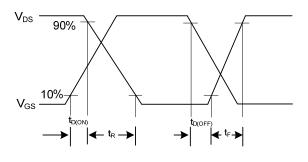
Peak Diode Recovery dv/dt Waveforms



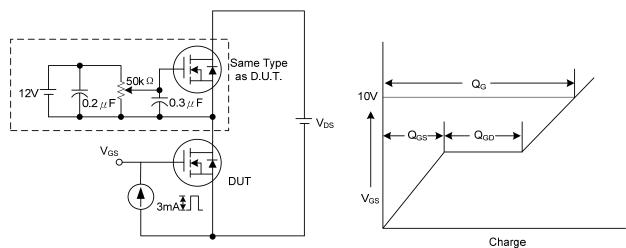
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



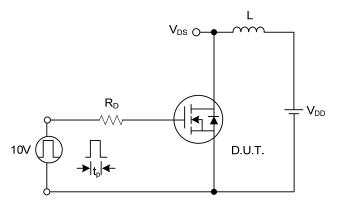
Switching Test Circuit



Switching Waveforms

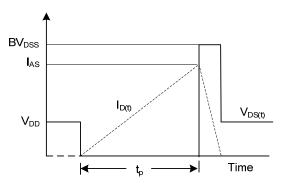


Gate Charge Test Circuit



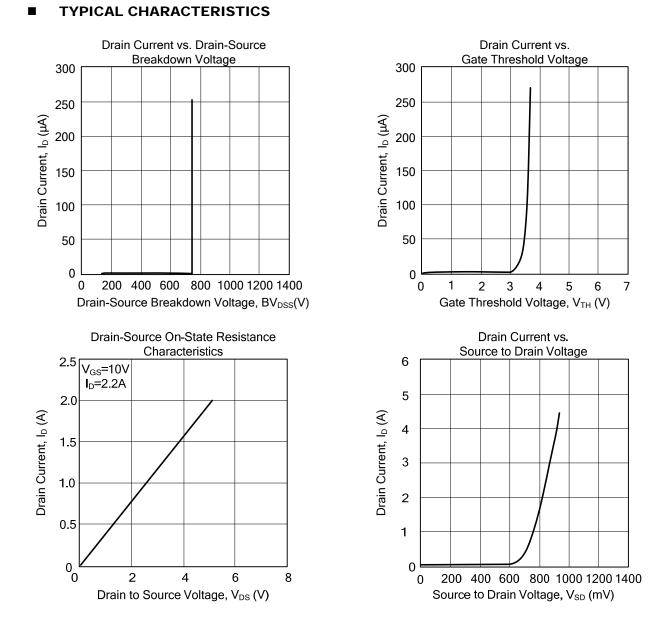
Unclamped Inductive Switching Test Circuit





Unclamped Inductive Switching Waveforms





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