

# **Small Signal Switching Diode**

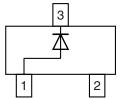
#### **Features**

- Silicon Epitaxial Planar Diodes
- Fast switching diode in case SOT-23, especially suited for automatic insertion.
- This diodes are also available in other case styles including: the DO-35 case with the type designation 1N4148, the MiniMELF case with the type designation LL4148, and the SOD-123 case with the type designation 1N4148W-V.



 Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC





### **Mechanical Data**

Case: SOT-23

Weight: approx. 8.8 mg
Packaging Codes/Options:

GS18 / 10 k per 13" reel (8 mm tape), 10 k/box GS08 / 3 k per 7" reel (8 mm tape), 15 k/box

#### **Parts Table**

Part	Ordering code	Marking	Remarks	
IMBD4148-V	IMBD4148-V-GS18 or IMBD4148-V-GS08	A2	Tape and Reel	

#### **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit	
Reverse voltage		$V_{R}$	75	V	
Peak reverse voltage		V <sub>RM</sub>	100	V	
Rectified current (average) half wave rectification with resist.	≥ f ≥ 50 Hz	I <sub>F(AV)</sub>	150 <sup>1)</sup>	mA	
Surge forward current	t < 1 s, T <sub>j</sub> = 25 °C	I <sub>FSM</sub>	500	mA	
Power dissipation	up to T <sub>amb</sub> = 25 °C	P <sub>tot</sub>	350 <sup>1)</sup>	mW	

<sup>1)</sup> Device on fiberglass substrate, see layout (SOT-23).



### **Thermal Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Thermal resistance junction to ambient air		$R_{thJA}$	450 <sup>1)</sup>	°C/W
Junction temperature		T <sub>j</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	- 65 to + 150	°C

<sup>1)</sup> Device on fiberglass substrate, see layout (SOT-23).

### **Electrical Characteristics**

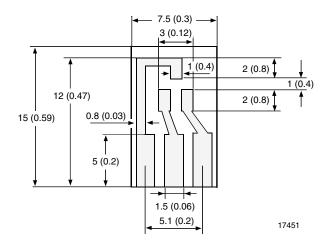
T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min.	Тур.	Max.	Unit
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>			1.0	V
Leakage current	V <sub>R</sub> = 70 V	I <sub>R</sub>			2.5	μΑ
	V <sub>R</sub> = 70 V, T <sub>j</sub> = 150 °C	I <sub>R</sub>			50	μΑ
	V <sub>R</sub> = 25 V, T <sub>j</sub> = 150 °C	I <sub>R</sub>			30	μΑ
Diode capacitance	$V_F = V_R = 0$	C <sub>D</sub>			4	pF
Reverse recovery time (see figures)	$I_F = 10 \text{ mA}, I_R = 10 \text{ mA},$ $V_R = 6 \text{ V}, R_L = 100 \Omega$	t <sub>rr</sub>			4	ns

# Layout for $R_{thJA}$ test

Thickness:

Fiberglass 1.5 mm (0.059 in.) Copper leads 0.3 mm (0.012 in.)



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### **Typical Characteristics**

### T<sub>amb</sub> = 25 °C unless otherwise specified

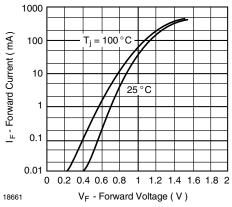


Figure 1. Forward Current vs. Forward Voltage

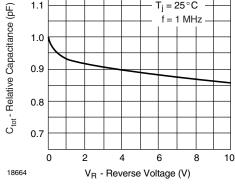


Figure 4. Relative Capacitance vs. Reverse Voltage

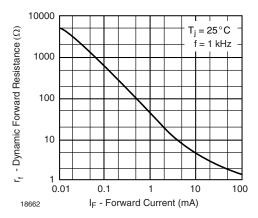


Figure 2. Dynamic Forward Resistance vs. Forward Current

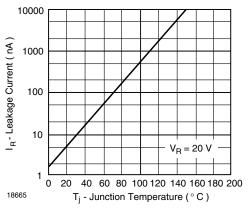


Figure 5. Leakage Current vs. Junction Temperature

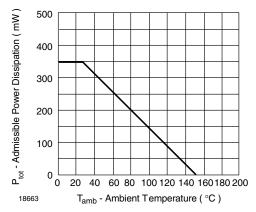


Figure 3. Admissible Power Dissipation vs. Ambient Temperature



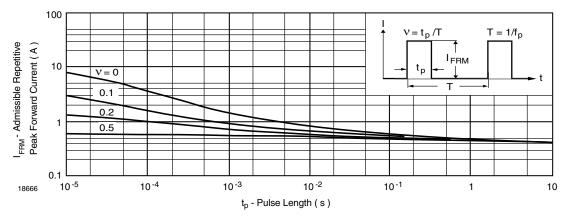
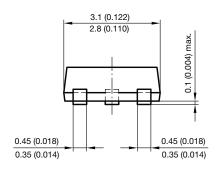
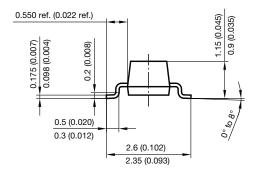
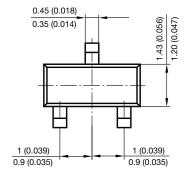


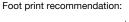
Figure 6. Admissible Repetitive Peak Forward Current vs. Pulse Duration

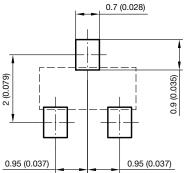
### Package Dimensions in millimeters (inches): SOT-23











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17418



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