

Solid State Devices, Inc.

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Designer's Data Sheet

Part Number/Ordering Information ^{1/} SFS23

Screening 2/ = Not Screened TX = TX Level TXV = TXV S = S Level

Voltage/Family

23 = 50V 27 = 250V 24 =100V 28 = 300V 26 = 200V 29 = 400V

SFS2323 thru SFS2329

1.6 AMP
SILICON CONTROLLED
RECTIFIER
50 – 400 VOLTS

FEATURES:

- Low-Level Gate Characteristics
- $I_{GT} = 200 \,\mu\text{A} \,(\text{Max}) \,@ \,25^{\circ}\text{C}$
- Low Holding Current I^H = 1 mA (Max) @ 25°C
- Anode Common to Case
- Hermetically Sealed

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MAXIMUM RATINGS		Symbol	Value	Units	
Peak Repetitive Reverse Voltage and DC Blocking Voltage	SFS2323 SFS2324 SFS2326 SFS2327 SFS2328 SFS2329	$oldsymbol{V}_{DRM}$	50 100 200 250 300 400	Volts	
Non-Repetitive Peak Reverse Blocking Voltage (t < 5.0 ms)	SFS2323 SFS2324 SFS2326 SFS2327 SFS2328 SFS2329	V_{RSM}	75 150 300 350 400 500	Volts	
RMS On-State Current (All Conduction Angles)		I _{T (RMS)}	1.6	Amps	
Peak Non-Repetitive Surge Current (One Cycle, 60 Hz, $T_c = 80^{\circ}C$)		I _{TSM}	15	Amps	
Peak Gate Power		P _{GM}	0.1	Watts	
Average Gate Power		P _{G (AV)}	0.01	Watts	
Peak Gate Current		I _{GM}	0.1	Amps	
Peak Gate Voltage		V_{GM}	6.0	Volts	
Operating Junction Temperature Range		TJ	-65 to +125	°C	
Storage Temperature Range		T_{stg}	-65 to +150	°C	
Thermal Resistance, Junction to Case		$R_{ heta JC}$	30	°C/W	

NOTES:

- 1/ For ordering information, price, operating curves, and availability- Contact factory.
- 2/ Screening based on MIL-PRF-19500. Screening flows available on request.
- 3/ Unless otherwise specified, all electrical characteristics @25°C.

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NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: SCR004C

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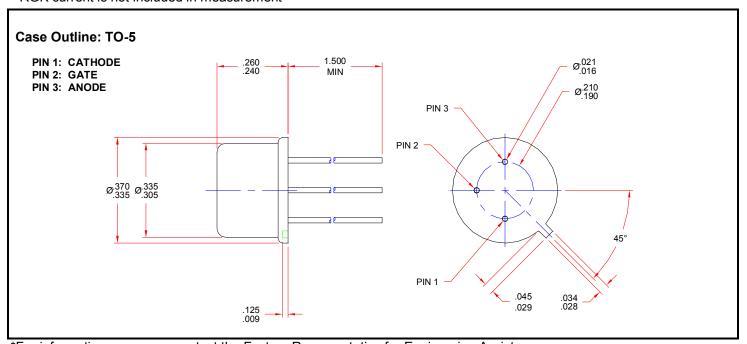
SFS2323 thru SFS2329

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ELECTRICAL CHARACTERISTICS	Symbol	Min	Typical	Max	Unit
Peak Reverse Blocking Current (Rated V _{RRM} , T _C = 25°C) (Rated V _{RRM} , T _C = 125°C)	I _{RRM}	_	0.12 0.1	1 100	μΑ
Peak Forward Blocking Current (Rated V _{RRM} , T _C = 25°C) (Rated V _{RRM} , T _C = 125°C)	I _{DRM}	_	0.14 1.0	1 100	μА
Peak On-State Voltage (I _F = 1.6 A Peak)	V _{TM}	_	1.1	1.3	Volts
Gate Trigger Current (V _D = 6 V _{DC} , R _L = 100 Ω, T _C = 25°C) (V _D = 6 V _{DC} , R _L = 100 Ω, T _C = -65°C)	I _{GT}	_	3.5 9.2	200 350	μА
Gate Trigger Voltage $(V_D = 6 \ V_{DC}, \ R_L = 100 \ \Omega, \ T_C = 25^{\circ}C)$ $(V_D = 6 \ V_{DC}, \ R_L = 100 \ \Omega, \ T_C = -65^{\circ}C)$ $(V_D = 6 \ V_{DC}, \ R_L = 100 \ \Omega, \ T_C = 125^{\circ}C)$	V _{GT}	— 0.1	0.51 0.74 0.25	0.7 0.9 0.9	Volts
Holding Current ($V_D = 6 V_{DC}, R_L = 100 \Omega, T_C = 25^{\circ}C$) ($V_D = 6 V_{DC}, R_L = 100 \Omega, T_C = -65^{\circ}C$) ($V_D = 6 V_{DC}, R_L = 100 \Omega, T_C = 125^{\circ}C$)	I _H	0.8 1.5 0.15	1.0 1.8 0.46	2.0 3.0 —	mA

NOTES:

^{*} RGK current is not included in measurement



^{*}For information on curves, contact the Factory Representative for Engineering Assistance.