

# 2SC5343U

**NPN Silicon Transistor** 

## **Description**

• General small signal amplifier

#### **Features**

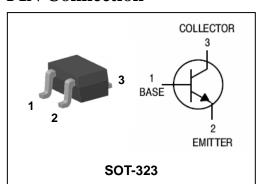
- Low collector saturation voltage : V<sub>CE</sub>=0.25V(Max.)
- Low output capacitance : C<sub>ob</sub>=2pF(Typ.)
- Complementary pair with 2SA1980U

## **Ordering Information**

Type NO.	Marking	Package Code
2SC5343U	<u>D</u> <u>□</u> <u>□</u> <u>0</u> <u>0</u> <u>3</u>	SOT-323

1Device Code 2hFE Rank 3Year&Week Code

#### **PIN Connection**



**Absolute maximum ratings** 

Ta=25°C

Characteristic	Symbol	Ratings	Unit	
Collector-Base voltage	$V_{CBO}$	60	V	
Collector-Emitter voltage	$V_{\sf CEO}$	50	V	
Emitter-Base voltage	$V_{EBO}$	5	V	
Collector current	I <sub>C</sub>	150	mA	
Collector dissipation	$P_{C(J-A)}$	200	mW	
Junction temperature	T <sub>j</sub>	150	°C	
Storage temperature	T <sub>stg</sub>	-55~150	°C	

Characteristic		Symbol	Тур.	Max	Unit
Thermal resistance	Junction-ambient	$R_{th(J-A)}$	-	625.0	°C/W

## **Electrical Characteristics**

Ta=25°C

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_C = 100 \mu A, I_E = 0$	60	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C=1$ mA, $I_B=0$	50	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	$I_E = 10 \mu A, I_C = 0$	5	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 60V, I_{E} = 0$	-	-	0.1	μΑ
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = 5V$ , $I_{C} = 0$	-	-	0.1	μА
DC current gain	h <sub>FE</sub> *	$V_{CE}=6V$ , $I_{C}=2mA$	70	-	700	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =10mA	-	-	0.25	V
Transistion frequency	f <sub>T</sub>	$V_{CE}=10V$ , $I_{C}=1mA$	80	-	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10V, I_{E} = 0, f = 1MHz$	-	2	3.5	рF
Noise figure	NF	$V_{CE}$ =6V, $I_{C}$ =0.1mA, f=1KHz, Rg=10K $\Omega$	-	-	10	dB

<sup>\* :</sup>  $h_{FE}$  rank / O : 70 ~ 140, Y : 120 ~ 240, G : 200 ~ 400, L : 300 ~ 700

KSD-T5D023-002

### **Electrical Characteristic Curves**

Fig. 1  $P_C - T_a$ 

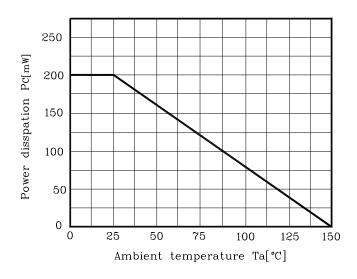


Fig. 2  $I_C$  - $V_{BE}$ 

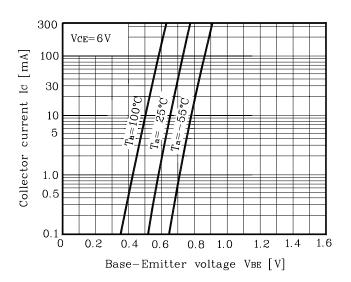


Fig. 3  $I_C$  - $V_{CE}$ 

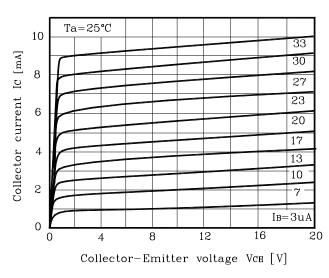


Fig. 4  $h_{FE}$ - $I_C$ 

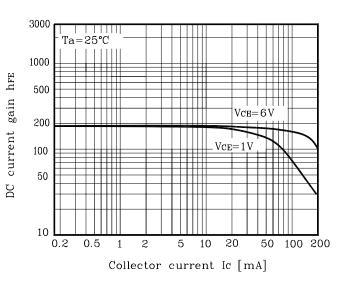
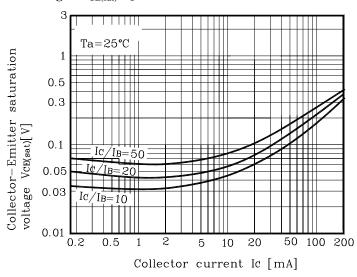


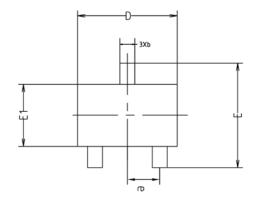
Fig. 5  $V_{\text{CE}(\text{sat})}$  -I  $_{\text{C}}$ 

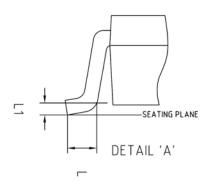


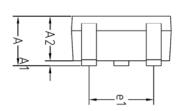
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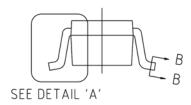
2

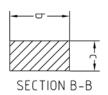
## **Outline Dimension**





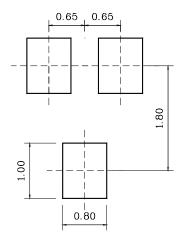






SYMBOL	1	NOTE		
STRIBOL	MINIMUM	NOMINAL	MAXIMUM	NUTE
Α	0.90	-	1.25	
A1	0.00	-	0.10	
A2	0.85	0.90	0.95	
Ь	0.30	-	0.40	
С	0.10	-	0.25	
D	1.90	2.00	2.10	
E	1.95	2.10	2.25	
E1	1.15	1.25	1.35	
е	0.65BSC			
e1	1.20	-	1.40	
L	0.10	-	-	
L1		0.12BS	C	

## \*Recommend PCB solder land [Unit: mm]



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KSD-T5D023-002