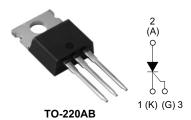


Vishay High Power Products

Phase Control SCR, 10 A



PRODUCT SUMMARY			
V _T at 10 A	< 1.4 V		
I _{TSM}	200 A		
V_{RRM}	800/1200 V		

DESCRIPTION/FEATURES

temperature.

The 16TTS..PbF High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level and lead (Pb)-free ("PbF" suffix).

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS		
Capacitive input filter T _A = 55 °C, T _J = 125 °C, common heatsink of 1 °C/W	13.5	17	А		

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	10	٨		
I _{RMS}		16	Α		
V _{DRM} /V _{RRM}	Range (1)	800/1200	V		
I _{TSM}		200	A		
V _T	10 A, T _J = 25 °C	1.4	V		
dV/dt		500	V/μs		
dl/dt		150	A/μs		
T _J	Range	- 40 to 125	°C		

(1) For higher voltage up to 1600 V contact factory

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
16TTS08PbF	800	800	10			
16TTS12PbF	1200	1200	10			

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^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEGT COMPLETIONS		VALUES		UNITS
PARAMETER	STINIBUL		TEST CONDITIONS		MAX.	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 98 °C, 1	80° conduction, half sine wave	10		
Maximum RMS on-state current	I _{RMS}			1	6	1 ,
Maximum peak, one-cycle,	ı	10 ms sine p	ulse, rated V _{RRM} applied	170		A
non-repetitive surge current	I _{TSM}	10 ms sine p	ulse, no voltage reapplied	20	00	1
Maximum 12+ for fusing	l ² t	10 ms sine p	ulse, rated V _{RRM} applied	144		- A ² s
Maximum I ² t for fusing	i-r	10 ms sine p	0 ms sine pulse, no voltage reapplied		200	
Maximum I ² √t for fusing	I ² √t	t = 0.1 to 10 r	t = 0.1 to 10 ms, no voltage reapplied		00	A²√s
Maximum on-state voltage drop	V_{TM}	10 A, T _J = 25 °C		1	.4	V
On-state slope resistance	r _t	T 105 00		24	1.0	mΩ
Threshold voltage	V _{T(TO)}	T _J = 125 °C		1	.1	V
Maximum various and divast lackage current	1 /1	T _J = 25 °C	V Dated V A/	0	.5	
Maximum reverse and direct leakage current	I_{RM}/I_{DM}	T _J = 125 °C	$V_R = Rated V_{RRM}/V_{DRM}$	1	0	
Holding current	I _H		$v = 6 \text{ V}$, resistive load, initial $I_T = 1 \text{ A}$ T, 16TTS12PbF	-	100	mA
Maximum latching current	ΙL	Anode supply = 6 V, resistive load		20	00	
Maximum rate of rise of off-state voltage	dV/dt			50	00	V/µs
Maximum rate of rise of turned-on current	dl/dt			15	50	A/μs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P_{GM}		8.0	۱۸/	
Maximum average gate power	P _{G(AV)}		2.0	W	
Maximum peak positive gate current	+ I _{GM}		1.5	Α	
Maximum peak negative gate voltage	- V _{GM}		10	V	
Maximum required DC gate current to trigger	I _{GT}	Anode supply = 6 V, resistive load, T _J = - 65 °C	90	mA	
		Anode supply = 6 V, resistive load, T _J = 25 °C	60		
		Anode supply = 6 V, resistive load, T _J = 125 °C	35		
		Anode supply = 6 V, resistive load, T _J = - 65 °C	3.0		
Maximum required DC gate voltage to trigger	V_{GT}	Anode supply = 6 V, resistive load, T _J = 25 °C	2.0	V	
		Anode supply = 6 V, resistive load, T _J = 125 °C	1.0	V	
Maximum DC gate voltage not to trigger	V_{GD}	T 105 °C V Poted volvo	0.2		
Maximum DC gate current not to trigger	I _{GD}	T _J = 125 °C, V _{DRM} = Rated value		mA	

SWITCHING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9		
Typical reverse recovery time	t _{rr}	T _{.1} = 125 °C	4	μs	
Typical turn-off time	t _q	1J = 125	110		



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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_J , T_{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R_{thJC}	DC operation	1.3	
Maximum thermal resistance, junction to ambient		R_{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R_{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque ———	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf · in)
Marking device			Coop atula TO 200AB	16TTS08	
			Case style TO-220AB		16TTS12

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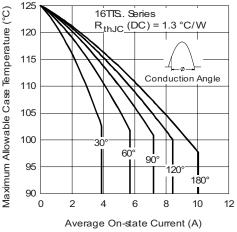


Fig. 1 - Current Rating Characteristics

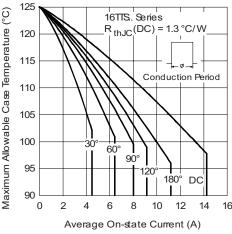


Fig. 2 - Current Rating Characteristics

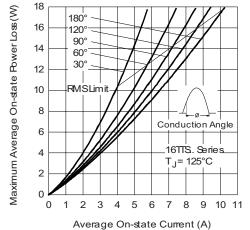


Fig. 3 - On-State Power Loss Characteristics

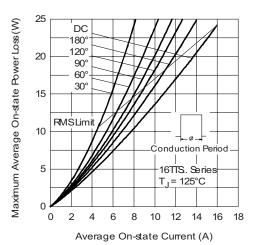


Fig. 4 - On-State Power Loss Characteristics

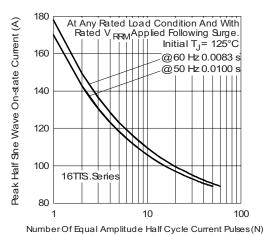


Fig. 5 - Maximum Non-Repetitive Surge Current

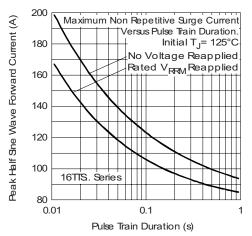


Fig. 6 - Maximum Non-Repetitive Surge Current



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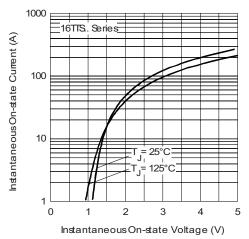


Fig. 7 - On-State Voltage Drop Characteristics

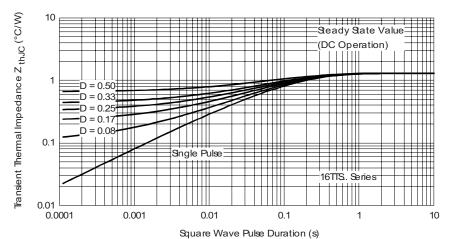
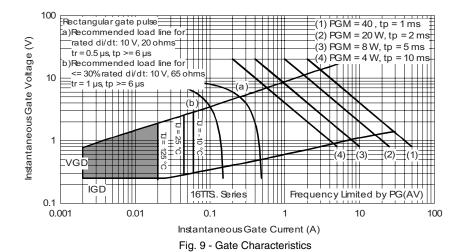


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

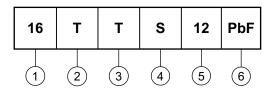


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ORDERING INFORMATION TABLE

Device code



1 - Current rating

2 - Circuit configuration:

T = Single thyristor

3 - Package:

T = TO-220AB

4 - Type of silicon:

S = Converter grade

5 - Voltage code x 100 = V_{RRM} ----

08 = 800 V 12 = 1200 V

6 - • None = Standard production

• PbF = Lead (Pb)-free

Note: For higher voltage up to 1600 V contact factory

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95222			
Part marking information	http://www.vishay.com/doc?95225		



Vishay

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