N-Channel 80-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

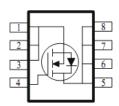
Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
80	41 @ V _{GS} = 10V	7.1		
80	57 @ V _{GS} = 4.5V	6.1		







ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter			Symbol	Limit	Units	
Drain-Source Voltage				80	V	
Gate-Source Voltage				±20	V	
Continuous Dunis Comment®		=25°C	1	7.1	А	
Continuous Drain Current ^a	TA	=70°C	I _D	6		
Pulsed Drain Current ^b			I_{DM}	50		
Continuous Source Current (Diode Conduction) a			Is	3.8	Α	
Device Dissinction 8		=25°C	P_{D}	3.1	W	
Power Dissipation ^a	T _A	=70°C	гD	2.2		
Operating Junction and Storage Temperature Range			T_J,T_stq	-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	40	°C/W		
Maximum Sunction-to-Ambient	Steady State	IN _θ JA	80	C/VV		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

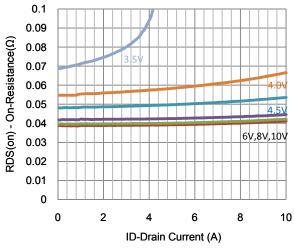
Parameter	Symbol	ymbol Test Conditions		Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, ID = 250 uA				V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±10	uA	
Zero Gate Voltage Drain Current	1	$V_{DS} = 64 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Brain Current	I _{DSS}	$V_{DS} = 64 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	uA	
On-State Drain Current	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, I_D = 5.4 \text{ A}$			41	mΩ	
Dialii-Source Oil-Resistance	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 4.6 \text{ A}$			57		
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 5.4 \text{ A}$		20		S	
Diode Forward Voltage	V_{SD}	$I_{S} = 2 \text{ A}, V_{GS} = 0 \text{ V}$		0.76		V	
		Dynamic					
Total Gate Charge	Q_g	V _{DS} = 40 V, V _{GS} = 4.5 V, ID = 5.4 A		19.5		nC	
Gate-Source Charge	Q_gs			4.5			
Gate-Drain Charge	Q_gd			10			
Turn-On Delay Time	t _{d(on)}			12.8			
Rise Time	t _r	$V_{DD} = 40 \text{ V}, R_L = 7.5 \Omega, I_D = 5.4 \text{ A},$		24		20	
Turn-Off Delay Time	t _{d(off)}	V_{GEN} = 10 V, R_{GEN} = 6 Ω		52.3		ns	
Fall Time	t _f	1		46			
Input Capacitance	C _{iss}			1522			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		129		pF	
Reverse Transfer Capacitance	C _{rss}			87			

Notes

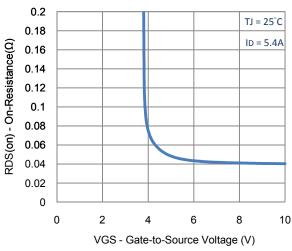
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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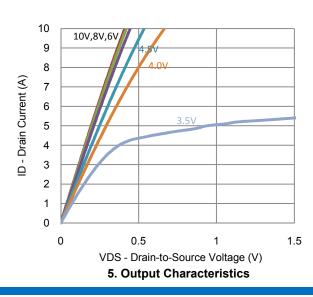
Typical Electrical Characteristics

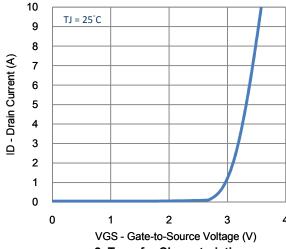


1. On-Resistance vs. Drain Current

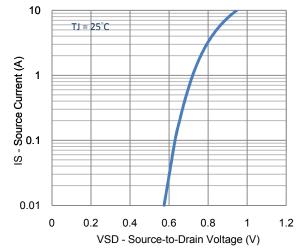


3. On-Resistance vs. Gate-to-Source Voltage

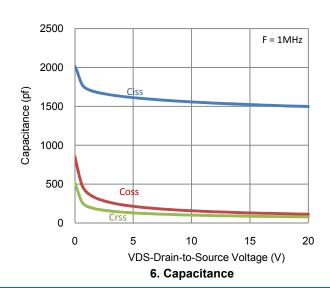




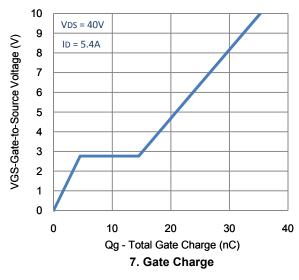
2. Transfer Characteristics

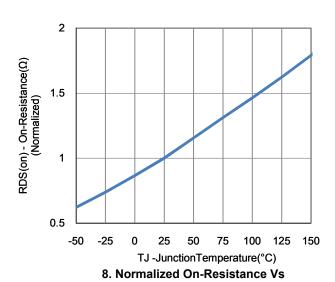


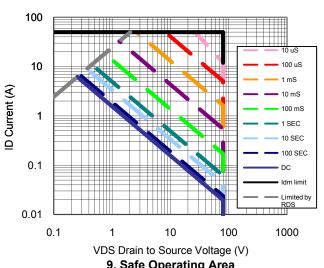
4. Drain-to-Source Forward Voltage

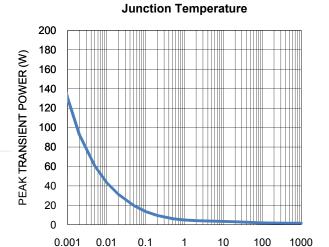


Typical Electrical Characteristics



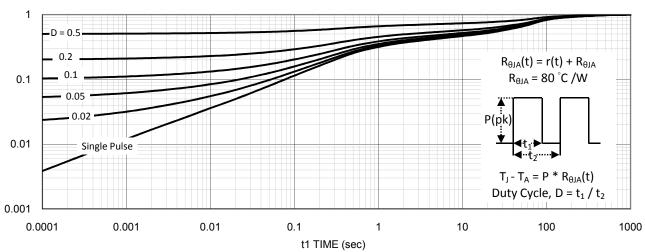






t1 TIME (SEC)

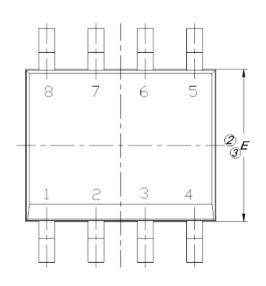


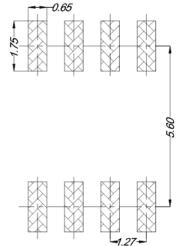


11. Normalized Thermal Transient Junction to Ambient

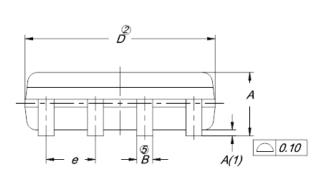
Package Information

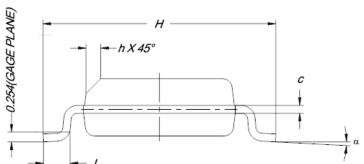
Land Pattern (Only for Reference)





DII.4	MILLIMETERS					
DIM.	MIN.	NOM.	MAX.			
Α	1.35	1.55	1.75			
A(1)	0.10	0.18	0.25			
В	0.38	0.45	0.51			
С	0.19	0.22	0.25			
D	4.80	4.90	5.00			
E	3.80	3.90	4.00			
е	1.27 BSC					
Н	5.80	6.00	6.20			
L	0.50	0.72	0.93			
α	0°	4°	8°			
h	0.25	0.38	0.50			





Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess
 Of B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The
 Foot.

Ordering Information

AM4480N-T1-XX

- A: **Analog Power**

- M: **MOSFET**

- 4480: Part number

- N: N-Channel

- T1: Tape & reel

– XX: Blank: Standard

PF: Leadfree