

# UNISONIC TECHNOLOGIES CO., LTD

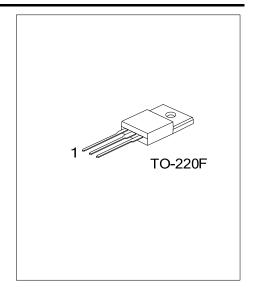
13N40 **Preliminary Power MOSFET** 

# 13A, 400V N-CHANNEL POWER MOSFET

#### **DESCRIPTION**

The UTC 13N40 is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology specializes in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

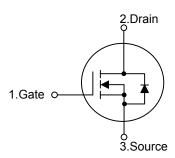
The UTC 13N40 is universally applied in electronic lamp ballast based on half bridge topology and high efficient switched mode power supply.



#### **FEATURES**

- \*  $R_{DS(ON)}$ =0.35 $\Omega$  @  $V_{GS}$ =10V
- \* High switching speed
- \* 100% avalanche tested

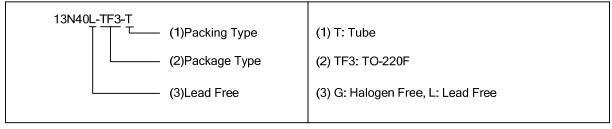
#### **SYMBOL**



#### ORDERING INFORMATION

Ordering Number		Daalaasa	Pin	Daakina			
Lead Free	Halogen Free	Package	1	2	3	Packing	
13N40L-TF3-T	13N40G-TF3-T	TO-220F	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



# ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	400	V
Gate-Source Voltage		$V_{GSS}$	±30	V
Drain Current	Continuous (T <sub>C</sub> =25°C)	I <sub>D</sub>	13	Α
	Pulsed (Note 2)	I <sub>DM</sub>	52	Α
Avalanche Energy	y Single Pulsed (Note 3)		705	mJ
Power Dissipation		$P_D$	48	W
Junction Temperature		TJ	+150	°C
Storage Temperature Range		T <sub>STG</sub>	-55~+150	°C

- Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
  - 2. Repetitive Rating: Pulse width limited by maximum junction temperature
  - 3. L = 8.34mH,  $I_{AS}$  = 13A,  $V_{DD}$  = 50V,  $R_{G}$ = 25 $\Omega$  , Starting  $T_{J}$  = 25 $^{\circ}$ C

# ■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	$\theta_{JA}$	60	°C/W	
Junction to Case	$\theta_{JC}$	2.58	°C/W	

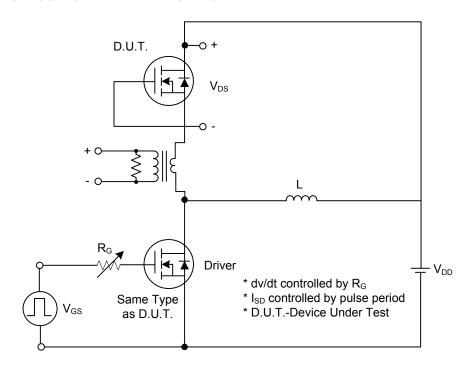
#### ■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C, unless otherwise noted)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS		_					
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	$I_D=250\mu A, V_{GS}=0V$ 400				٧
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =400V, V <sub>GS</sub> =0V			1	μΑ
Gate- Source Leakage Current	Forward	I <sub>GSS</sub>	V <sub>GS</sub> =+30V, V <sub>DS</sub> =0V			+100	nΑ
	Reverse		$V_{GS}$ =-30V, $V_{DS}$ =0V			-100	nΑ
ON CHARACTERISTICS					_		
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_{D}=250\mu A$ 2.0			4.0	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =6.5A			0.35	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C <sub>ISS</sub>			1283		рF
Output Capacitance		Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz		218		рF
Reverse Transfer Capacitance		C <sub>RSS</sub>			120		рF
SWITCHING PARAMETERS							
Turn-ON Delay Time		t <sub>D(ON)</sub>			16		ns
Rise Time		t <sub>R</sub>	$V_{DD}$ =200V, $I_{D}$ =13A, $R_{G}$ =25 $\Omega$		20		ns
Turn-OFF Delay Time		t <sub>D(OFF)</sub>	(Note 1, 2)		100		ns
Fall-Time		t <sub>F</sub>			42		ns
Total Gate Charge		$Q_{G}$	V 000 V 1 40A		79	100	nC
Gate-Source Charge		$Q_{GS}$	V <sub>DS</sub> =320 V, I <sub>D</sub> =13A,		7.2	12	nC
Gate-Drain Charge		$Q_{GD}$	V <sub>GS</sub> =10 V (Note 1,2)		43	55	nC
SOURCE- DRAIN DIODE RATII	NGS AND	CHARACTERI	STICS				
Drain-Source Diode Forward Voltage		V <sub>SD</sub>	I <sub>S</sub> =13A, V <sub>GS</sub> =0V			1.2	V
Maximum Body-Diode Continuous Current		I <sub>S</sub>				13	Α
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				52	Α

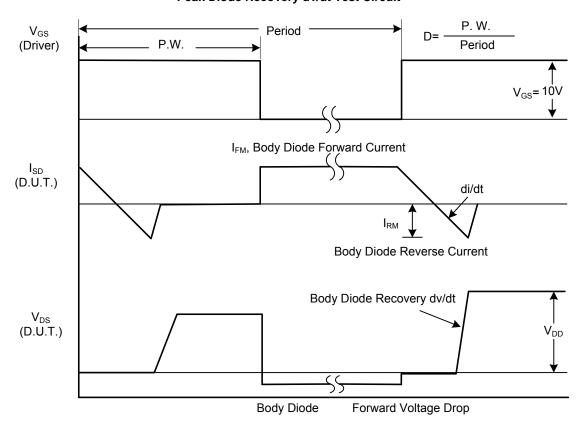
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

2. Essentially independent of operating temperature

# ■ TEST CIRCUITS AND WAVEFORMS

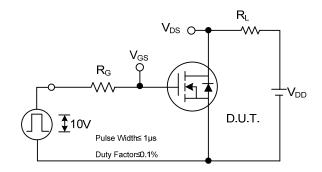


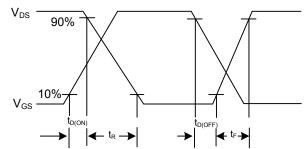
# Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

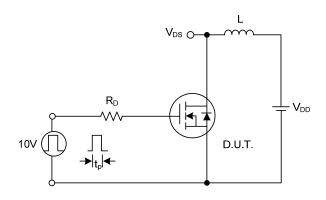
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

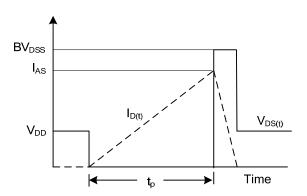




**Switching Test Circuit** 

**Switching Waveforms** 





**Unclamped Inductive Switching Test Circuit** 

**Unclamped Inductive Switching Waveforms** 

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