

PARALLEL TO SDI CONVERTER CARD WITH OPTIONAL EDH INSERTION

PI-001

INPUT Parallel multiplexed component signals to CCIR Rec 656.

Note. To ease interfacing into equipment data is latched by the rising edge of the clock.

Data Set up time 3 ns min to positive edge of clock
Data hold time 2.5 ns min from positive edge of clock

OUTPUT Serial digital 143,177, 270 & 360Mb/s NRZI

No of Outputs: 3 off serial
Output Impedance: 75 Ohms.
Output return loss: >15dB 5 MHz to 270 MHz.

EDH OPTION

Indications: EDH - Error detected Here (Note 1.)
 EDA - Error detected already (Note 1.)
 UES - Unknown Error status (Note 1.)

Controls: Transmit/Receive Mode: (Note 2.)
 Bypass Mode: (Note 2.)

Note 1. These are accessible only through the serial interface SCL and SDA.

Note 2. These are available only through the I/O interface see connector for details.

Mechanical Details

The following formats are available:

Format A STD configuration with low profile pins and BNC connectors for outputs.
 Format B LOW profile pins and MCX connectors for outputs.
 Format C STAND-OFF configuration with BNC outputs.

PACKAGE DETAIL

