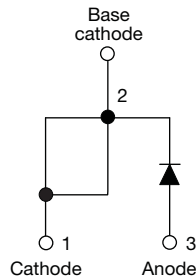


High Performance Generation 5.0 Schottky Rectifier, 30 A



TO-247AC modified



FEATURES

- 175 °C high performance Schottky diode
- Very low forward voltage drop
- Extremely low reverse leakage
- Optimized V_F vs. I_F trade off for high efficiency
- Increased ruggedness for reverse avalanche capability
- RBSOA available
- Negligible switching losses
- Submicron trench technology
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47



RoHS COMPLIANT

PRODUCT SUMMARY

Package	TO-247AC modified
$I_{F(AV)}$	30 A
V_R	100 V
V_F at I_F	0.64 V
I_{RM} max.	15 mA at 125 °C
T_J max.	175 °C
Diode variation	Single die
E_{AS}	135 mJ

APPLICATIONS

- High efficiency SMPS
- Automotive
- High frequency switching
- Output rectification
- Reverse battery protection
- Freewheeling
- DC/DC systems
- Increased power density systems

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
V_{RRM}		100	V
V_F	30 Apk, $T_J = 125$ °C (typical)	0.61	
T_J	Range	- 55 to 175	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VS-30PT100	UNITS
Maximum DC reverse voltage	V_R	$T_J = 25$ °C	100	V

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	$I_{F(AV)}$	50 % duty cycle at $T_C = 156$ °C, rectangular waveform	30	A
Maximum peak one cycle non-repetitive surge current	I_{FSM}	5 μ s sine or 3 μ s rect. pulse	2200	
		10 ms sine or 6 ms rect. pulse	450	
Non-repetitive avalanche energy	E_{AS}	$T_J = 25$ °C, $I_{AS} = 3$ A, $L = 30$ mH	135	mJ
Repetitive avalanche current	I_{AR}	Limited by frequency of operation and time pulse duration so that $T_J < T_J$ max. I_{AS} at T_J max. as a function of time pulse See fig. 8	I_{AS} at T_J max.	A



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS	
Forward voltage drop	$V_{FM}^{(1)}$	30 A	$T_J = 25\text{ }^\circ\text{C}$	-	0.77	V
		60 A		-	0.9	
		30 A	$T_J = 125\text{ }^\circ\text{C}$	-	0.64	
		60 A		-	0.76	
Reverse leakage current	$I_{RM}^{(1)}$	$T_J = 25\text{ }^\circ\text{C}$	$V_R = \text{Rated } V_R$	-	200	μA
		$T_J = 125\text{ }^\circ\text{C}$		-	15	mA
Junction capacitance	C_T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 $^\circ\text{C}$	1650	-	pF	
Series inductance	L_S	Measured lead to lead 5 mm from package body	7.5	-	nH	
Maximum voltage rate of change	dV/dt	Rated V_R	-	10 000	V/ μs	

Note

(1) Pulse width < 300 μs , duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		- 55 to 175	$^\circ\text{C}$
Maximum thermal resistance, junction to case	R_{thJC}	DC operation	0.8	$^\circ\text{C}/\text{W}$
Typical thermal resistance, case to heatsink	R_{thCS}	Mounting surface, smooth and greased	0.25	
Approximate weight			6	g
			0.21	oz.
Mounting torque	minimum		6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style TO-247AC modified (JEDEC)	30PT100	

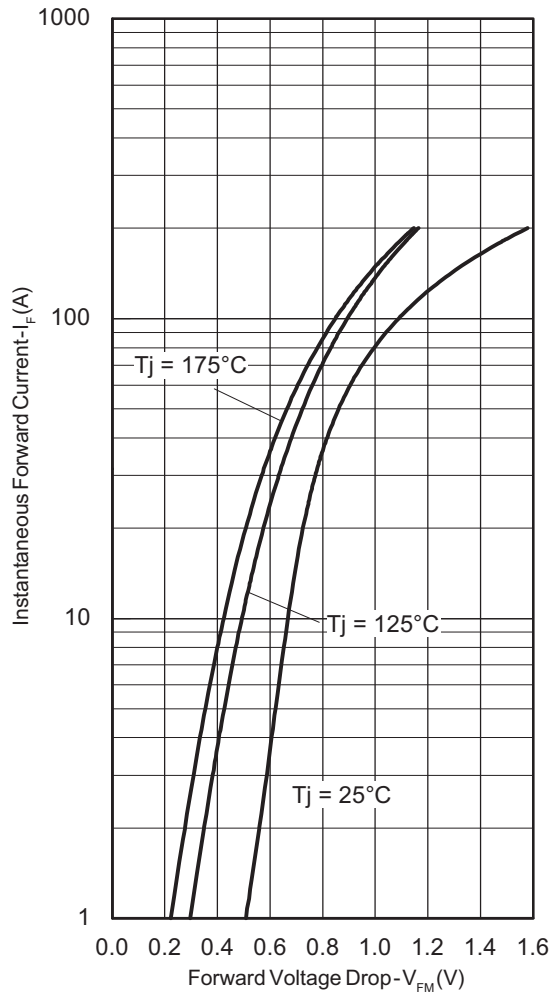


Fig. 1 - Maximum Forward Voltage Drop Characteristics

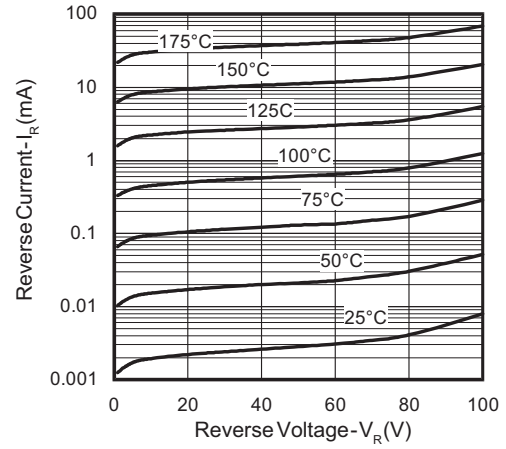


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

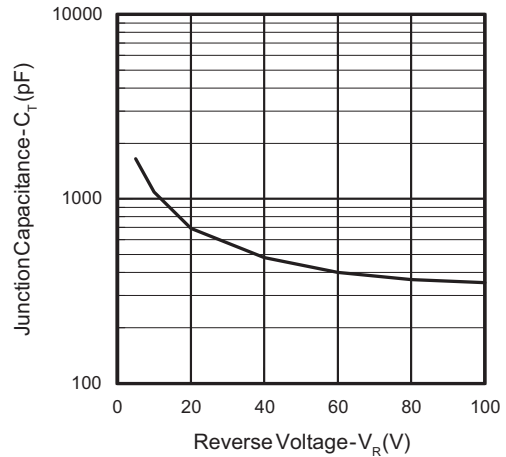


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

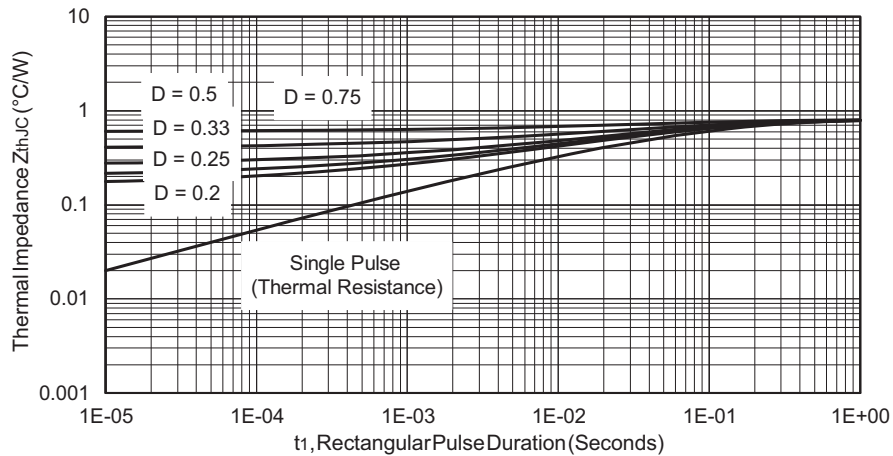


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

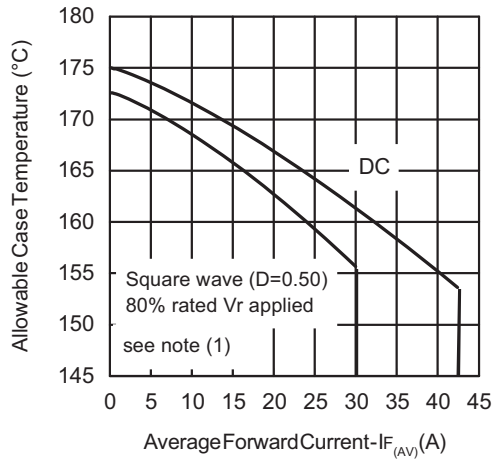


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

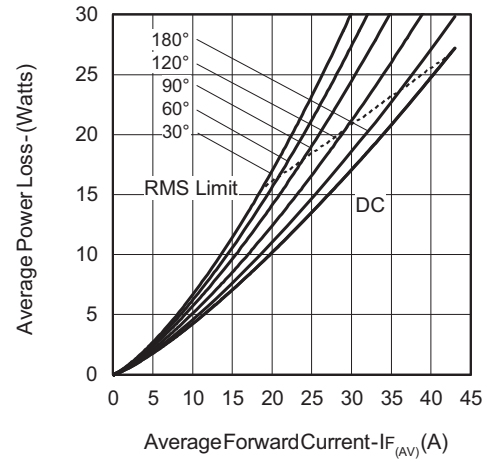


Fig. 6 - Forward Power Loss Characteristics

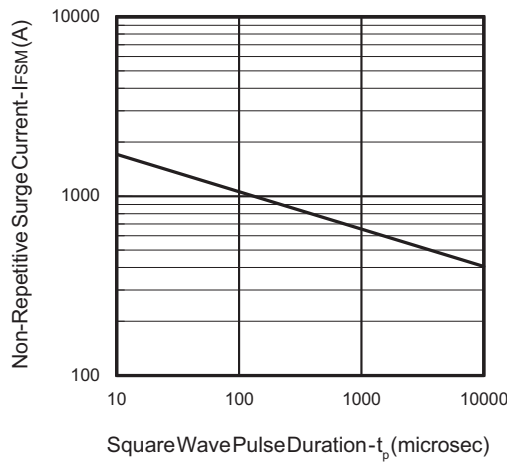


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 $P_{d_{REV}}$ = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R

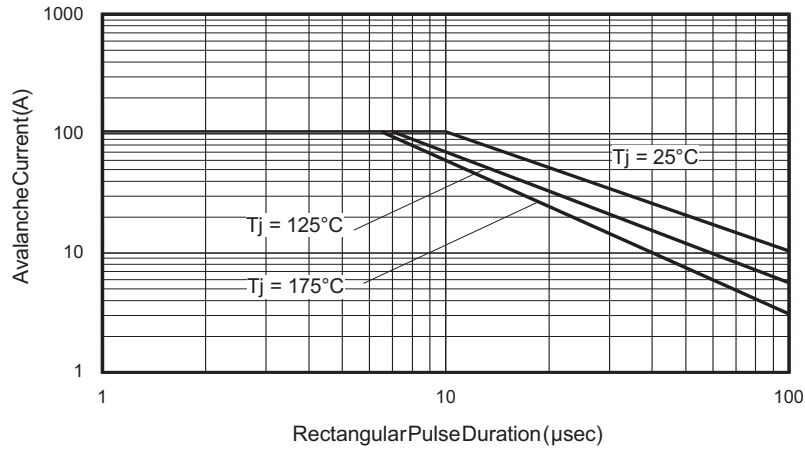


Fig. 8 - Reverse Bias Safe Operating Area (Avalanche Current vs. Rectangular Pulse Duration)

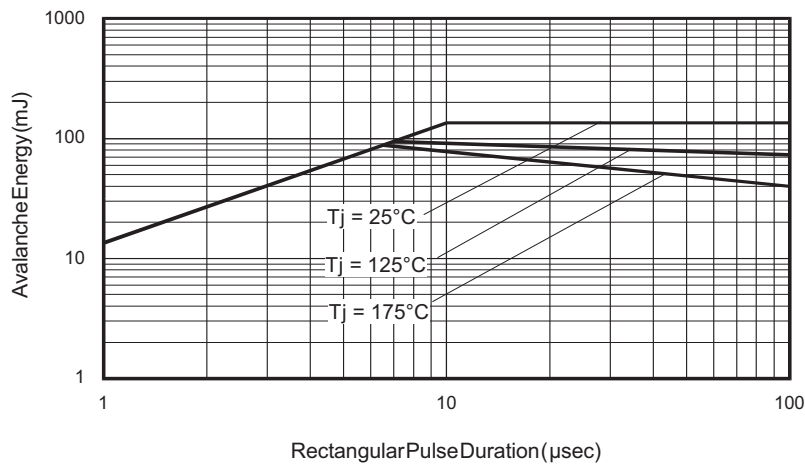
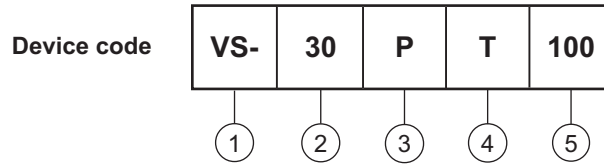


Fig. 9 - Reverse Bias Safe Operating Area (Avalanche Energy vs. Rectangular Pulse Duration)



ORDERING INFORMATION TABLE



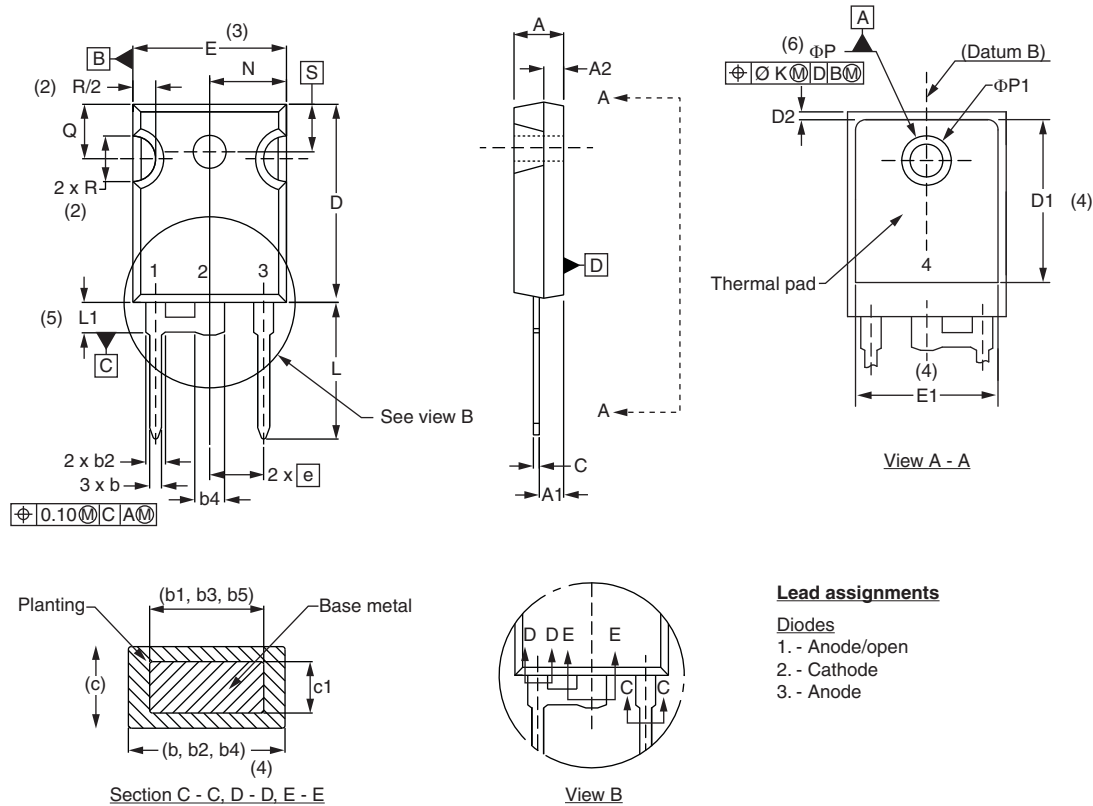
- 1** - Vishay Semiconductors product
- 2** - Current rating (30 A)
- 3** - Package:
P = TO-247 (modified)
- 4** - T = Trench
- 5** - Voltage code (100 V)

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95253
Part marking information	www.vishay.com/doc?95255
SPICE model	www.vishay.com/doc?95232



DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
c	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
e	5.46 BSC		0.215 BSC		
ΦK	2.54		0.010		
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62 BSC		0.3		
ΦP	3.56	3.66	0.14	0.144	
ΦP1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51 BSC		0.217 BSC		

Notes

- Dimensioning and tolerance per ASME Y14.5M-1994
- Contour of slot optional
- Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Thermal pad contour optional with dimensions D1 and E1
- Lead finish uncontrolled in L1
- ΦP to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- Outline conforms to JEDEC outline TO-247 with exception of dimension c



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