# **Small Signal MOSFET**

# 25 V, 1.2 A, Single, N-Channel, SC-88

#### **Features**

- Advance Planar Technology for Fast Switching, Low R<sub>DS(on)</sub>
- Higher Efficiency Extending Battery Life
- Pb-Free Packages are Available

## **Applications**

- Boost and Buck Converter
- Load Switch
- Battery Protection

### MAXIMUM RATINGS (T<sub>.1</sub> = 25°C unless otherwise noted)

Ratino	Symbol	Value	Unit		
Drain-to-Source Voltage	$V_{DSS}$	25	V		
Gate-to-Source Voltage	$V_{GS}$	±8.0	V		
Drain Current	t < 5 s	T <sub>A</sub> = 25°C	I <sub>D</sub>	1.2	Α
Continuous Drain Current	I Steady I A		I <sub>D</sub>	1.0	Α
(Note 1) State		T <sub>A</sub> = 75°C		0.80	
Power Dissipation (Note 1)	Stead	dy State	P <sub>D</sub>	0.63	W
Power Dissipation (Note 1)	t≤	≤ 5 s	P <sub>D</sub>	0.89	W
Pulsed Drain Current $t_p = 10 \mu s$			I <sub>DM</sub>	3.7	Α
Operating Junction and Sto	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C		
Source Current (Body Diod	I <sub>S</sub>	0.8	Α		
Lead Temperature for Sold (1/8" from case for 10 s	TL	260	°C		
ESD Rating – Machine Mo		25	V		

## THERMAL RESISTANCE RATINGS

Rating	Symbol	Max	Unit
Junction-to-Lead - Steady State (Note 1)	$R_{\theta JL}$	102	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	200	
Junction-to-Ambient - t ≤ 5 s (Note 1)	$R_{\thetaJA}$	140	1

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

 Surface mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [1 oz] including traces).

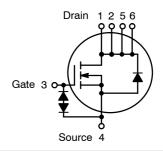


## ON Semiconductor®

#### http://onsemi.com

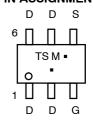
V <sub>(BR)DSS</sub>	V <sub>(BR)DSS</sub> R <sub>DS(on)</sub> Typ	
25 V	249 mΩ @ 4.5 V	1.2 A
	299 mΩ @ 2.7 V	1.24

#### N-Channel



# MARKING DIAGRAM & PIN ASSIGNMENT





TS = Device Code

M = Date Code

Pb-Free Package

(Note: Microdot may be in either location)

### ORDERING INFORMATION

Device	Package	Shipping†
NTJS4405NT1	SC-88	3000 / Tape & Reel
NTJS4405NT1G	SC-88 (Pb-Free)	3000 / Tape & Reel
NTJS4405NT4	SC-88	10,000 / Tape & Reel
NTJS4405NT4G	SC-88 (Pb-Free)	10,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# **ELECTRICAL CHARACTERISTICS** ( $T_J = 25$ °C unless otherwise noted)

Characteristic	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS					•		•
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		25			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>				30		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25°C			1.0	μА
		$V_{DS} = 20 \text{ V}$	T <sub>J</sub> = 125°C			10	7
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = 8.0 V				100	nA
ON CHARACTERISTICS (Note 2)							-
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_{D}$	= 250 μΑ	0.65		1.5	V
Negative Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>				-2.0		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	$R_{DS(on)}$ $V_{GS} = 4.5 \text{ V}, I_D = 0.6 \text{ A}$ $V_{GS} = 2.7 \text{ V}, I_D = 0.2 \text{ A}$			249	350	mΩ
	•				299	400	
	V <sub>GS</sub> = 4.5 V, I <sub>D</sub>		<sub>D</sub> = 1.2 A		260		
Forward Transconductance	9FS	V <sub>DS</sub> = 5.0 V, I <sub>D</sub> = 0.5 A			0.5		S
CHARGES AND CAPACITANCES	-				<u> </u>	<u>-</u>	-
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V, f = 1.0 MHz, V <sub>DS</sub> = 10 V			49	60	pF
Output Capacitance	C <sub>OSS</sub>				22.4	30	
Reverse Transfer Capacitance	C <sub>RSS</sub>	100			8.0	12	
Total Gate Charge	Q <sub>G(TOT)</sub>				0.75	1.5	nC
Threshold Gate Charge	Q <sub>G(TH)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>[</sub>	<sub>os</sub> = 5.0 V,		0.10		1
Gate-to-Source Charge	$Q_{GS}$	$I_D = 0.95 \text{ A}$			0.30	0.50	
Gate-to-Drain Charge	$Q_{GD}$				0.20	0.40	1
SWITCHING CHARACTERISTICS (No	ote 3)						-
Turn-On Delay Time	t <sub>d(ON)</sub>	$V_{GS}$ = 4.5 V, $V_{DS}$ = 6.0 V, $I_{D}$ = 0.5 A, $R_{G}$ = 50 $\Omega$			6.0	12	ns
Rise Time	t <sub>r</sub>				4.7	8.0	1
Turn-Off Delay Time	t <sub>d(OFF)</sub>				25	35	1
Fall Time	t <sub>f</sub>				41	60	
DRAIN-SOURCE DIODE CHARACTE	RISTICS						
Forward Diode Voltage	$V_{SD}$	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 0.6 A	T <sub>J</sub> = 25°C		0.82	1.20	V

Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

# TYPICAL PERFORMANCE CURVES ( $T_J = 25^{\circ}C$ unless otherwise noted)

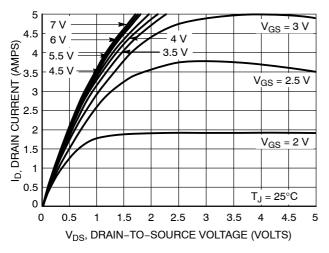


Figure 1. On-Region Characteristics

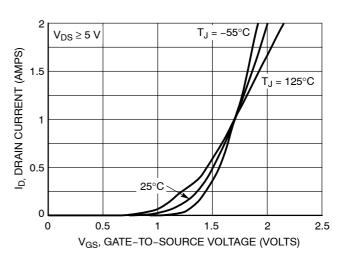


Figure 2. Transfer Characteristics

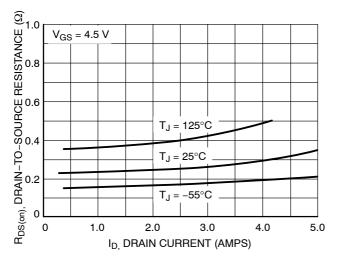


Figure 3. On-Resistance vs. Drain Current and Temperature

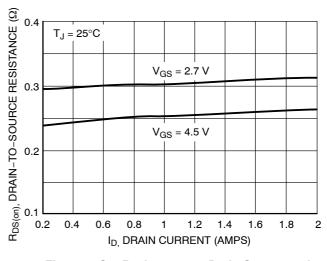


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

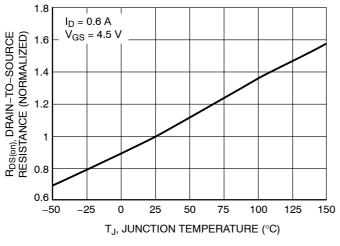


Figure 5. On–Resistance Variation with Temperature

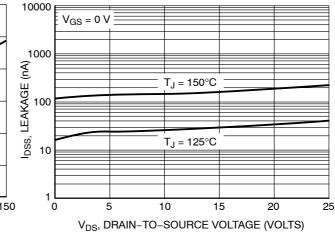


Figure 6. Drain-to-Source Leakage Current vs. Voltage

## TYPICAL PERFORMANCE CURVES (T<sub>J</sub> = 25°C unless otherwise noted)

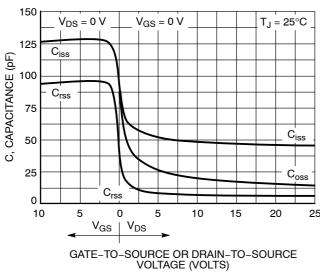


Figure 7. Capacitance Variation

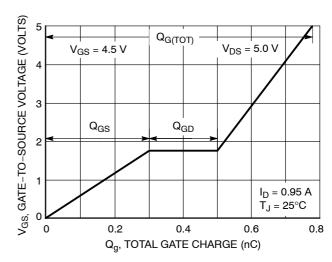


Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

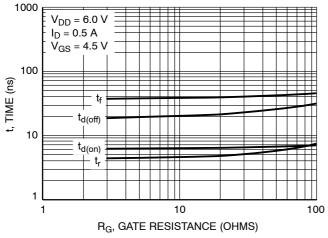


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

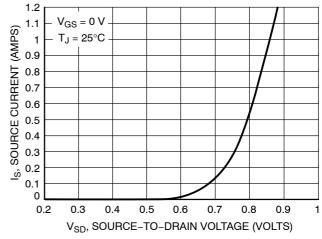
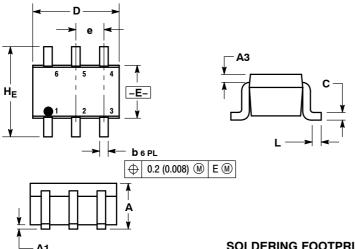


Figure 10. Diode Forward Voltage vs. Current

#### PACKAGE DIMENSIONS

#### SC-88/SC70-6/SOT-363 CASE 419B-02 **ISSUE W**

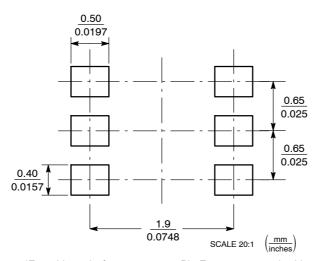


#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
- 419B-01 OBSOLETE, NEW STANDARD 419B-02.

	MILLIMETERS			INCHES				
DIM	MIN	NOM	MAX	MIN	NOM	MAX		
Α	0.80	0.95	1.10	0.031	0.037	0.043		
A1	0.00	0.05	0.10	0.000	0.002	0.004		
А3	0.20 REF			0.008 REF				
b	0.10	0.21	0.30	0.004	0.008	0.012		
С	0.10	0.14	0.25	0.004	0.005	0.010		
D	1.80	2.00	2.20	0.070	0.078	0.086		
E	1.15	1.25	1.35	0.045	0.049	0.053		
е	0.65 BSC			0	.026 BS	С		
L	0.10	0.20	0.30	0.004	0.008	0.012		
He	2.00	2.10	2.20	0.078	0.082	0.086		

**SOLDERING FOOTPRINT\*** 



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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