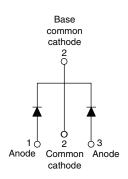


## VS-63CTQ100GPbF, VS-63CTQ100G-N3

Vishay Semiconductors

# Schottky Rectifier, 2 x 30 A

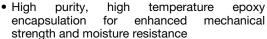


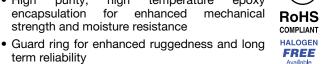


PRODUCT SUMMARY						
Package	TO-220AB					
I <sub>F(AV)</sub>	2 x 30 A					
$V_{R}$	100 V					
V <sub>F</sub> at I <sub>F</sub>	0.69 V					
I <sub>RM</sub> max.	20 mA at 125 °C					
T <sub>J</sub> max.	175 °C					
Diode variation	Common cathode					
E <sub>AS</sub>	11.25 mJ					

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Low forward voltage drop
- High frequency operation





- term reliability Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### **DESCRIPTION**

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	UNITS					
I <sub>F(AV)</sub>	Rectangular waveform (per device)	60	А			
V <sub>RRM</sub>		100	V			
I <sub>FRM</sub>	T <sub>C</sub> = 139 °C (per leg)	60	^			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1500	A			
V <sub>F</sub>	30 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.69	V			
T <sub>J</sub>	Range	- 65 to 175	°C			

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-63CTQ100GPbF	VS-63CTQ100G-N3	UNITS			
Maximum DC reverse voltage	imum DC reverse voltage V <sub>R</sub> 100 100		V				
Maximum working peak reverse voltage	$V_{RWM}$	100	100	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average per leg forward current per device			50 % duty cycle at T <sub>C</sub> = 139 °C, rectangular waveform		30	A	
		I <sub>F(AV)</sub>			60		
Peak repetitive forward current per leg		I <sub>FRM</sub>	Rated $V_R$ , square wave, 20 kHz, $T_C$ = 140 °C		60		
Maximum peak one cycle non-repetitive surge current per leg		l	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1500		
		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	300		
Non-repetitive avalanche energy	per leg	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 0.75  \text{A},  L = 40  \text{mH}$		11.25	mJ	
Repetitive avalanche current pe	r leg	$I_{AR}$ Current decaying linearly to zero in 1 μs Frequency limited by $T_J$ maximum $V_A = 1.5$ x $V_R$ typical		0.75	Α		



# VS-63CTQ100GPbF, VS-63CTQ100G-N3

# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			UNITS	
Maximum forward voltage drop		30 A	T <sub>.1</sub> = 25 °C	0.78	0.82	V	
	V <sub>FM</sub> <sup>(1)</sup>	60 A	1j=25 C	0.94	1.0		
	V FM (1)	30 A	T <sub>.1</sub> = 125 °C	0.64	0.69		
		60 A	1J = 125 C	0.78	0.83		
Maximum instantaneous reverse current	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.02	0.3	mA	
waxiinum instantaneous reverse current		T <sub>J</sub> = 125 °C	VR = nateu VR	11	20	IIIA	
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		11	00	pF	
Typical series inductance	L <sub>S</sub>	Measured from top of term	8	.0	nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	Rated V <sub>R</sub> 10			V/µs	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction and storage temperature range	ge	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C	
Maximum thermal resistance, junction to case per leg		$R_{thJC}$	DC operation	1.2	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
minii			Non-lubricated threads	6 (5)	kgf ⋅ cm	
Mounting torque -	maximum		Non-lubricated trireads	12 (10)	(lbf · in)	
Marking device			Case style TO-220AB	63CTC	Q100G	

# Vishay Semiconductors

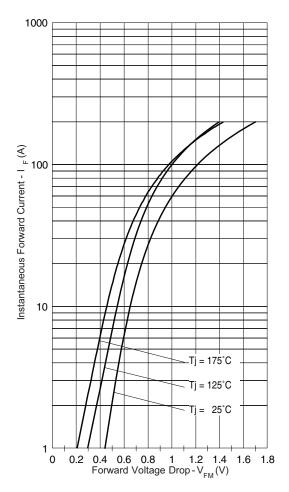


Fig. 1 - Maximum Forward Voltage Drop Characteristics

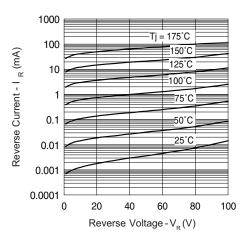


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

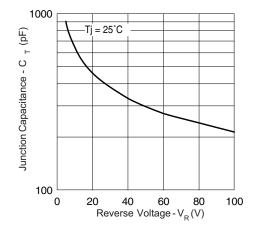


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

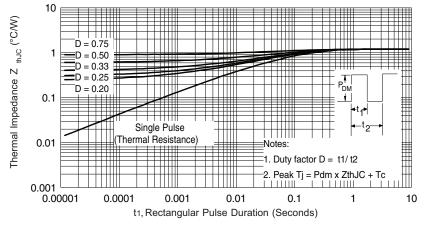


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

## Vishay Semiconductors

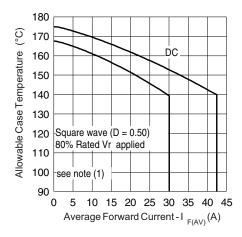


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

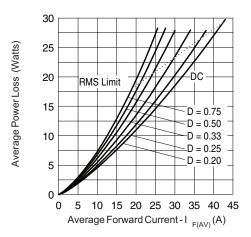


Fig. 6 - Forward Power Loss Characteristics

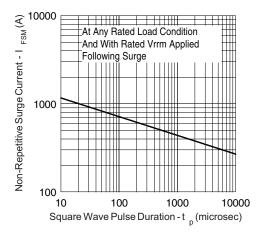


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

## Note

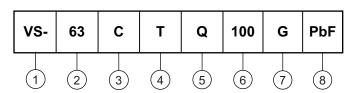
 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} = \text{Forward power loss} = I_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/D) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} = \text{Inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \text{ \% rated V}_{\text{R}} \\ \end{array}$ 

# VS-63CTQ100GPbF, VS-63CTQ100G-N3

Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (60 A)

3 - C = Common cathode

4 - T = TO-220

Q = Schottky "Q" series

Voltage rating (100 = 100 V)

7 - G = Schottky generation

8 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-63CTQ100GPbF	50	1000	Antistatic plastic tube				
VS-63CTQ100G-N3	50	1000	Antistatic plastic tube				

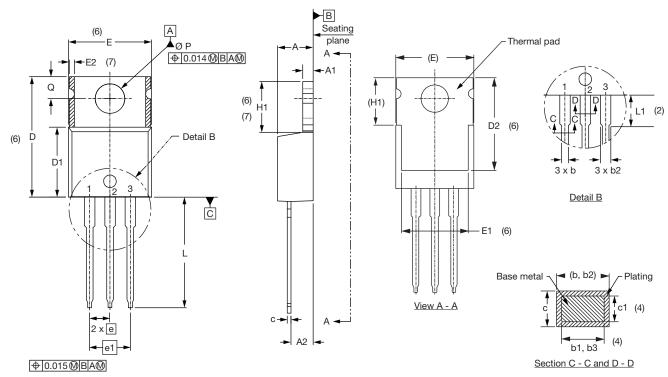
LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95222						
Dort marking information	TO-220AB PbF	www.vishay.com/doc?95225				
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028				



## Vishay Semiconductors

## **TO-220AB**

### **DIMENSIONS** in millimeters and inches



## Lead assignments

## **Diodes**

- 1. Anode/open
- 2. Cathode
- 3. Anode

### Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	
		•	•	•	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip



## **Legal Disclaimer Notice**

Vishay

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