

HDMI05-CL01F3

5-line IPAD[™], HDMI[™] control line ESD protection

Features

- Low line capacitance
- High efficiency in ESD protection
- Lead-free package
- Very thin package
- High reliability offered by monolithic integration
- High reduction of parasitic elements through integration and wafer level packaging

Complies with the standards:

- IEC 61000-4-2 Level 4
 - ± 15 kV (air discharge)
 - ± 8 kV (contact discharge)
- IEC 61000-4-2 Level 1
 - ± 2 kV (air discharge)
 - ± 2 kV (contact discharge)

Application

Where ESD protection for HDMI control lines (CEC, HPD, SCL and SDA) is required:

- Mobile phones and communication systems
- Portable multimedia players
- Camcorder, digital still cameras

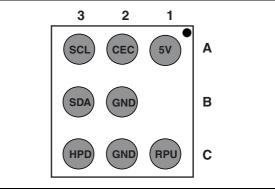
Description

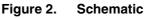
The HDMI05-CL01F3 chip is a low capacitance ESD protection for HDMI control pins. It also integrates a pull-up resistor for I²C bus and a pull-down resistor for hot plug detect.

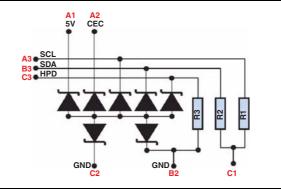
The ESD protection circuitry prevents damage to the protected device when subjected to ESD surges up to 15 kV.



Figure 1. Pin configuration (bump side)







- 1. B2 and C2 bumps must be connected together in the PCB
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1 Characteristics

Symbol	Parameter	Value	Unit
V _{PP}	External pins (A1, A2, A3, B3 and C3): ESD IEC 61000-4-2, level 4 - air discharge ESD IEC 61000-4-2, level 4 - contact discharge Internal pin (C1): ESD IEC 61000-4-2, level 1 - air discharge ESD IEC 61000-4-2, level 1 - contact discharge	15 8 2 2	kV
Pd	Line resistance power dissipation at 70 °C	60	mW
T _{op}	Operating temperature range	-30 to + 85	°C
T _{stg}	Storage temperature range	-55 to + 150	°C

Table 1. Absolute maximum ratings ($T_{amb} = 25 \ ^{\circ}C$)

Figure 3. Electrical characteristics - definitions

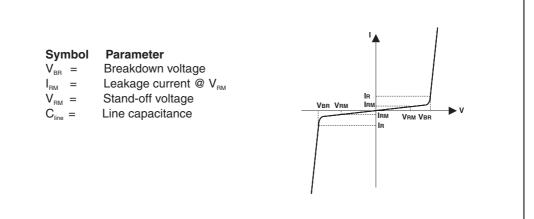


Table 2.Electrical characteristics (T_{amb} = 25 °C)

Symbol	Test condition	Min.	Тур.	Max.	Unit
V _{BR}	I _R = 1 mA	14			V
I _{RM}	V _{RM} = 3 V per line		50	200	nA
R ₁ , R ₂		1575	1750	1925	Ω
R ₃		80	100	120	kΩ
C _{line}	$V_{line} = 0 V$, $V_{osc} = 30 mV$, F = 1 MHz (measured under zero light conditions, B2 and C2 bumps connect together)	8	10	12	pF

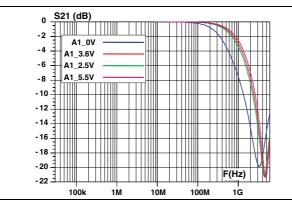


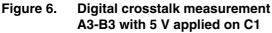
F(Hz)

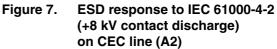
1G

Analog crosstalk measurements

Figure 4. S21(dB) versus frequency on A1





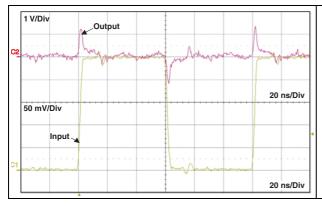


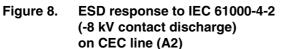
10M

100M

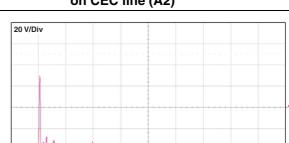
1M

TIT





57



C2 20 ns/Div

Figure 9. Line capacitance versus reverse applied voltage on A2-B2

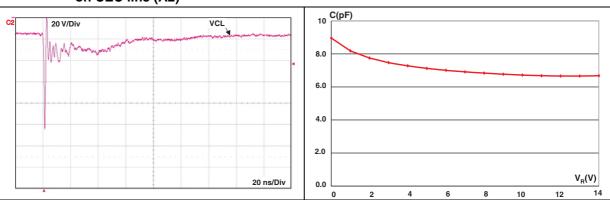


Figure 5.

0

- 5

- 10

- 15

- 20

- 25

- 30

- 35

- 40 -

B3-A3

Crosstalk (dB) B3_A3_0V

B3 A3 3.6V

B3 A3 2.5V

B3_A3_5.5V

П

Ш

100k

2 Typical application schematic

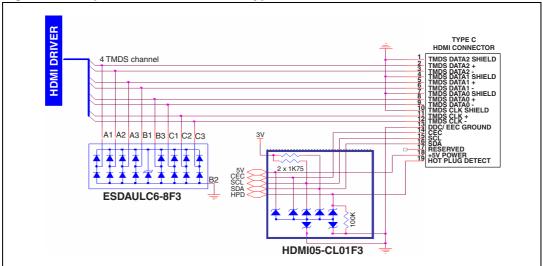
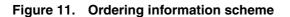
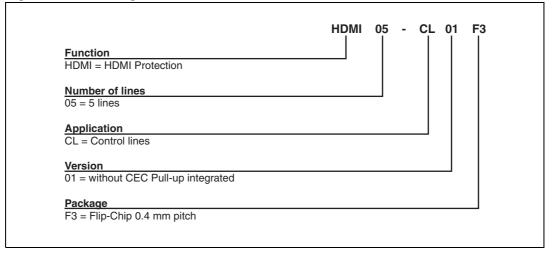


Figure 10. Implementation with HDMI type C connector



3 Ordering information scheme



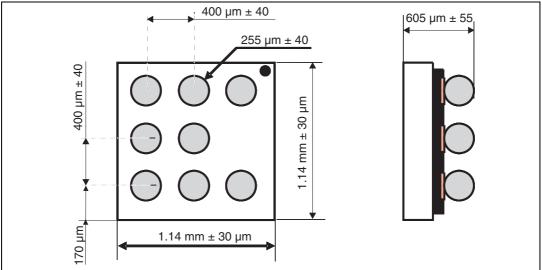


4 Package information

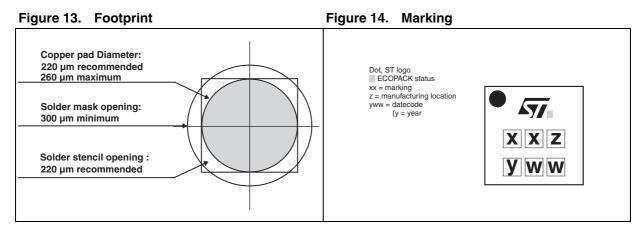
- Epoxy meets UL94, V0
- Lead-free package

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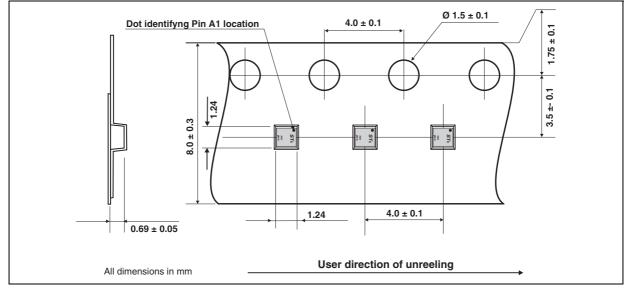
Figure 12. Flip-Chip dimensions













5 Ordering information

Table 3.Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
HDMI05-CL01F3	JN	Flip-Chip	1.9 mg	5000	Tape and reel (7")

6 Revision history

Table 4.Document revision history

Date	Revision	Changes
30-Apr-2010	1	Initial release.



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