

## **DESCRIPTION**

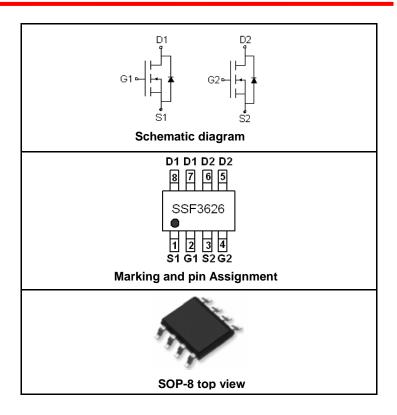
The SSF3626 uses advanced trench technology to provide excellent  $R_{\rm DS(ON)}$  and low gate charge .This device is suitable for use as a load switch or in PWM applications.

## **GENERAL FEATURES**

- $V_{DS} = 30V, I_D = 6.9A$   $R_{DS(ON)} < 51m\Omega @ V_{GS} = 4.5V$  $R_{DS(ON)} < 35m\Omega @ V_{GS} = 10V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

## **Application**

- ●PWM applications
- Load switch
- Power management



## PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
SSF3626	SSF3626	SOP-8	Ø330mm	12mm	2500 units

ABSOLUTE MAXIMUM RATINGS(TA=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current Continuous & Current Bulged (Note 1)	I <sub>D</sub>	6.9	А
Drain Current-Continuous@ Current-Pulsed (Note 1)	I <sub>DM</sub>	30	А
Maximum Power Dissipation	P <sub>D</sub>	2.8	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$

## THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	62.5	°C/W
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**ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)** 

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS						I
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V			1	μA



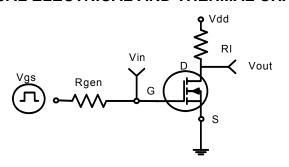
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20 $V$ , $V_{DS}$ =0 $V$			±100	nA
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	1.5		3	V
Drain-Source On-State Resistance	$R_{DS(ON)} = \frac{V_{GS}=4.5V, I_{D}=4.9A}{V_{GS}=10V, I_{D}=5.9A}$	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4.9A		41	51	$m\Omega$
Dialii-30dice Oii-3tate Resistance			25	35	$m\Omega$	
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =10V,I <sub>D</sub> =5.9A		5		S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C <sub>lss</sub>			550		PF
Output Capacitance	Coss	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V, F=1.0MHz		100		PF
Reverse Transfer Capacitance	C <sub>rss</sub>			50		PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>			5		nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DS}$ =15V, $V_{GS}$ =10V, $R_{GEN}$ =32 $\Omega$		25		nS
Turn-Off Delay Time	$t_{\sf d(off)}$	I <sub>D</sub> =4.7A		12		nS
Turn-Off Fall Time	tf			10		nS
Total Gate Charge	$Q_g$			9		nC
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =15V,I <sub>D</sub> =5.9A,V <sub>GS</sub> =10V		1.8		nC
Gate-Drain Charge	$Q_{gd}$			1.7		nC
Body Diode Reverse Recovery Time	T <sub>rr</sub>	1 -4 74 -41/45-4004/:		20		nS
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	– I <sub>F</sub> =4.7A, dl/dt=100A/μs		12		nC
DRAIN-SOURCE DIODE CHARACTERISTI	cs	-				
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =4.7A		1	1.2	V

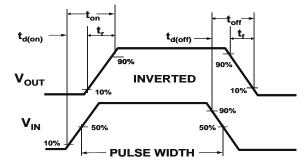
# **NOTES:**

- Repetitive Rating: Pulse width limited by maximum junction temperature.
  Surface Mounted on 1in² FR4 Board, t ≤ 10 sec.
  Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
  Guaranteed by design, not subject to production testing.



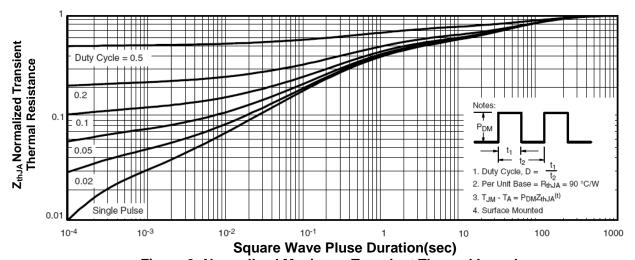
# TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS





**Figure 1: Switching Test Circuit** 

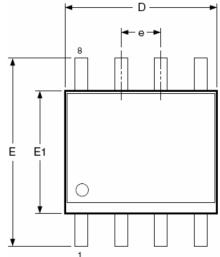
**Figure 2:Switching Waveforms** 

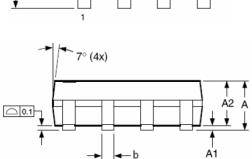


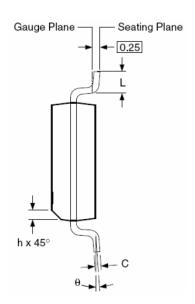
**Figure 3: Normalized Maximum Transient Thermal Impedance** 



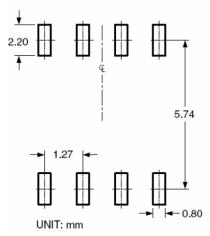
# **SOP-8 PACKAGE INFORMATION**







## RECOMMENDED LAND PATTERN



Symbols	Min.	Nom.	Max.		
Α	1.35	1.65	1.75		
A1	0.10	_	0.25		
A2	1.25	1.50	1.65		
b	0.31	_	0.51		
С	0.17	_	0.25		
D	4.80	4.90	5.00		
E1	3.80	3.90	4.00		
е		1.27 BSC			
E	5.80	6.00	6.20		
h	0.25	_	0.50		
L	0.40	_	1.27		

Dimensions in millimeters

Dimensions in inches							
Symbols	Min.	Nom.	Max.				
Α	0.053	0.065	0.069				
A1	0.004	_	0.010				
A2	0.049	0.059	0.065				
b	0.012	_	0.020				
С	0.007	_	0.010				
D	0.189	0.193	0.197				
E1	0.150	0.154	0.157				
е	0	С					
E	0.228	0.236	0.244				
h	0.010	_	0.020				
L	0.016	_	0.050				
θ	<b>0</b> °	_	8°				

## NOTES:

- Dimensions are inclusive of plating
  Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.
- 3. Dimension L is measured in gauge plane.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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