

# FCP380N60 / FCPF380N60

## 600V N-Channel MOSFET

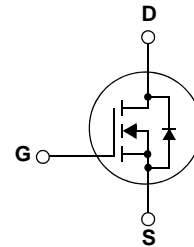
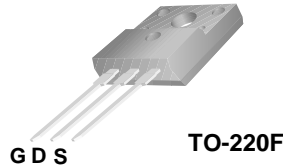
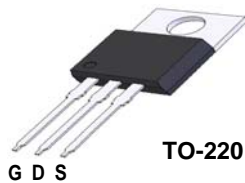
### Features

- 650V @ $T_J = 150^\circ\text{C}$
- Max.  $R_{DS(on)} = 380\text{m}\Omega$
- Ultra low gate charge (typ.  $Q_g = 30\text{nC}$ )
- Low effective output capacitance (typ.  $C_{oss-eff} = 95\text{pF}$ )
- 100% avalanche tested

### Description

SuperFET®II is, Fairchild's proprietary, new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET®II is very suitable for various AC/DC power conversion in switching mode operation for system miniaturization and higher efficiency.



### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	FCP380N60	FCPF380N60	Units
$V_{DSS}$	Drain to Source Voltage	600		V
$V_{GSS}$	Gate to Source Voltage	-DC	$\pm 20$	V
		-AC (f>1HZ)	$\pm 30$	
$I_D$	Drain Current	-Continuous ( $T_C = 25^\circ\text{C}$ )	10.2	10.2*
		-Continuous ( $T_C = 100^\circ\text{C}$ )	6.4	6.4*
$I_{DM}$	Drain Current	- Pulsed (Note 1)	30.6	30.6*
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)	211.6		mJ
$I_{AR}$	Avalanche Current (Note 1)	2.3		A
$E_{AR}$	Repetitive Avalanche Energy (Note 1)	1.06		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	20		V/ns
	MOSFET dv/dt	100		
$P_D$	Power Dissipation	( $T_C = 25^\circ\text{C}$ )	106	31
		- Derate above $25^\circ\text{C}$	0.85	0.25
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150		$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		$^\circ\text{C}$

\*Drain current limited by maximum junction temperature

### Thermal Characteristics

Symbol	Parameter	FCP380N60	FCPF380N60	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.18	4	$^\circ\text{C/W}$
$R_{\theta CS}$	Thermal Resistance, Case to Heat Sink (Typical)	0.5	0.5	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62.5	62.5	

## Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCP380N60	FCP380N60	TO-220	-	-	50
FCPF380N60	FCPF380N60	TO-220F	-	-	50

## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
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### Off Characteristics

$BV_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS} = 0V, I_D = 10mA, T_J = 25^\circ\text{C}$	600	-	-	V
		$V_{GS} = 0V, I_D = 10mA, T_J = 150^\circ\text{C}$	650	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 10mA$ , Referenced to $25^\circ\text{C}$	-	0.6	-	$V/^\circ\text{C}$
$BV_{DS}$	Drain-Source Avalanche Breakdown Voltage	$V_{GS} = 0V, I_D = 10A$	-	700	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 480V, V_{GS} = 0V$	-	-	1	$\mu\text{A}$
		$V_{DS} = 480V, T_C = 125^\circ\text{C}$	-	-	10	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	$\pm 100$	nA

### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	2.5	-	3.5	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 5A$	-	0.33	0.38	$\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 20V, I_D = 5A$	-	11	-	S

### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V$ $f = 1\text{MHz}$	-	1250	1665	pF
$C_{oss}$	Output Capacitance		-	905	1205	pF
$C_{rss}$	Reverse Transfer Capacitance		-	45	60	pF
$C_{oss}$	Output Capacitance	$V_{DS} = 380V, V_{GS} = 0V, f = 1.0\text{MHz}$	-	23	-	pF
$C_{oss \text{ eff.}}$	Effective Output Capacitance	$V_{DS} = 0V \text{ to } 480V, V_{GS} = 0V$	-	95	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10V	$V_{DS} = 380V, I_D = 5A$ $V_{GS} = 10V$	-	30	40	nC
$Q_{gs}$	Gate to Source Gate Charge		-	5	-	nC
$Q_{gd}$	Gate to Drain "Miller" Charge		(Note 4)	-	10	-
ESR	Equivalent Series Resistance	Drain Open	-	1	-	$\Omega$

### Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 380V, I_D = 5A$ $V_{GS} = 10V, R = 4.7\Omega$	-	14	38	ns
$t_r$	Turn-On Rise Time		-	7	24	ns
$t_{d(off)}$	Turn-Off Delay Time		-	45	100	ns
$t_f$	Turn-Off Fall Time		(Note 4)	-	6	22

### Drain-Source Diode Characteristics

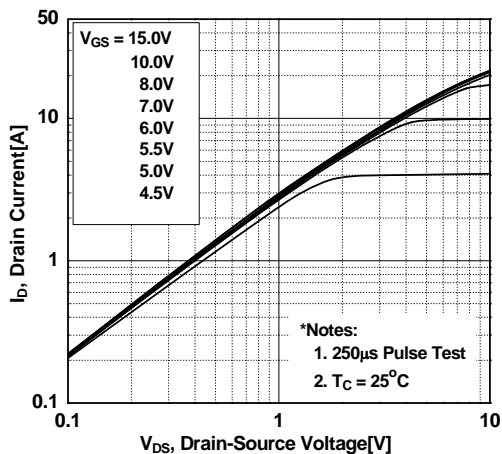
$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	10.2	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	30.6	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{SD} = 5A$	-	-	1.2	V
$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0V, I_{SD} = 5A$	-	240	-	ns
$Q_{rr}$	Reverse Recovery Charge	$di_F/dt = 100A/\mu\text{s}$	-	2.7	-	$\mu\text{C}$

#### Notes:

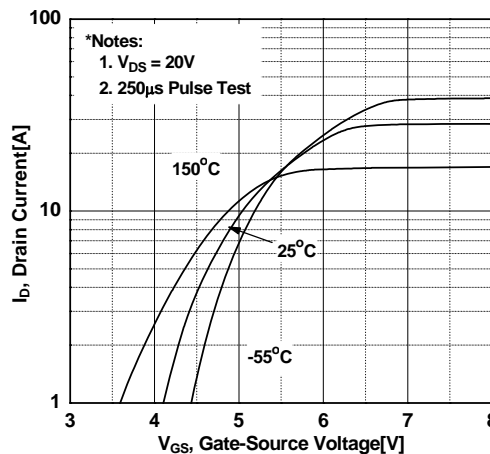
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2.  $I_{AS} = 2.3A, V_{DD} = 50V, R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
3.  $I_{SD} \leq 5.1A, di/dt \leq 200A/\mu\text{s}, V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$
4. Essentially Independent of Operating Temperature Typical Characteristics

## Typical Performance Characteristics

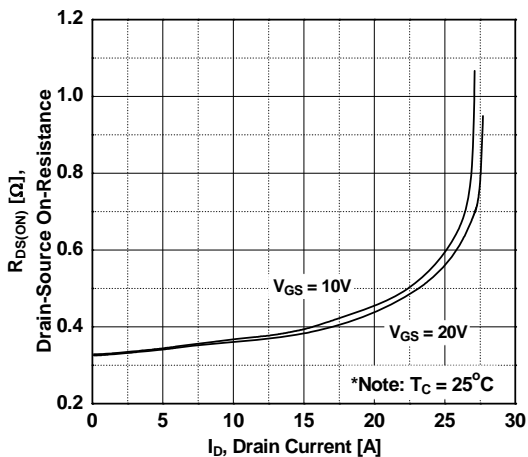
**Figure 1. On-Region Characteristics**



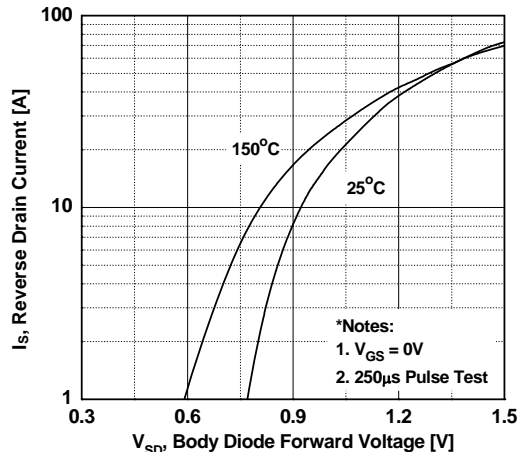
**Figure 2. Transfer Characteristics**



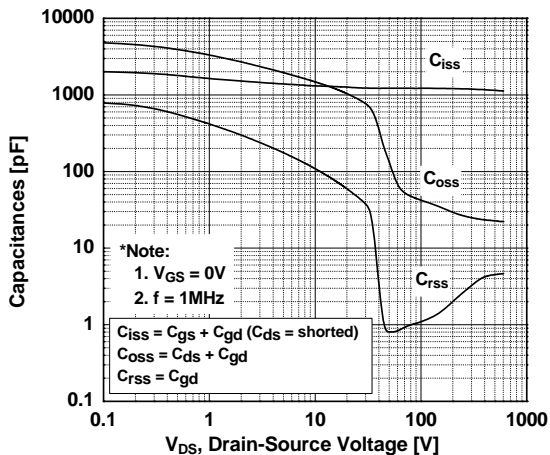
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



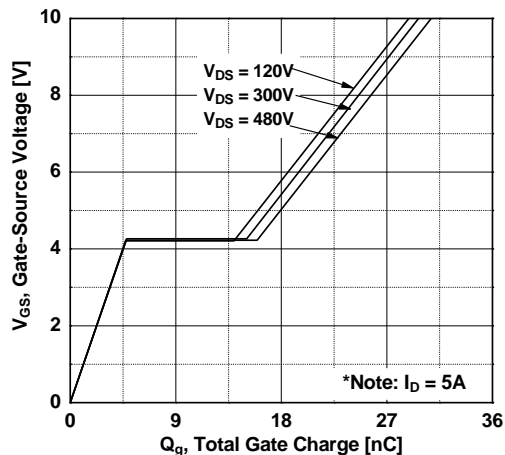
**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**



**Figure 5. Capacitance Characteristics**

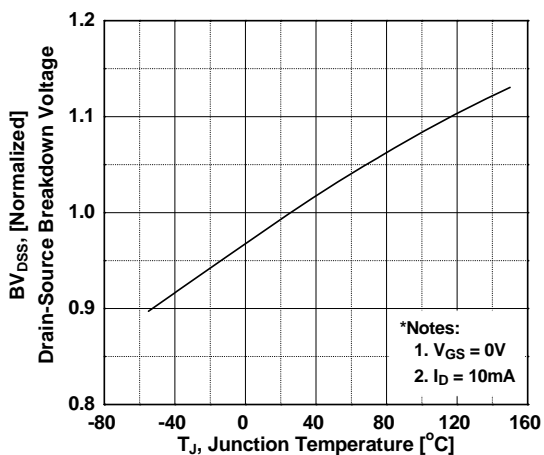


**Figure 6. Gate Charge Characteristics**

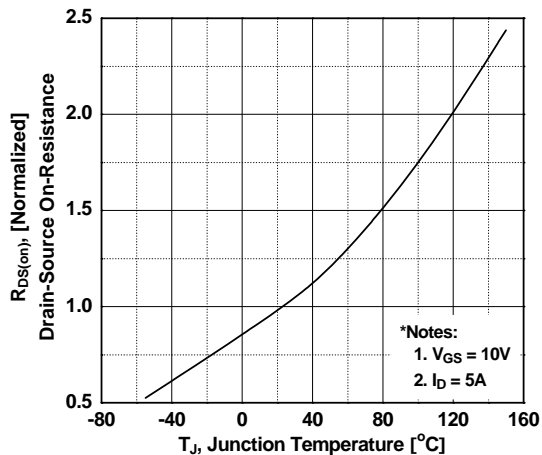


## Typical Performance Characteristics (Continued)

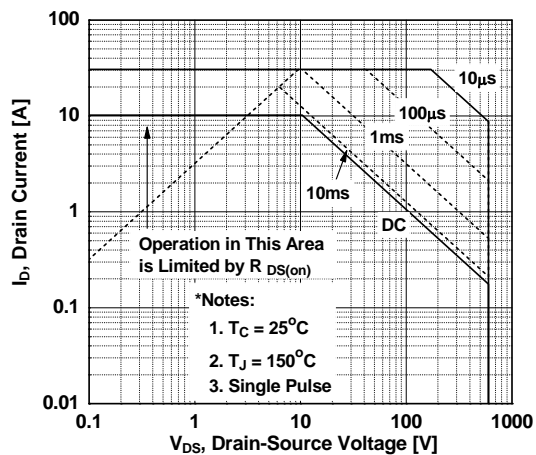
**Figure 7. Breakdown Voltage Variation vs. Temperature**



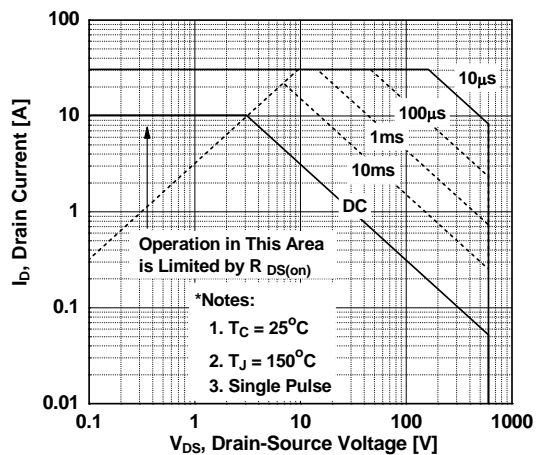
**Figure 8. On-Resistance Variation vs. Temperature**



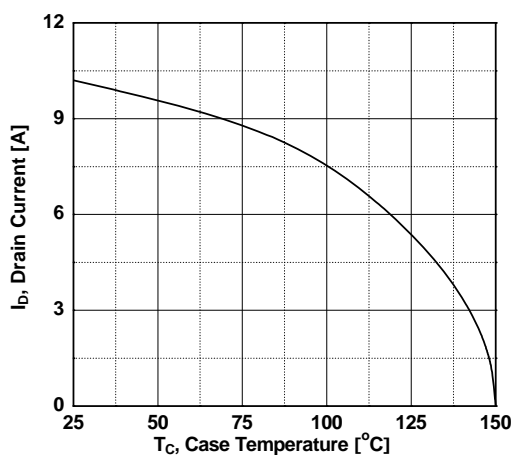
**Figure 9. Maximum Safe Operating Area vs. Case Temperature - FCP380N60**



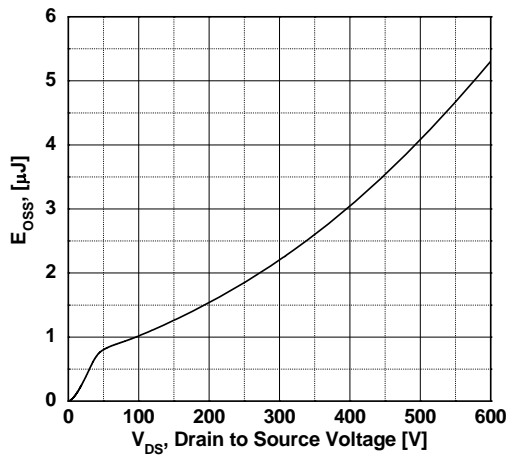
**Figure 10. Maximum Safe Operating Area vs. Case Temperature - FCPF380N60**



**Figure 11. Maximum Drain Current**



**Figure 12. E<sub>oss</sub> vs. Drain to Source Voltage Switching Capability**



Typical Performance Characteristics (Continued)

Figure 13. Transient Thermal Response Curve - FCP380N60

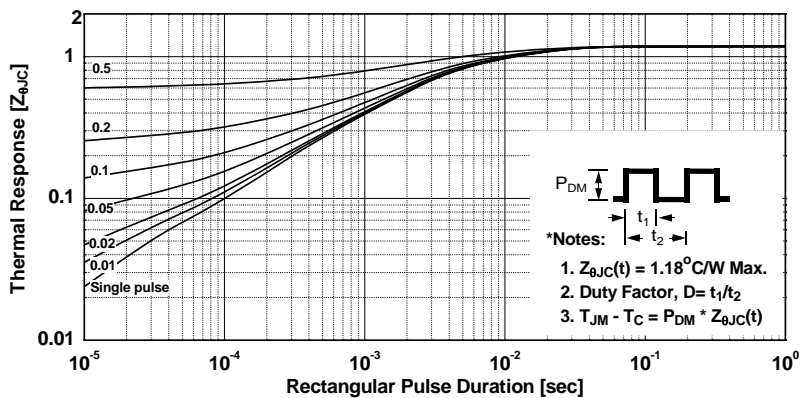
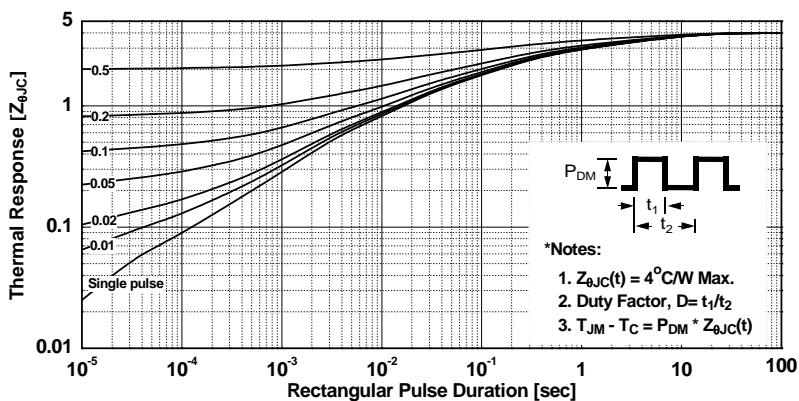
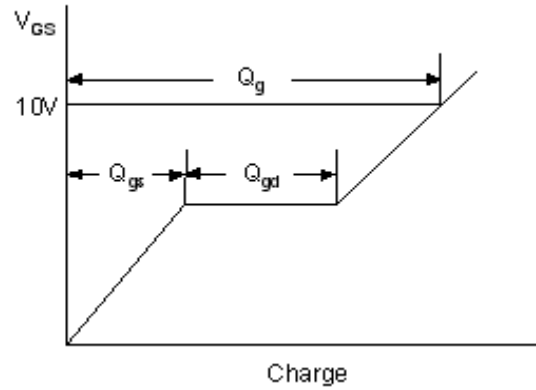
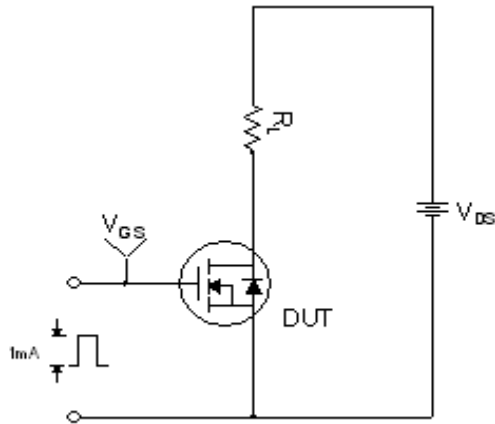


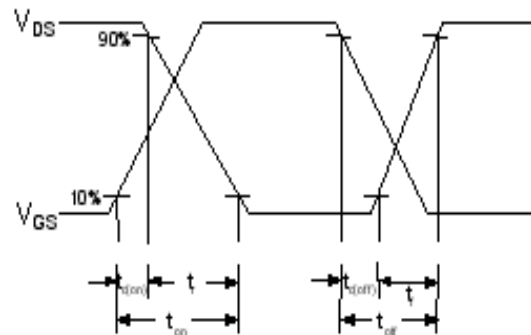
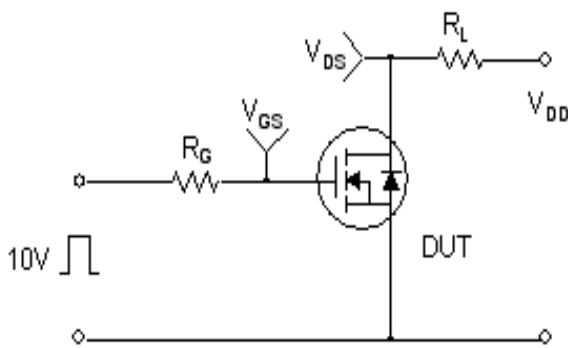
Figure 14. Transient Thermal Response Curve - FCPF380N60



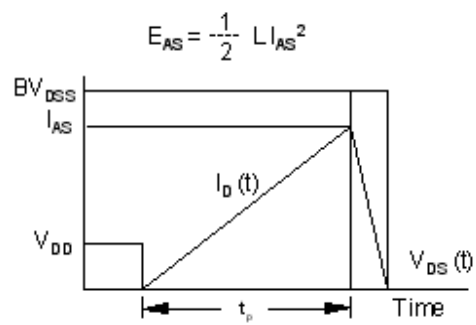
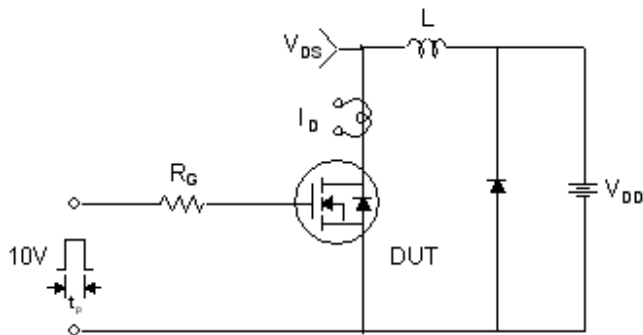
Gate Charge Test Circuit & Waveform



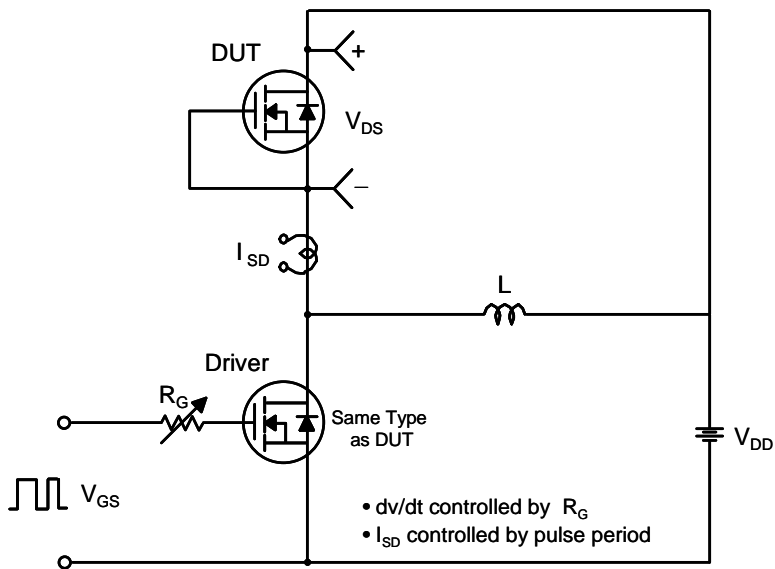
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

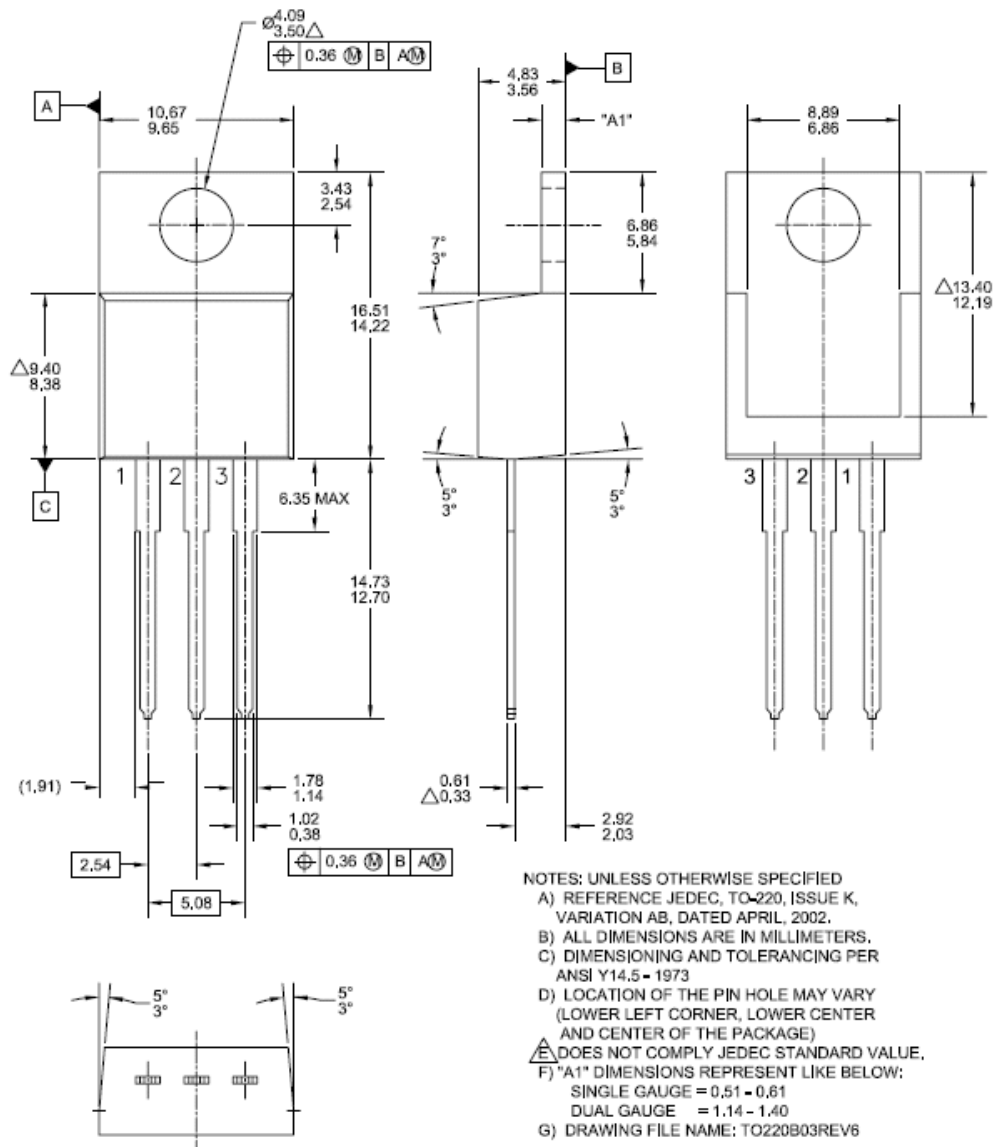


Peak Diode Recovery dv/dt Test Circuit & Waveforms



**Mechanical Dimensions**

**TO-220**

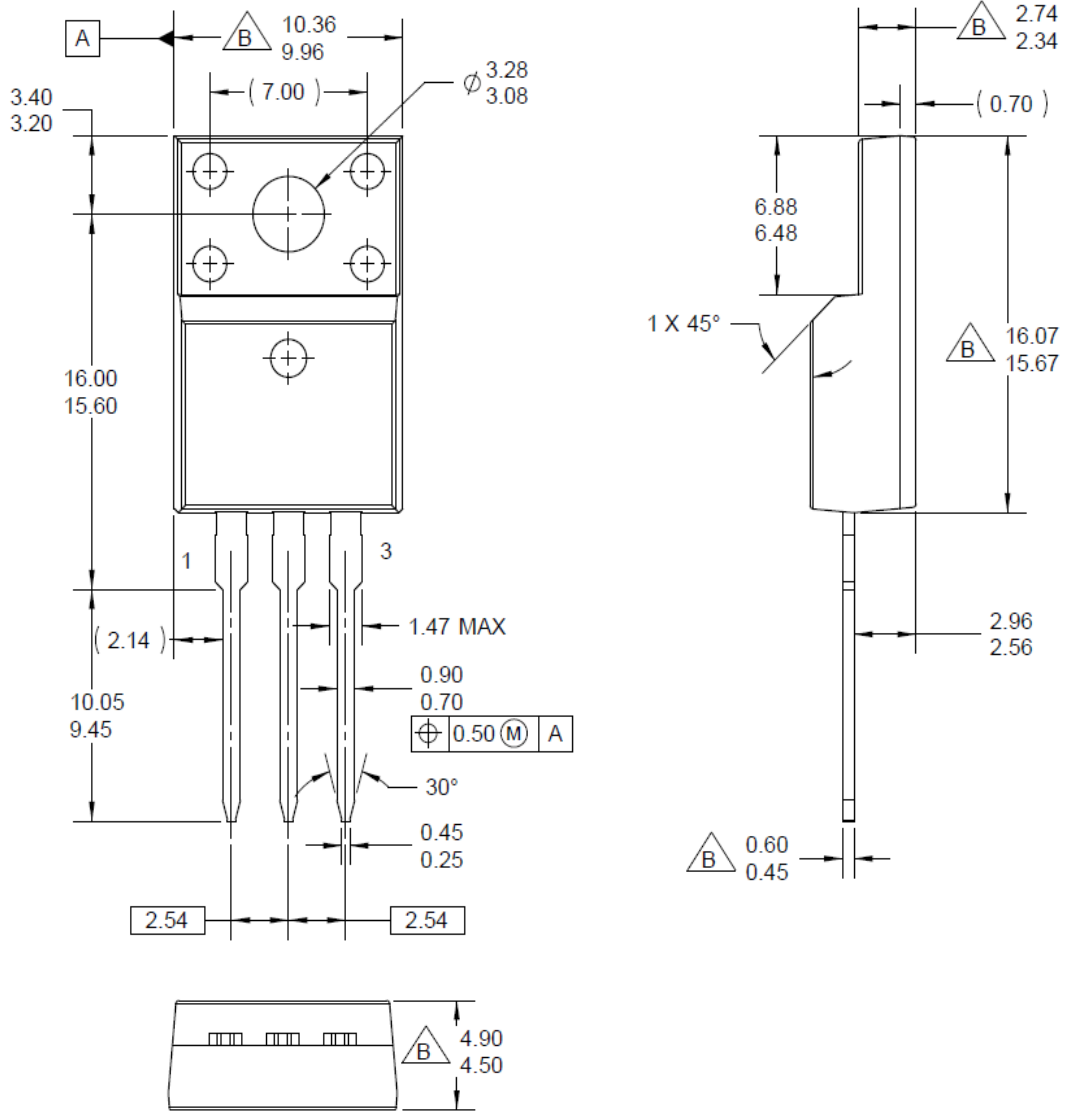


Dimensions in Millimeters



Package Dimensions

TO-220F (Retractable)



\* Front/Back Side Isolation Voltage : AC 2500V

Dimensions in Millimeters



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