

FCP190N60E / FCPF190N60E 600V N-Channel MOSFET

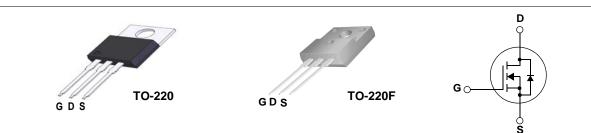
Features

- 650V @T_J = 150°C
- Max. R_{DS(on)} = 190mΩ
- Ultra Low Gate Charge (Typ. Q_g = 63nC)
- Low Effective Output Capacitance (Typ. C_{oss}.eff = 178pF)
- 100% Avalanche Tested

Description

SuperFET[®]II is, Fairchild's proprietary, new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET[®]II is very suitable for various AC/DC power conversion in switching mode operation for system miniaturization and higher efficiency.



MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter			FCP190N60E	FCPF190N60E	Units	
V _{DSS}	Drain to Source Voltage			600		V	
V _{GSS}		- DC	- DC		±20		
	Gate to Source Voltage	- AC	- AC (f > 1Hz)		±30		
-	Drain Current	-Continuous (T _C = 25°C)		20.6	20.6*	A	
D		-Continuous ($T_C = 100^{\circ}C$)		13.1	13.1*		
I _{DM}	Drain Current	- Pulsed (Note 1)		61.8	61.8*	Α	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		400		mJ		
I _{AR}	Avalanche Current		(Note 1)	4.0		Α	
E _{AR}	Repetitive Avalanche Energy		(Note 1)	2.1		mJ	
Peak Diode Recovery		(Note 3)		20		1/20	
dv/dt	MOSFET dv/dt			100		V/ns	
6	David Diacidation	$(T_{C} = 25^{\circ}C)$		208	39	W	
P _D	Power Dissipation	- Derate above 25°C		1.67	0.31	W/ºC	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C		
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300		°C		

*Drain current limited by maximum junction temperature

Thermal Characteristics

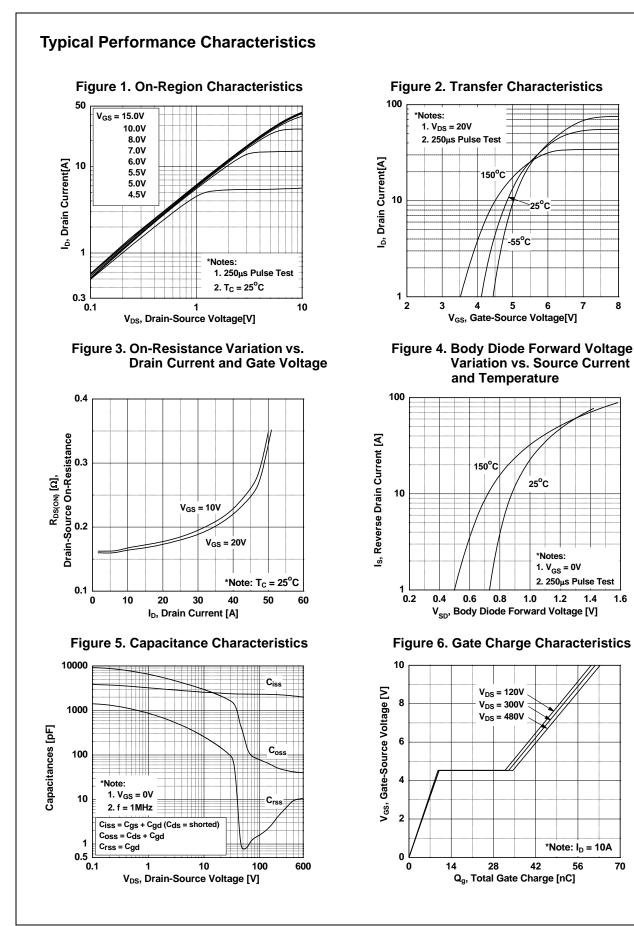
Symbol	Parameter	FCP190N60E	FCPF190N60E	Units
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	0.6	3.2	
$R_{\theta CS}$	Thermal Resistance, Case to Heat Sink (Typical)		0.5	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	62.5	62.5	

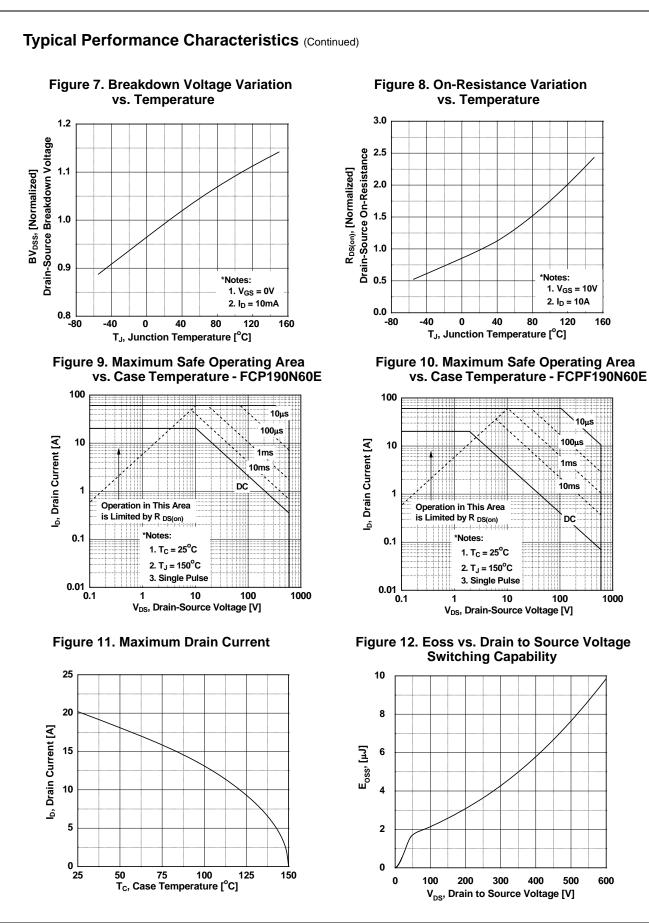
March 2012

SuperFET[®] II

Device Marking Device Pac		Packa	ge	Reel Size	Таре	e Width		Quantit	у	
		TO-22	20	-		-		50		
FCPF190	N60E	FCPF190N60E	TO-22	0F	-		-		50	
Electrica	I Char	acteristics T _c =	25ºC unless	otherwi	se noted					
Symbol		Parameter			Test Conditions		Min.	Тур.	Max.	Units
Off Charac	teristic	S								
BV _{DSS}	Drain to Source Breakdown Voltage		$V_{GS} = 0V, I_D = 10mA, T_J = 25^{\circ}C$		600	-	-	V		
				$V_{GS} = 0V, I_D = 10mA, T_J = 150^{\circ}C$		650	-	-	V	
ΔBV _{DSS} ΔΤ.ι	Coeffic	own Voltage Temperatu ient	lie	I _D = 1	$I_D = 10mA$, Referenced to $25^{\circ}C$		-	0.67	-	V/ºC
BV _{DS}	Drain-S	Source Avalanche Break	down	$y_{-} = 0y_{-} = 200$		_	700	_	V	
50	Voltage				V _{GS} = 0V, I _D = 20A		-	700	-	v
I _{DSS}	Zero G	ate Voltage Drain Curre	nt	20	$V_{DS} = 480V, V_{GS} = 0V$		-	-	1	μA
	0	- Ourse of			$480V, T_{C} = 125^{\circ}C$		-	-	10	
I _{GSS}	Gate to	Body Leakage Current		V _{GS} =	$\pm 20V, V_{DS} = 0V$		-	-	±100	nA
On Charac	teristic	S								
V _{GS(th)}		hreshold Voltage			$V_{DS}, I_{D} = 250 \mu A$		2.5	-	3.5	V
R _{DS(on)}		Prain to Source On Res	stance	660 , D		-	0.16	0.19	Ω	
9fs	Forward Transconductance		$V_{DS} = 20V, I_{D} = 10A$		-	20	-	S		
Dynamic C	haract	eristics								
C _{iss}	Input C	ut Capacitance			-	2385	3175	pF		
C _{oss}	Output	Capacitance			V _{DS} = 25V, V _{GS} = 0V f = 1MHz		-	1795	2396	pF
C _{rss}	Revers	e Transfer Capacitance				-	110	165	pF	
C _{oss}	Output	put Capacitance		$V_{DS} = 380V, V_{GS} = 0V, f = 1.0MHz$		-	42	-	pF	
C _{oss} eff.	Effectiv	tive Output Capacitance		$V_{DS} = 0V$ to 480V, $V_{GS} = 0V$		-	178	-	pF	
Q _{g(tot)}	Total G	ate Charge at 10V				_	-	63	82	nC
Q _{gs}	Gate to	Source Gate Charge		V _{DS} = 380V, I _D = 10A V _{GS} = 10V (Note 4)		-	10	-	nC	
Q _{gd}	Gate to	Drain "Miller" Charge				-	24	-	nC	
ESR	Equivalent Series Resistance		Drain open		-	5	-	Ω		
Switching	Charac	toristics			-	I.				
t _{d(on)}	1	n Delay Time					-	23	56	ns
t _r		n Rise Time		$V_{DD} = 380V, I_D = 10A$ $V_{GS} = 10V, R_G = 4.7\Omega$		-	14	38	ns	
t _{d(off)}		f Delay Time				-	101	212	ns	
t _f		f Fall Time				(Note 4)	-	15	40	ns
	ce Dio	de Characteristics		I						1
I _s	1	m Continuous Drain to		le Forwa	ard Current		-	-	20.6	A
I _{SM}	Maximum Pulsed Drain to Source Diode Fo				-	-	61.8	Α		
V _{SD}		Drain to Source Diode Forward Voltage		$V_{GS} = 0V, I_{SD} = 10A$		-	-	1.2	V	
t _{rr}		Reverse Recovery Time		$V_{GS} = 0V, I_{SD} = 10A$		-	308	-	ns	
Q _{rr}	Reverse Recovery Charge		$dI_{F}/dt = 100A/\mu s$		-	4.8	-	μC		
Notes:	1			·					I.	
	-	th limited by maximum junction	temperature							
2. I _{AS} = 4A, V _{DD} = 8. I _{SD} ≤ 10A, di/dt ⊴		5Ω, Starting $T_J = 25^{\circ}C$								



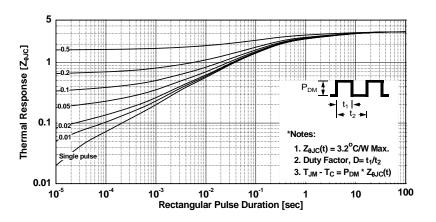






Typical Performance Characteristics (Continued) Figure 13. Transient Thermal Response Curve - FCP190N60E Thermal Response [Z_{6JC}] 0.5 0.2 P_{DM} 0.1 0.1 Notes: າ ດ: 1. $Z_{\theta JC}(t) = 0.6^{\circ}C/W$ Max. 2. Duty Factor, D= t₁/t₂ 3. T_{JM} - $T_C = P_{DM} * Z_{\theta JC}(t)$ 0.01 10⁻³ 10⁻⁵ 10⁻⁴ 10⁻² **10**⁻¹ 1 **Rectangular Pulse Duration [sec]**

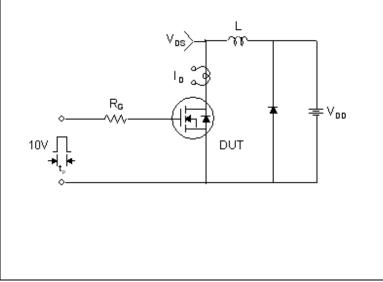


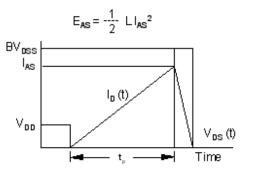






Unclamped Inductive Switching Test Circuit & Waveforms

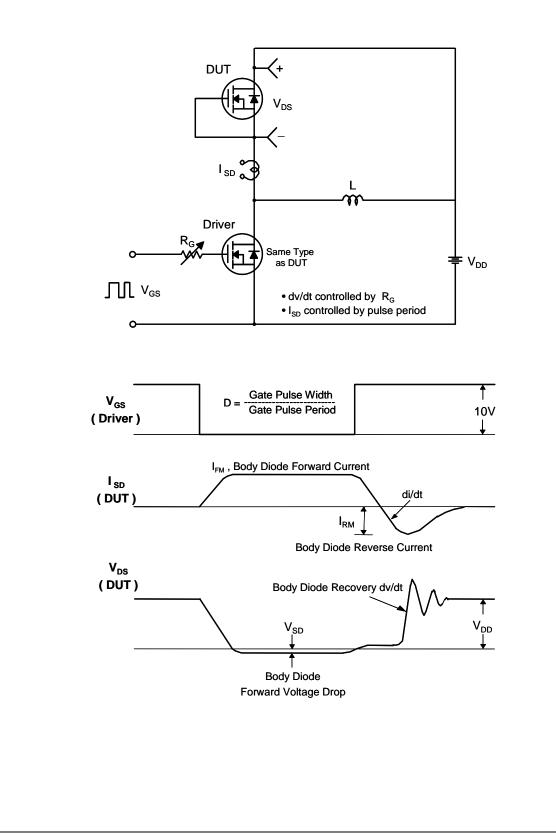


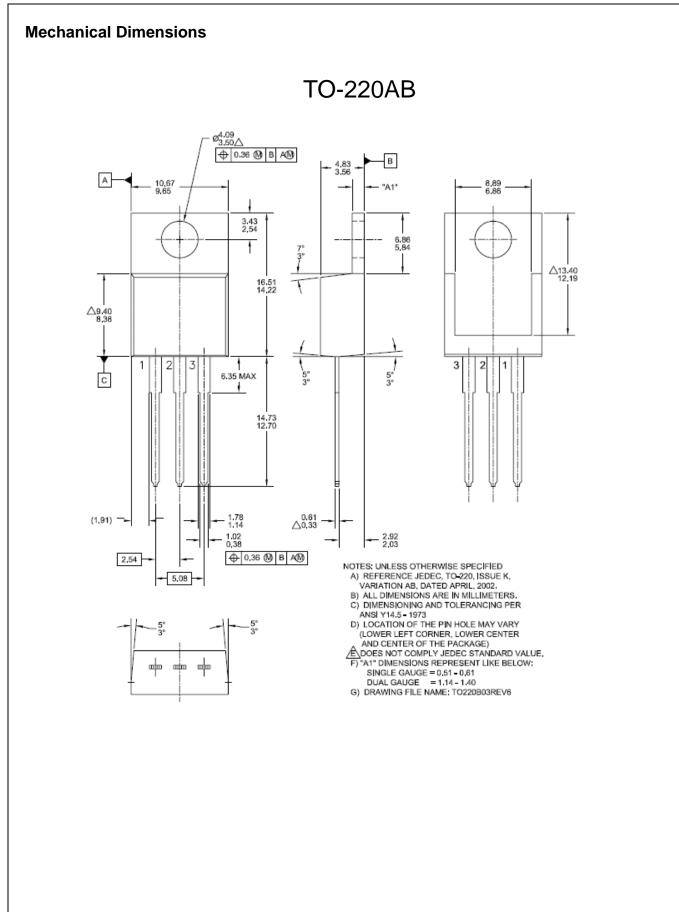


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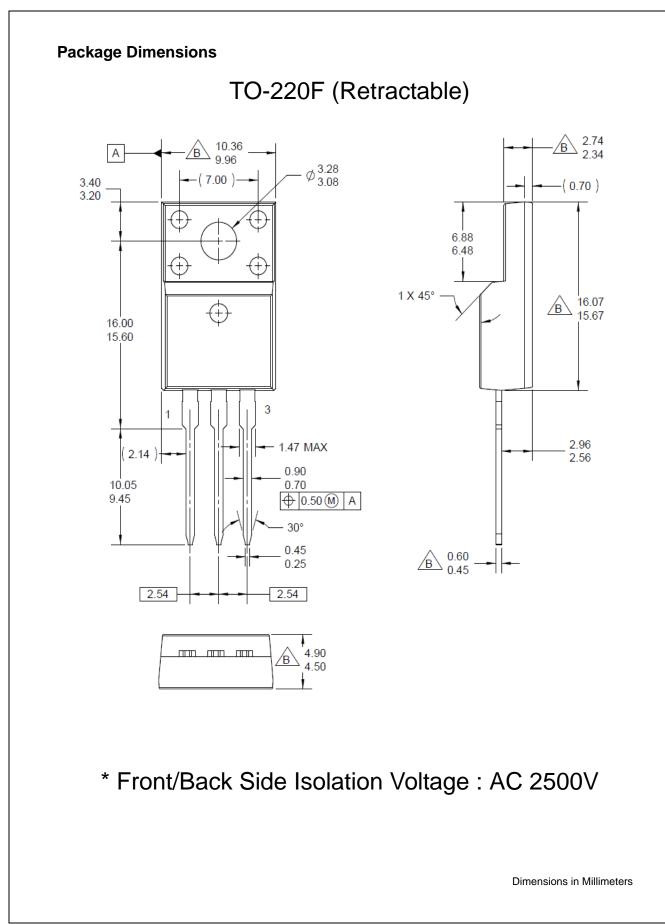
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