

# Dual N-channel MOSFET

## ELM34814AA-N

### ■General description

ELM34814AA-N uses advanced trench technology to provide excellent  $R_{ds(on)}$ , low gate charge and low gate resistance.

### ■Features

- $V_{ds}=30V$
- $I_d=7A$
- $R_{ds(on)} < 23m\Omega$  ( $V_{gs}=10V$ )
- $R_{ds(on)} < 30m\Omega$  ( $V_{gs}=4.5V$ )

### ■Maximum absolute ratings

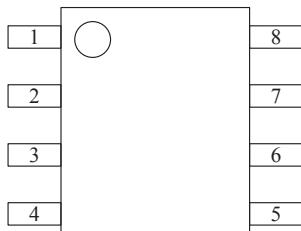
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	$V_{ds}$	$\pm 30$	V	
Gate-source voltage	$V_{gs}$	$\pm 20$	V	
Continuous drain current Ta=25°C	$I_d$	7	A	3
Ta=70°C	$I_d$	6		
Pulsed drain current	$I_{dm}$	40	A	
Power dissipation Ta=25°C	$P_d$	2.0	W	
Ta=70°C	$P_d$	1.3		
Junction and storage temperature range	$T_j, T_{stg}$	-55 to 150	°C	

### ■Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	Steady-state	$R_{\theta ja}$		62.5	°C/W	

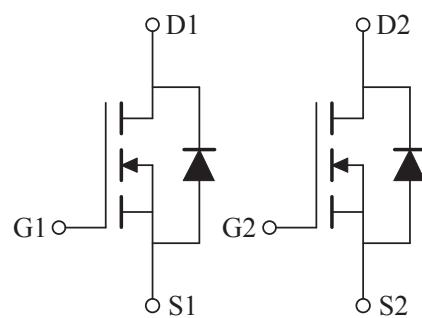
### ■Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE1
2	GATE1
3	SOURCE2
4	GATE2
5	DRAIN2
6	DRAIN2
7	DRAIN1
8	DRAIN1

### ■Circuit



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### ■Electrical characteristics

$T_a=25^\circ C$

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
<b>STATIC PARAMETERS</b>							
Drain-source breakdown voltage	BVdss	$I_d=250\mu A, V_{gs}=0V$	30			V	
Zero gate voltage drain current	Idss	$V_{ds}=24V, V_{gs}=0V$			1	$\mu A$	
		$V_{ds}=20V, V_{gs}=0V, T_j=55^\circ C$			10		
Gate-body leakage current	Igss	$V_{ds}=0V, V_{gs}=\pm 20V$			$\pm 100$	nA	
Gate threshold voltage	Vgs(th)	$V_{ds}=V_{gs}, I_d=250\mu A$	1.0	1.5	3.0	V	
On state drain current	Id(on)	$V_{gs}=10V, V_{ds}=5V$	25			A	1
Static drain-source on-resistance	Rds(on)	$V_{gs}=10V, I_d=7A$		15	23	$m\Omega$	1
		$V_{gs}=4.5V, I_d=6A$		21	30	$m\Omega$	
Forward transconductance	Gfs	$V_{ds}=15V, I_d=5A$		16		S	1
Diode forward voltage	Vsd	$I_f=1A, V_{gs}=0V$			1	V	1
Max.body-diode continuous current	Is				3	A	
Pulsed current	Ism				6	A	3
<b>DYNAMIC PARAMETERS</b>							
Input capacitance	Ciss	$V_{gs}=0V, V_{ds}=15V, f=1MHz$		830		pF	
Output capacitance	Coss			185		pF	
Reverse transfer capacitance	Crss			80		pF	
<b>SWITCHING PARAMETERS</b>							
Total gate charge	Qg	$V_{gs}=5V, V_{ds}=15V, I_d=7A$		9.0	13.0	nC	2
Gate-source charge	Qgs			2.8		nC	2
Gate-drain charge	Qgd			3.1		nC	2
Turn-on delay time	td(on)	$V_{gs}=10V, V_{ds}=15V, I_d \approx 1A$ $R_{gen}=6\Omega$		5.7		ns	2
Turn-on rise time	tr			10.0		ns	2
Turn-off delay time	td(off)			18.0		ns	2
Turn-off fall time	tf			5.0		ns	2
Body diode reverse recovery time	trr	$I_f=5A, dI/dt=100A/\mu s$		15.5		ns	
Body diode reverse recovery charge	Qrr			7.9		nC	

#### NOTE :

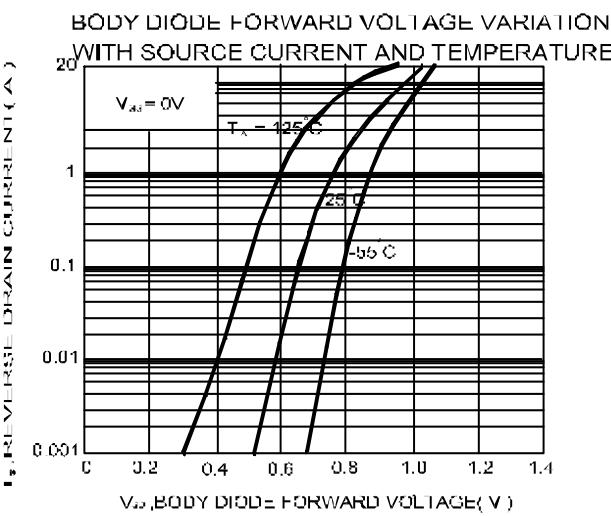
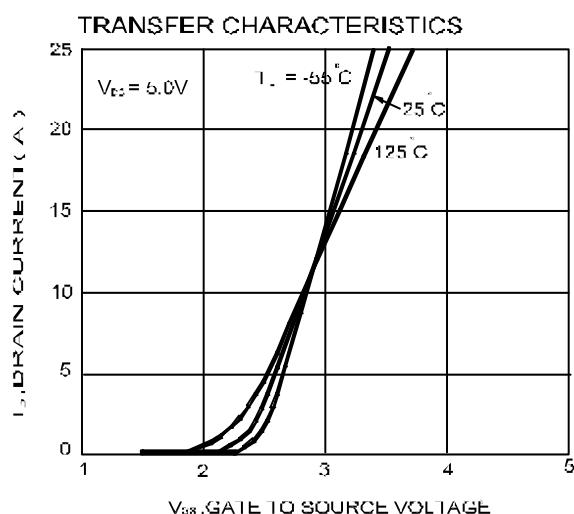
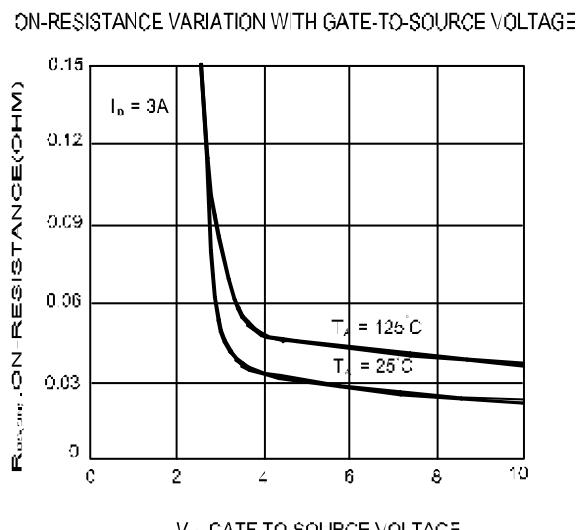
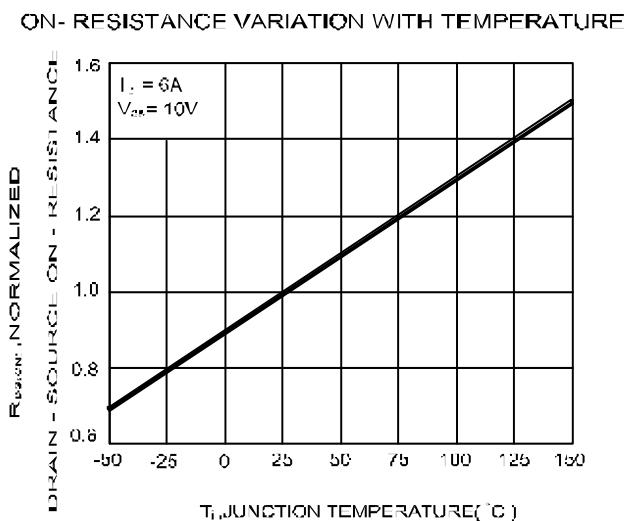
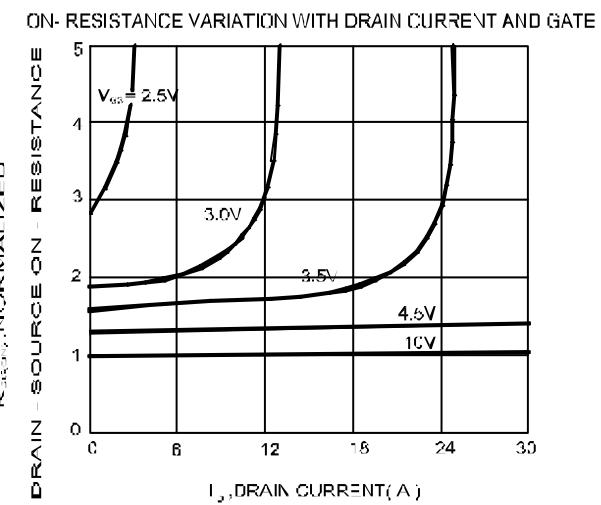
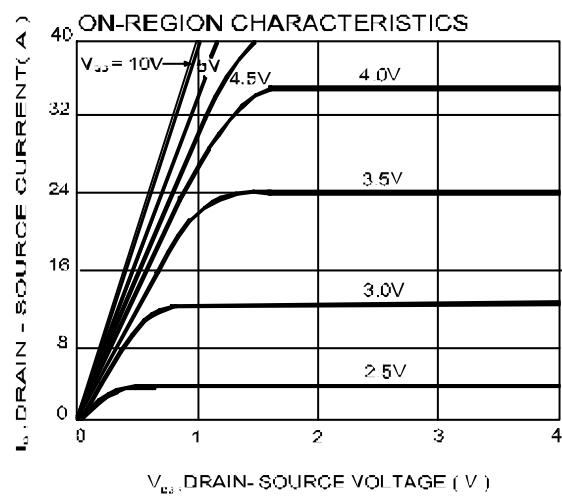
1. Pulsed width  $\leq 300\mu sec$  and Duty cycle  $\leq 2\%$ .
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle  $\leq 1\%$ .



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### ■Typical electrical and thermal characteristics



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