PNP Silicon General Purpose High Voltage Transistor

This PNP Silicon Planar Transistor is designed for general purpose amplifier applications. This device is housed in the SC-59 package which is designed for low power surface mount applications.

Features

• This is a Pb-Free Device

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Value	Unit
Collector-Base Voltage	V _{(BR)CBO}	-300	Vdc
Collector-Emitter Voltage	V _{(BR)CEO}	-300	Vdc
Emitter-Base Voltage	V _{(BR)EBO}	-5.0	Vdc
Collector Current - Continuous	I _C	150	mAdc

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation (Note 1)	P_{D}	150	mW
Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{stg}	-55~+150	°C

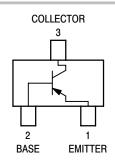
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

 Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.



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MARKING DIAGRAM



SC-59 CASE 318D STYLE 1



J2D = Device Marking Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MSB92T1G	SC-59 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Min	Max	Unit
Collector-Emitter Breakdown Voltage (I _C = -1.0 mAdc, I _B = 0)	V _(BR) CEO	-300	-	Vdc
Collector-Base Breakdown Voltage $(I_C = -100 \mu Adc, I_E = 0)$	V _{(BR)CBO}	-300	-	Vdc
Emitter-Base Breakdown Voltage (I _E = -100 μAdc, I _E = 0)	V _{(BR)EBO}	-5.0	-	Vdc
Collector-Base Cutoff Current (V _{CB} = -200 Vdc, I _E = 0)	I _{CBO}	-	-0.25	μΑ
Emitter-Base Cutoff Current (V _{EB} = -3.0 Vdc, I _B = 0)	I _{EBO}	-	-0.1	μΑ
DC Current Gain (Note 2)	h _{FE1} h _{FE2} h _{FE3}	25 40 25	- - -	-
Collector-Emitter Saturation Voltage (I _C = -20 mAdc, I _B = -2.0 mAdc)	V _{CE(sat)}	_	-0.5	Vdc
Base-Emitter Saturation Voltage (I _C = -20 mAdc, I _B = -2.0 mAdc)	V _{BE(sat)}	_	-0.9	Vdc
SMALL SIGNAL CHARACTERISTICS	<u>.</u>			
Current - Gain - Bandwidth Product	f	50		МНэ

Current - Gain - Bandwidth Product (I _C = -10 mAdc, V _{CE} = -20 Vdc, f = 20 MHz)	f⊤	50	-	MHz
Collector-Base Capacitance (V _{CB} = -20 Vdc, I _E = 0, f = 1.0 MHz)	C _{cb}	-	6.0	pF

^{2.} Pulse Test: Pulse Width ≤ 300 μs, D.C. ≤ 2%.

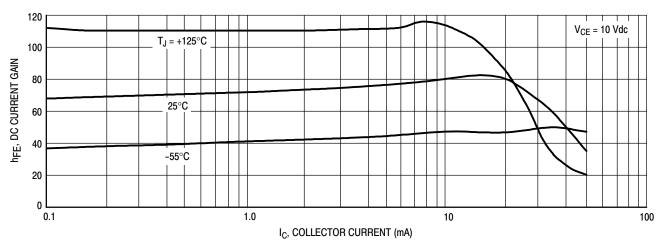


Figure 1. DC Current Gain

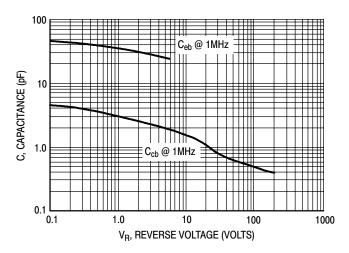


Figure 2. Capacitance

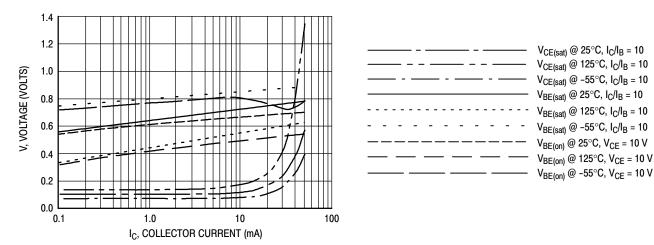
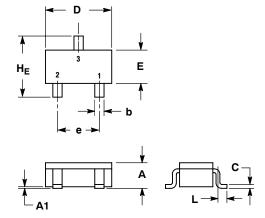


Figure 3. "ON" Voltages

PACKAGE DIMENSIONS

SC-59 CASE 318D-04 ISSUE G

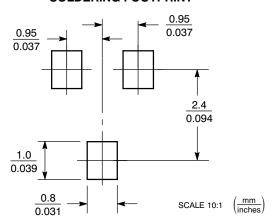


- DIMENSIONING AND TOLERANCING PER ANSI
- 2. CONTROLLING DIMENSION: MILLIMETER.

	М	MILLIMETERS INCHES				
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.00	1.15	1.30	0.039	0.045	0.051
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.35	0.43	0.50	0.014	0.017	0.020
С	0.09	0.14	0.18	0.003	0.005	0.007
D	2.70	2.90	3.10	0.106	0.114	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
е	1.70	1.90	2.10	0.067	0.075	0.083
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.80	3.00	0.099	0.110	0.118

PIN 1. EMITTER
2. BASE
3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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