

## Small Signal MOSFET 115 mAmps, 60 Volts N-Channel SOT-23

- RoHS product for packing code suffix "G"  
Halogen free product for packing code suffix "H"
- Weight: 0.008g

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	60	Vdc
Drain-Gate Voltage ( $R_{GS} = 1.0 \text{ M}\Omega$ )	$V_{DGR}$	60	Vdc
Drain Current – Continuous $T_C = 25^\circ\text{C}$ (Note 1.) $T_C = 100^\circ\text{C}$ (Note 1.) – Pulsed (Note 2.)	$I_D$ $I_D$ $I_{DM}$	$\pm 115$ $\pm 75$ $\pm 800$	mAdc
Gate-Source Voltage – Continuous – Non-repetitive ( $t_p \leq 50 \mu\text{s}$ )	$V_{GS}$ $V_{GSM}$	$\pm 20$ $\pm 40$	Vdc Vpk

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 3.) $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	225 1.8	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate, (Note 4.) $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	300 2.4	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

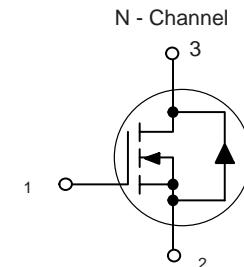
1. The Power Dissipation of the package may result in a lower continuous drain current.
2. Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .
3. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
4. Alumina =  $0.4 \times 0.3 \times 0.025$  in 99.5% alumina.

**2N7002LT1**

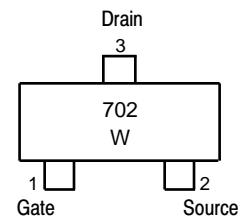


SOT-23 (TO-236AB)

**115 mAmps  
60 Volts  
 $R_{DS(on)} = 7.5 \Omega$**



### MARKING DIAGRAM & PIN ASSIGNMENT



702 = Device Code  
W = Work Week

### ORDERING INFORMATION

Device	Marking	Shipping
2N7002LT1	702	3000 Tape & Reel

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## 2N7002LT1

### ELECTRICAL CHARACTERISTICS (TA = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Drain–Source Breakdown Voltage (V <sub>GS</sub> = 0, I <sub>D</sub> = 10 µAdc)	V <sub>(BR)DSS</sub>	60	—	—	Vdc
Zero Gate Voltage Drain Current (V <sub>GS</sub> = 0, V <sub>DS</sub> = 60 Vdc) T <sub>J</sub> = 25°C T <sub>J</sub> = 125°C	I <sub>DSS</sub>	— —	— —	1.0 500	µAdc
Gate–Body Leakage Current, Forward (V <sub>GS</sub> = 20 Vdc)	I <sub>GSSF</sub>	—	—	100	nAdc
Gate–Body Leakage Current, Reverse (V <sub>GS</sub> = -20 Vdc)	I <sub>GSSR</sub>	—	—	-100	nAdc

### ON CHARACTERISTICS (Note 2.)

Gate Threshold Voltage (V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 µAdc)	V <sub>GS(th)</sub>	1.0	1.6	2	Vdc
On–State Drain Current (V <sub>DS</sub> ≥ 2.0 V <sub>DS(on)</sub> , V <sub>GS</sub> = 10 Vdc)	I <sub>D(on)</sub>	500	—	—	mA
Static Drain–Source On–State Voltage (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 500 mA) (V <sub>GS</sub> = 5.0 Vdc, I <sub>D</sub> = 50 mA)	V <sub>DS(on)</sub>	— —	— —	3.75 0.375	Vdc
Static Drain–Source On–State Resistance (V <sub>GS</sub> = 10 V, I <sub>D</sub> = 500 mA) T <sub>C</sub> = 25°C T <sub>C</sub> = 125°C (V <sub>GS</sub> = 5.0 Vdc, I <sub>D</sub> = 50 mA) T <sub>C</sub> = 25°C T <sub>C</sub> = 125°C	r <sub>DS(on)</sub>	— — — —	1.4 — 1.8 —	7.5 13.5 7.5 13.5	Ohms
Forward Transconductance (V <sub>DS</sub> ≥ 2.0 V <sub>DS(on)</sub> , I <sub>D</sub> = 200 mA)	g <sub>FS</sub>	80	—	—	mmhos

### DYNAMIC CHARACTERISTICS

Input Capacitance (V <sub>DS</sub> = 25 Vdc, V <sub>GS</sub> = 0, f = 1.0 MHz)	C <sub>iss</sub>	—	17	50	pF
Output Capacitance (V <sub>DS</sub> = 25 Vdc, V <sub>GS</sub> = 0, f = 1.0 MHz)	C <sub>oss</sub>	—	10	25	pF
Reverse Transfer Capacitance (V <sub>DS</sub> = 25 Vdc, V <sub>GS</sub> = 0, f = 1.0 MHz)	C <sub>rss</sub>	—	2.5	5.0	pF

### SWITCHING CHARACTERISTICS (Note 2.)

Turn-On Delay Time	(V <sub>DD</sub> = 25 Vdc, I <sub>D</sub> ≈ 500 mA, R <sub>G</sub> = 25 Ω, R <sub>L</sub> = 50 Ω, V <sub>gen</sub> = 10 V)	t <sub>d(on)</sub>	—	7	20	ns
Turn-Off Delay Time		t <sub>d(off)</sub>	—	11	40	ns

### BODY–DRAIN DIODE RATINGS

Diode Forward On–Voltage (I <sub>S</sub> = 115 mA, V <sub>GS</sub> = 0 V)	V <sub>SD</sub>	—	—	-1.5	Vdc
Source Current Continuous (Body Diode)	I <sub>S</sub>	—	—	-115	mA
Source Current Pulsed	I <sub>SM</sub>	—	—	-800	mA

2. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.

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## TYPICAL ELECTRICAL CHARACTERISTICS

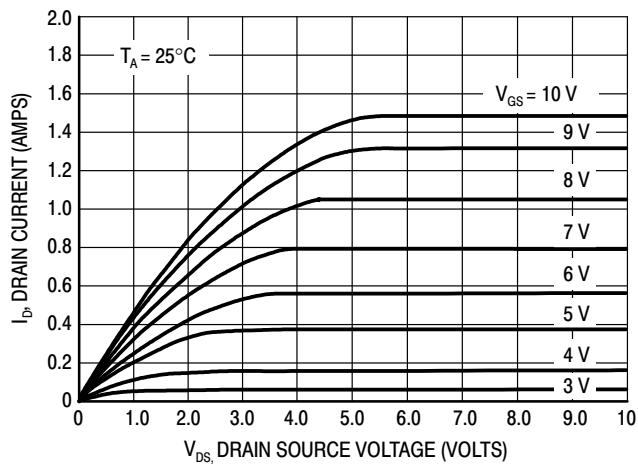


Figure 1. Ohmic Region

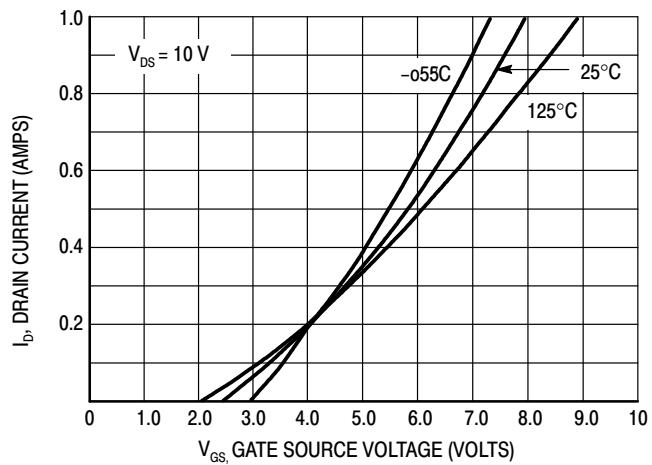


Figure 2. Transfer Characteristics

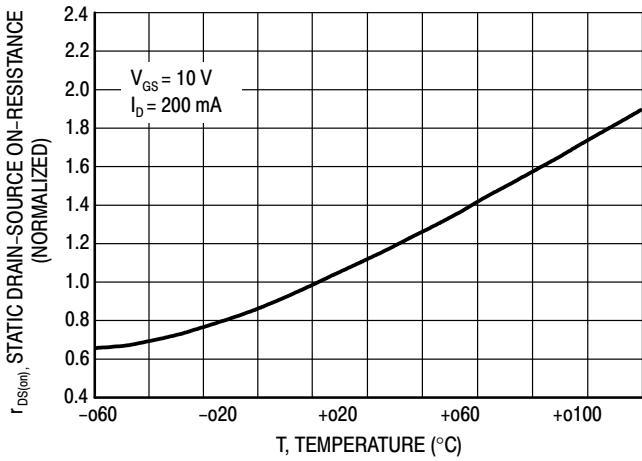


Figure 3. Temperature versus Static Drain–Source On–Resistance

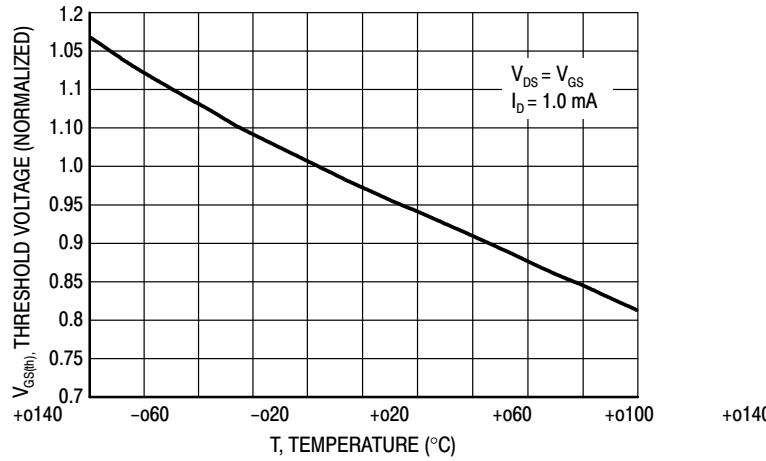
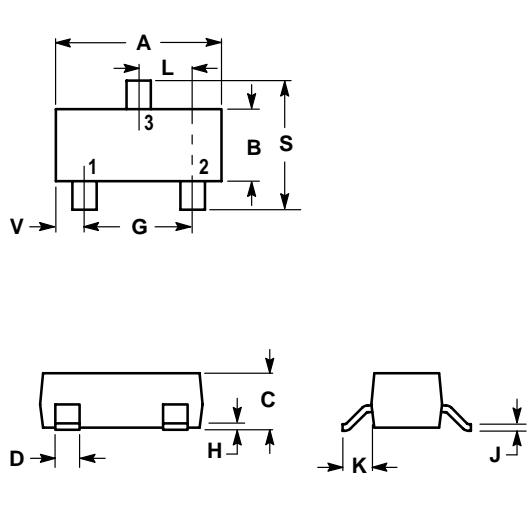


Figure 4. Temperature versus Gate Threshold Voltage

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## SOT-23



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

