

STEVAL-CCA010V1

Demonstration board for video buffer plus filter with SAG correction based on the TSH122

Data Brief

Features

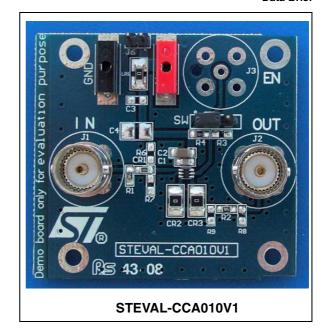
- Operation supply from 2.25 to 5 V single power supply
- Very low consumption: 1.7 mA
- Ultra low power-down mode: 4 nA typ., 500 nA max.
- Internal 6th order reconstruction filter
- Internal gain of 6 dB
- Rail-to-rail output buffer for 75 Ω video line
- Excellent video performances:
 - Differential gain 0.5%
 - Differential phase 0.10°
 - Group delay of 10 ns
- SAG correction
- Bottom of video signal close to 0 V

Description

The demonstration board STEVAL-CCA010V1 is designed to evaluate the TSH122 that is a video buffer that uses a voltage feedback amplifier, with an internal gain of 6 dB, an output rail-to-rail, an internal input DC-shift and a SAG correction. A power-down function allows switching to a sleep mode with an ultra-low consumption.

The TSH122 features a 6th-order internal reconstruction filter to attenuate the parasitic frequency of 27 MHz from the clock of the video DAC.

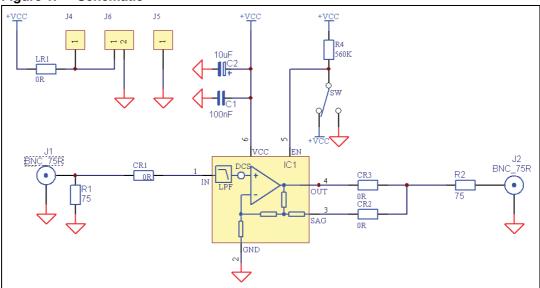
The TSH122 is a single operator available in a tiny SC70 plastic package for space saving.



Circuit schematic STEVAL-CCA010V1

1 Circuit schematic

Figure 1. Schematic



STEVAL-CCA010V1 Revision history

2 Revision history

Table 1. Document revision history

Date	Revision	Changes
02-Feb-2009	1	Initial release.

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