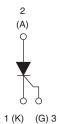


Vishay Semiconductors

Phase Control SCR, 70 A





-247

PRODUCT SUMMARY						
Package	Super TO-247					
Diode variation	Single SCR					
I _{T(AV)}	70 A					
V_{DRM}	1200 V, 1600 V					
V_{TM}	1.4 V					
I _{GT}	100 A					
TJ	- 40 °C to 125 °C					

FEATURES

- High surge capability
- · High voltage input rectification
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level



APPLICATIONS

- · AC switches
- High voltage input rectification (soft start)
- High current crow-bar
- Other phase-control circuits
- Designed to be used with Vishay input diodes, switches, and output rectifiers which are available in identical package outlines

DESCRIPTION

The VS-70TPS..PbF High Voltage Series of silicon controlled rectifiers are specifically designed for high and medium power switching, and phase control applications.

MAJOR RATINGS AND CHARACTERISTICS								
PARAMETER	TEST CONDITIONS	VALUES	UNITS					
I _{T(AV)}	Sinusoidal waveform	70	Λ.					
I _{RMS}	Lead current limitation	75	А					
V_{RRM}/V_{DRM}	Range	1200/1600	V					
I _{TSM}		1400	А					
V _T	100 A, T _J = 25 °C	1.4	V					
dV/dt		500	V/µs					
dl/dt		150	A/µs					
T _J		- 40 to 125	°C					

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA
VS-70TPS12PbF	1200	1300	15
VS-70TPS16PbF	1600	1700	15

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 82 °C, 180° co	T _C = 82 °C, 180° conduction half sine wave			
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}	Lead current limitat	Lead current limitation			А
Maximum peak, one-cycle	I _{TSM}	10 ms sine pulse, ra	ated V _{RRM} applied		1200	
non-repetitive surge current	TSM	10 ms sine pulse, ne	o voltage reapplied	LOCAL T	1400	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, ra	ated V _{RRM} applied	Initial $T_J = T_J$ maximum	7200	A ² s
Waximum 1-t for fusing	1-1	10 ms sine pulse, ne	10 ms sine pulse, no voltage reapplied		10 200	A-5
Maximum I ² √t for fusing	I²√t	t = 0.1 ms to 10 ms,	102 000	A²√s		
Low level value of threshold voltage	V _{T(TO)1}		0.916	V		
High level value of threshold voltage	V _{T(TO)2}	T 405 00			1.21	V
Low level value of on-state slope resistance	r _{t1}	T _J = 125 °C			4.138	~ 0
High level value of on-state slope resistance	r _{t2}					mΩ
Maximum peak on-state voltage	V_{TM}	100 A, T _J = 25 °C			1.4	V
Maximum rate of rise of turned-on current	dl/dt	T _J = 25 °C			150	A/µs
Maximum holding current	I _H				200	
Maximum latching current	ΙL	- T _J = 25 °C			400	
Market and a second discrete and a second		T _J = 25 °C			1.0	mA
Maximum reverse and direct leakage current	I _{RRM} /I _{DRM}	$T_J = 125 ^{\circ}\text{C}$ $V_R = \text{Rated } V_{RRM} / V_{DRM}$		15		
Maximum rate of rise of off-state voltage	dV/dt	T _J = 125 °C			500	V/µs

TRIGGERING					
PARAMETER	SYMBOL		TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P_{GM}	T = 30 µs		10	W
Maximum average gate power	P _{G(AV)}	1 = 30 μs		2.5	\ \ \
Maximum peak gate current	I _{GM}			2.5	Α
Maximum peak negative gate voltage	- V _{GM}			10	
		T _J = - 40 °C		4.0	v
Maximum required DC gate voltage to trigger	V_{GT}	T _J = 25 °C	Anode supply = 6 V resistive load	1.5	ľ
		T _J = 125 °C		1.1	
		T _J = - 40 °C		270	
Maximum required DC gate current to trigger	I_{GT}	T _J = 25 °C		100	mA
		T _J = 125 °C		80	
Maximum DC gate voltage not to trigger	V_{GD}	T _J = 120 °C, V _{DRM} = Rated value		0.25	V
Maximum DC gate current not to trigger	I_{GD}			6	mA

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THERMAL AND MECH	ANICAL S	PECIFICA	TIONS		
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range		TJ		- 40 to 125	°C
Maximum storage temperature	range	T _{Stg}		- 40 to 150	
Maximum thermal resistance, junction to case	,		DC operation	0.27	
Maximum thermal resistance, junction to ambient		R _{thJA}		40	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.2	
Approximate weight				6	g
				0.21	OZ.
Manusting to the second minimum				6 (5)	kgf · cm
Mounting torque	maximum			12 (10)	(lbf · in)
Marking device			Coop atula Super TO 247	70TPS	12
			Case style Super TO-247	70TPS	16

ΔR_{thJ-hs} Conduction per junction											
DEVICE	s	SINE HALF WAVE CONDUCTION REC						RECTANGULAR WAVE CONDUCTION			
DEVICE	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
VS-70TPSPbF	0.078	0.092	0.117	0.172	0.302	0.053	0.092	0.125	0.180	0.306	°C/W

Note

• The table above shows the increment of thermal resistance R_{thJ-hs} when devices operate at different conduction angles than DC

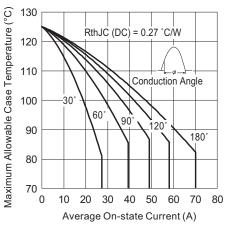


Fig. 1 - Current Rating Characteristics

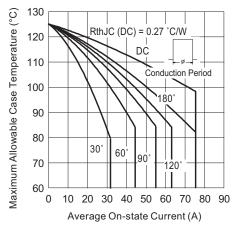


Fig. 2 - Current Rating Characteristics

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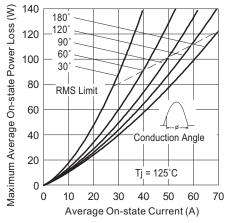


Fig. 3 - On-State Power Loss Characteristics

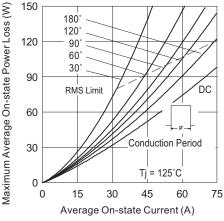


Fig. 4 - On-State Power Loss Characteristics

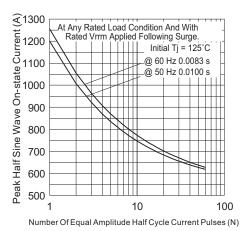


Fig. 5 - Maximum Non-Repetitive Surge Current

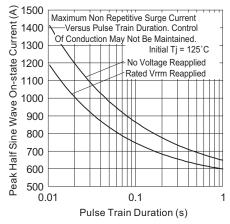


Fig. 6 - Maximum Non-Repetitive Surge Current

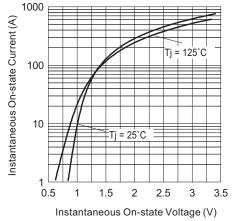


Fig. 7 - On-State Voltage Drop Characteristics

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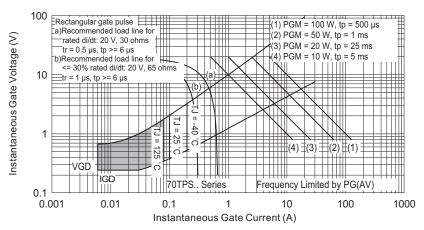


Fig. 8 - Gate Characteristics

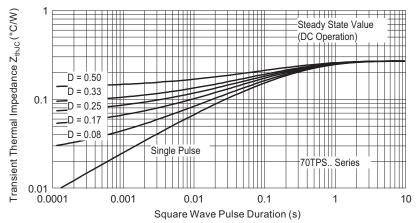


Fig. 9 - Thermal Impedance Z_{thJC} Characteristics

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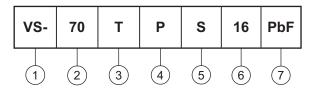


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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (70 = 70 A)

Circuit configuration:

T = Thyristor

4 - Package:

P = Super TO-247

5 - Type of silicon:

S = Standard recovery rectifier

12 = 1200 V 16 = 1600 V

- Voltage code x 100 = V_{RRM}

• None = Standard production

• PbF = Lead (Pb)-free

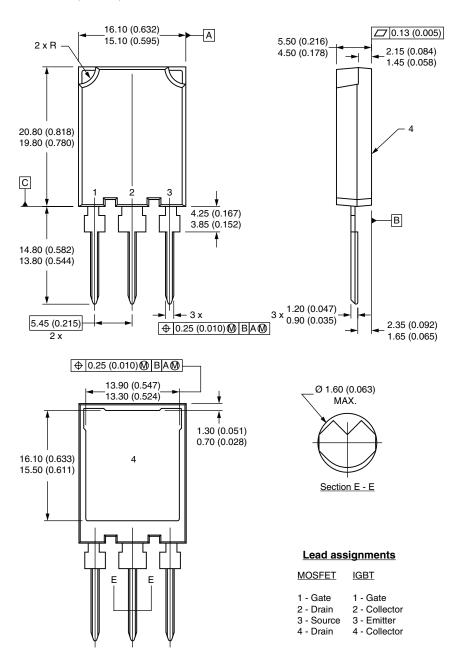
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95073					
Part marking information <u>www.vishay.com/doc?95070</u>					



Vishay High Power Products

Super TO-247

DIMENSIONS in millimeters (inches)



Notes

- (1) Dimension and tolerancing per ASME Y14.5M-1994
- (2) Controlling dimension: millimeter
- (3) Outline conforms to JEDEC outline TO-274AA





Vishay

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