

# LOW CAPACITANCE UNBUMPED FLIP CHIP TVS ARRAY

#### DESCRIPTION

The ULLC0402FC05C Flip Chip employs advanced silicon P/N junction technology for unmatched board-level transient voltage protection against Electrostatic Discharge (ESD) and Electrical Fast Transients (EFT). Developed specifically for high-density circuit protection, this series meets the IEC 61000-4-2 and 61000-4-4 requirements. These devices are ideally suited for handheld devices, PCMCIA and SMART cards.

This low capacitance device provides ESD protection greater than 25 kilovolts with a peak pulse power dissipation of 200 Watts per line for an 8/20µs waveform. In addition, the ULLC0402FC05C features superior clamping performance, low leakage current characteristics and a response time of less than a nanosecond. Their low inductance virtually eliminates overshoot voltage due to package inductance.

## **FEATURES**

- Compatible with IEC 61000-4-2 (ESD): Air 15kV, Contact 8kV
- Compatible with IEC 61000-4-4 (EFT): 40A, 5/50ns
- ESD Protection > 25 kilovolts
- Low ESD Overshoot Voltage
- · Bidirectional and Monolithic Structure
- Protection for 1 Line
- Low Capacitance: 6pF
- Low Leakage Current
- RoHS Compliant
- REACH Compliant

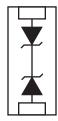
## **MECHANICAL CHARACTERISTICS**

- Standard EIA Chip Size: 0402
- Approximate Weight: 0.73 milligrams
- · Lead-Free Plating
- Flammability Rating UL 94V-0
- 8mm Plastic Tape per EIA Standard 481

## **APPLICATIONS**

- SMART Phones
- Portable Electronics
- SMART Cards

# **PIN CONFIGURATION**



# **TYPICAL DEVICE CHARACTERISTICS**

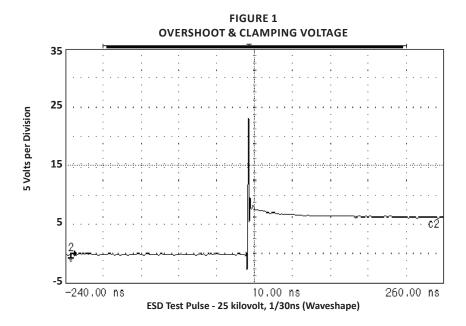
MAXIMUM RATINGS @ 25°C Unless Otherwise Specified						
PARAMETER SYMBOL VALUE UI						
Operating Temperature	T <sub>A</sub>	-55 to 150	°C			
Storage Temperature	T <sub>stg</sub>	-55 to 150	°C			

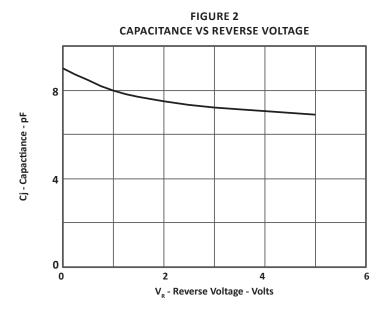
ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified							
PART RATED MINIMUM MAXIMUM TY NUMBER STAND-OFF BREAKDOWN LEAKAGE CAPA (Note 1) VOLTAGE VOLTAGE CURRENT							
	V <sub>wm</sub> VOLTS	@ 1mA V <sub>(BR)</sub> VOLTS	@V <sub>wм</sub> Ι <sub>D</sub> μΑ	@0V, 1MHz C pF			
ULLC0402FC05C	5.0	6.0	1.0	6			

## NOTES

<sup>1.</sup> Device is bidirectional.

# TYPICAL DEVICE CHARACTERISTICS





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# **SOLDER REFLOW INFORMATION**

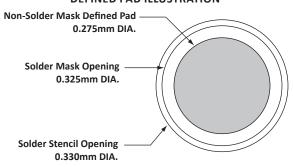
PRINTED CIRCUIT BOARD RECOMMENDATIONS					
PARAMETER	VALUE				
Pad Size on PCB	0.275mm				
Pad Shape	Round				
Pad Definition	Non-Solder Mask Defined Pads				
Solder Mask Opening	0.325mm Round				
Solder Stencil Thickness	0.150mm				
Solder Stencil Aperture Opening (Laser cut, 5% tapered walls)	0.330mm Round				
Solder Paste Type	No Clean				
Pad Protective Finish	OSP (Entek Cu Plus 106A)				
Tolerance - Edge To Corner Ball	±50μm				
Solder Ball Side Coplanarity (Only applies to bumped devices)	±20μm				
Maximum Dwell Time Above Liquidous (183°C)	60 seconds				
Soldering Maximum Temperature	270°C				

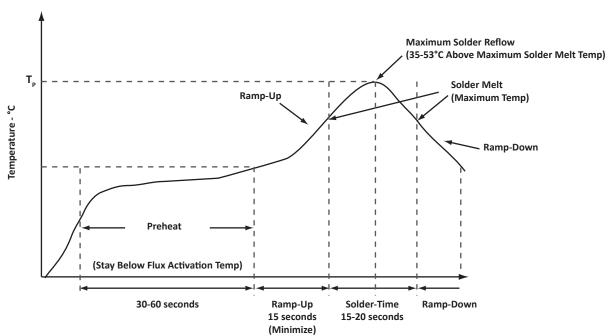
## **REQUIREMENTS**

#### Temperature:

 $\rm T_p$  for Lead-Free (Sn/Ag/Cu): 260-270°C Preheat time and temperature depends on solder paste and flux activation temperature, component size, weight, surface area and plating.

# RECOMMENDED NON-SOLDER MASK DEFINED PAD ILLUSTRATION



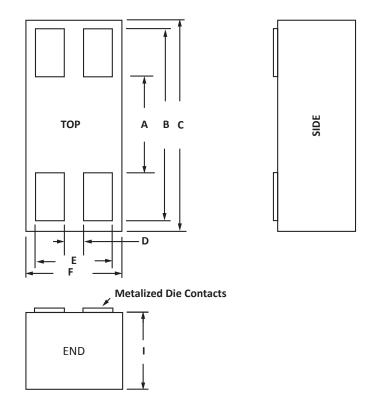


# **U0402 PACKAGE INFORMATION**

OUTLINE DIMENSIONS							
DIM	MILLIN	IETERS	INCHES				
	MIN	MAX	MIN	MAX			
Α	0.	61	0.0	24			
В	0.	86	0.034				
С	0.98 1.02		0.038 0.040				
D	0.	10	0.004				
Е	0.	35	0.0	)14			
F	0.458 0.508		0.018	0.020			
I	0.4	106	0.0	16			

#### NOTES

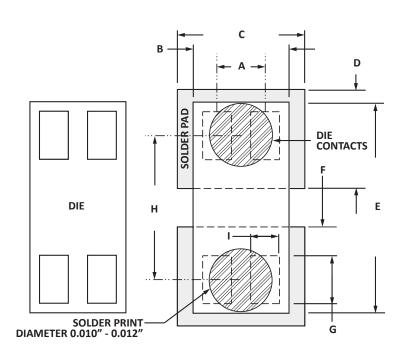
- 1. Controlling dimensions in inches.
- 2. Decimal tolerance:  $.xxx \pm 0.05mm (0.002")$ .
- 3. Maximum chip size: 1.02mm (0.040") by 0.51mm (0.020").



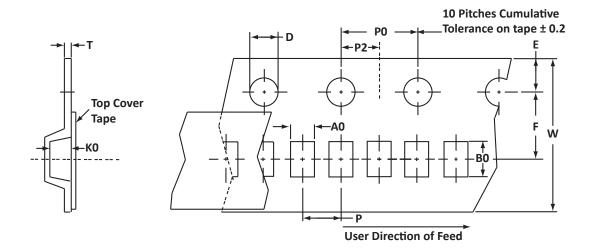
LAYOUT DIMENSIONS						
DINA	MILLIMETERS	INCHES				
DIM	NOMINAL	NOMINAL				
Α	0.23	0.009				
В	0.48	0.019				
С	0.69	0.027				
D	0.46	0.018				
Е	0.99	0.039				
F	0.20	0.008				
G	0.20	0.008				
Н	0.66	0.026				
Ī	0.13	0.005				

#### NOTES

- 1. Controlling dimensions in inches.
- 2. Decimal tolerance: .xxx ± 0.05mm (0.002").



# TAPE AND REEL INFORMATION

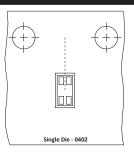


SPECIFICATIONS												
REEL DIA.	REEL DIA. TAPE WIDTH A0 B0 K0 D E F W P0 P2 P Tmax											
178(7")	8	0.70 ± 0.05	1.15 ± 0.10	0.56 ± 0.05	1.55 ± 0.05	1.75 ± 0.10	3.50 ± 0.05	8.00 ± 0.20	4.00 ± 0.05	2.00 ± 0.05	2.00 ± 0.05	0.25

#### NOTES

- Dimensions in millimeters.
- 2. Top view of tape. Metal contacts are face down in tape package.
- 3. Orientation: preferred stencil 0.1mm (0.004").
- 4. Surface mount product is taped and reeled in accordance with EIA 481.
- $5. \ \ 8 mm \ plastic \ tape: 7'' \ Reels 5,000 \ (pocket \ under \ hole \ skipped) \ or \ 10,000 \ pieces \ per \ reel.$
- 6. Marking on Reel part number, date code and lot number.

# **TAPE & REEL ORIENTATION**



Package outline, pad layout and tape specifications per document number 06001.R5 8/10.

ORDERING INFORMATION							
BASE PART NUMBER	LEADFREE SUFFIX	QTY/REEL	REEL SIZE	TUBE QTY			
ULLC0402FC05C	-n/a	-T75-1	5,000	7"	n/a		
ULLC0402FC05C	-n/a	-T710-1	10,000	7"	n/a		

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# **COMPANY INFORMATION**

#### **COMPANY PROFILE**

ProTek Devices, based in Tempe, Arizona USA, is a manufacturer of Transient Voltage Suppression (TVS) products designed specifically for the protection of electronic systems from the effects of lightning, Electrostatic Discharge (ESD), Nuclear Electromagnetic Pulse (NEMP), inductive switching and EMI/RFI. With over 25 years of engineering and manufacturing experience, ProTek designs TVS devices that provide application specific protection solutions for all electronic equipment/systems.

ProTek Devices Analog Products Division, also manufactures analog interface, control, RF and power management products.

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PATENT INFORMATION: This device is patented under U.S. Patent No. Des. "6,867,436".