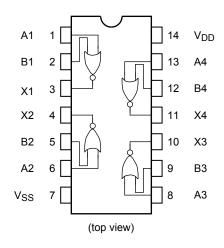
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4001BP,TC4001BF,TC4001BFT

TC4001B Quad 2 Input NOR Gate

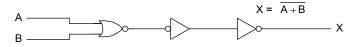
The TC4001B is 2-input positive NOR gate, respectively. Since the outputs of these gates are equipped with the buffers, the input/output transmission characteristics have been improved and the variation of transmission time due to an increase in the load capacity is kept minimum.

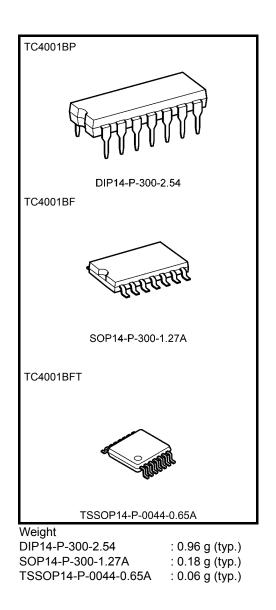
Pin Assignment



Logic Diagram

1/4 TC4001B





Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V _{DD}	$V_{\rm SS}$ – 0.5 to $V_{\rm SS}$ + 20	V
Input voltage	V _{IN}	V _{SS} – 0.5 to V _{DD} + 0.5	V
Output voltage	V _{OUT}	V _{SS} – 0.5 to V _{DD} + 0.5	V
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V _{DD}	—	3	_	18	V
Input voltage	V _{IN}	—	0	_	V _{DD}	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics (V_{SS} = 0 V)

Characteristics			Test Condition		-40°C		25°C			85°C		
		Symbol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level output voltage	V _{OH}	l _{OUT} < 1 μΑ	5	4.95	_	4.95	5.00	-	4.95	_		
		$V_{IN} = V_{SS}, V_{DD}$	10	9.95	—	9.95	10.00	—	9.95	—	V	
•	U		VIN - VSS, VDD	15	14.95	—	14.95	15.00	-	14.95		
		V _{OL}	l _{OUT} < 1 μΑ	5	—	0.05	—	0.00	0.05	—	0.05	
Low-leve output vo			$V_{IN} = V_{SS}, V_{DD}$	10	—	0.05	—	0.00	0.05	—	0.05	V
	-		VIN - VSS, VDD	15	-	0.05	—	0.00	0.05	—	0.05	
			V _{OH} = 4.6 V	5	-0.61	—	-0.51	-1.0	—	-0.42	_	mA
			V _{OH} = 2.5 V	5	-2.50	—	-2.10	-4.0	—	-1.70	_	
Output hi current	igh	IOH	V _{OH} = 9.5 V	10	-1.50	—	-1.30	-2.2	—	-1.10	_	
			V _{OH} = 13.5 V	15	-4.00	—	-3.40	-9.0	—	-2.80	_	
			$V_{IN} = V_{SS}$									
		IOL	V _{OL} = 0.4 V	5	0.61	_	0.51	1.2	-	0.42	_	mA
Output lo	w		V _{OL} = 0.5 V	10	1.50	—	1.30	3.2	—	1.10	—	
current			V _{OL} = 1.5 V	15	4.00	—	3.40	12.0	—	2.80	—	
			$V_{IN} = V_{SS}, V_{DD}$									
		VIH	V _{OUT} = 0.5 V	5	3.5	_	3.5	2.75	_	3.5	_	v
Input high	h		V _{OUT} = 1.0 V	10	7.0	_	7.0	5.50	_	7.0	_	
voltage			V _{OUT} = 1.5 V	15	11.0	—	11.0	8.25	—	11.0	_	
			l _{OUT} < 1 μΑ									
			V _{OUT} = 4.5 V	5		1.5	_	2.25	1.5	_	1.5	
Input low	,	VIL	V _{OUT} = 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0	V
voltage			V _{OUT} = 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	
			l _{OUT} < 1 μΑ									
	"H" level	Ιн	V _{IH} = 18 V	18		0.1	_	10 ⁻⁵	0.1	_	1.0	- μΑ
current	"L" level	IIL	V _{IL} = 0 V	18	_	-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	
		t I _{DD}		5		0.25	_	0.001	0.25	_	7.5	
Quiescer supply cu			$V_{IN} = V_{SS}, V_{DD}$	10	_	0.50	_	0.001	0.50	_	15.0	μA
			(Note)	15	_	1.00	_	0.002	1.00	_	30.0	

Note: All valid input combinations.

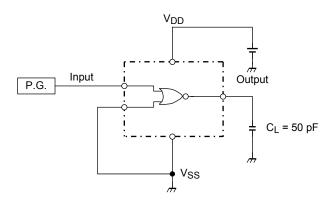
Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

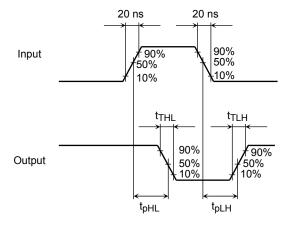
Characteristics	Symbol	Test Condition	Min	Turn	Max	Linit	
Characteristics	Symbol		V _{DD} (V)	IVIIII	Тур.	Max	Unit
Output transition time	tтLH		5	_	70	200	
		_	10	_	35	100	ns
			15	—	30	80	
Output transition time			5	_	70	200	
	t _{THL}	—	10	_	35	100	ns
			15		30	80	
	^t pLH		5		65	200	
Propagation delay time		_	10	—	30	100	ns
			15		25	80	
Propagation delay time	tpHL		5	_	65	200	
		—	10	—	30	100	ns
			15		25	80	
Input capacitance	C _{IN}	_			5	7.5	pF

Circuit and Waveform for Measurement of Dynamic Characteristics

Circuit

Waveform

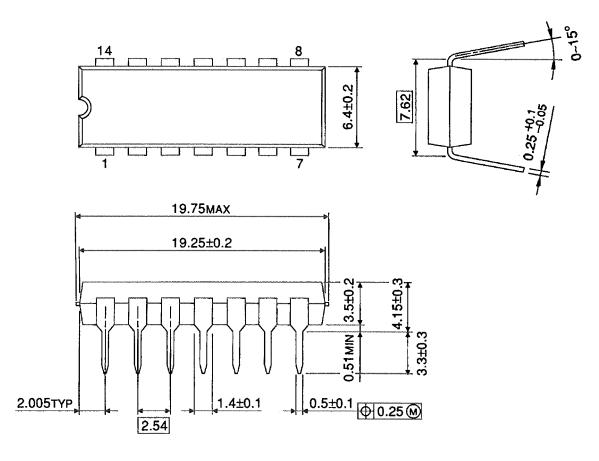




Package Dimensions

DIP14-P-300-2.54

Unit : mm



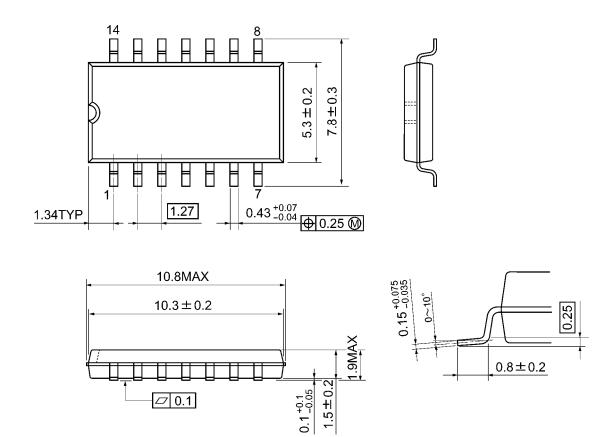
Weight: 0.96 g (typ.)



Package Dimensions

SOP14-P-300-1.27A

Unit: mm

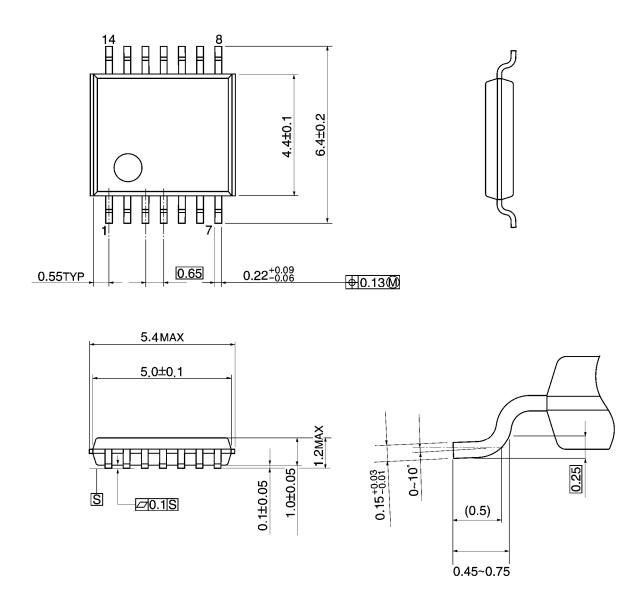


Weight: 0.18 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



Weight: 0.06 g (typ.)

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