

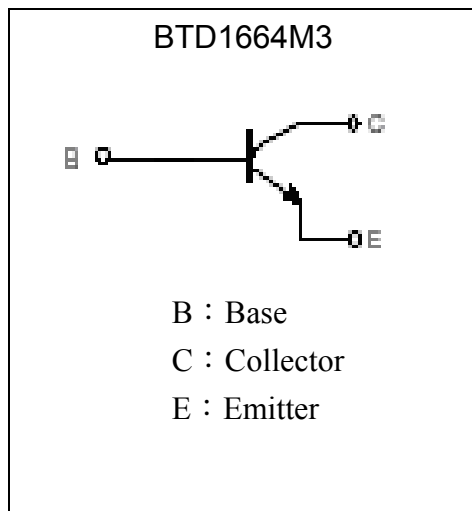
**Low Vcesat NPN Epitaxial Planar Transistor**

# BTD1664M3

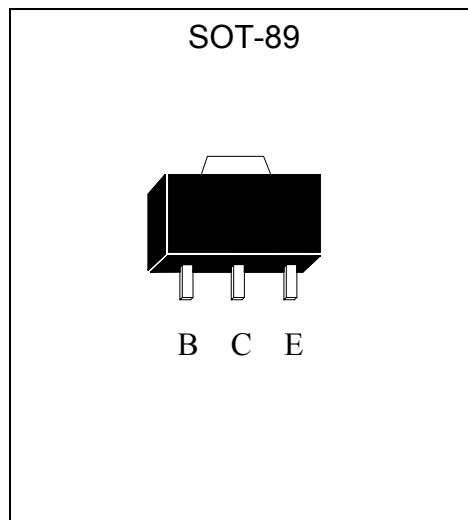
**Features**

- The BTD1664M3 is designed for general purpose low frequency power amplifier applications.
- Low  $V_{CE(sat)}$ ,  $V_{CE(sat)}=0.15V$  (typical), at  $I_C / I_B = 400mA / 20mA$
- Complementary to BTB1132M3

**Symbol**



**Outline**



**Absolute Maximum Ratings** ( $T_a=25^{\circ}C$ )

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	$V_{CBO}$	40	V
Collector-Emitter Voltage	$V_{CEO}$	20	V
Emitter-Base Voltage	$V_{EBO}$	5	V
Collector Current (DC)	$I_C$	800	mA
Collector Current (Pulse)	$I_{CP}$	1.5 (Note 1)	A
Power Dissipation	$P_d$	0.5	W
Power Dissipation	$P_d$	2 (Note 2)	W
Junction Temperature	$T_j$	150	$^{\circ}C$
Storage Temperature	$T_{stg}$	-55~+150	$^{\circ}C$

Note : 1. Single pulse,  $P_w = 20ms$ , duty  $\leq 2\%$ .  
 2. When mounted on a 40 x40 x0.7 mm ceramic board.



**Characteristics (Ta=25°C)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV <sub>CBO</sub>	40	-	-	V	I <sub>C</sub> =10μA, I <sub>E</sub> =0
BV <sub>CEO</sub>	20	-	-	V	I <sub>C</sub> =1mA, I <sub>B</sub> =0
BV <sub>EBO</sub>	5	-	-	V	I <sub>E</sub> =10μA, I <sub>C</sub> =0
I <sub>CBO</sub>	-	-	0.5	μA	V <sub>CB</sub> =20V, I <sub>E</sub> =0
I <sub>EBO</sub>	-	-	0.5	μA	V <sub>EB</sub> =4V, I <sub>C</sub> =0
*V <sub>CE(sat)1</sub>	-	0.15	0.3	V	I <sub>C</sub> =400mA, I <sub>B</sub> =20mA
*V <sub>CE(sat)2</sub>	-	0.2	0.4	V	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA
*V <sub>CE(sat)3</sub>	-	0.25	0.5	V	I <sub>C</sub> =800mA, I <sub>B</sub> =80mA
*V <sub>BE(on)</sub>	-	-	1	V	V <sub>CE</sub> =2V, I <sub>C</sub> =500mA
*h <sub>FE1</sub>	82	-	560	-	V <sub>CE</sub> =2V, I <sub>C</sub> =100mA
*h <sub>FE2</sub>	82	-	560	-	V <sub>CE</sub> =2V, I <sub>C</sub> =500mA
*h <sub>FE3</sub>	80	-	-	-	V <sub>CE</sub> =2V, I <sub>C</sub> =800mA
f <sub>T</sub>	-	150	-	MHz	V <sub>CE</sub> =5V, I <sub>E</sub> =50mA, f=100MHz
Cob	-	15	-	pF	V <sub>CB</sub> =10V, f=1MHz

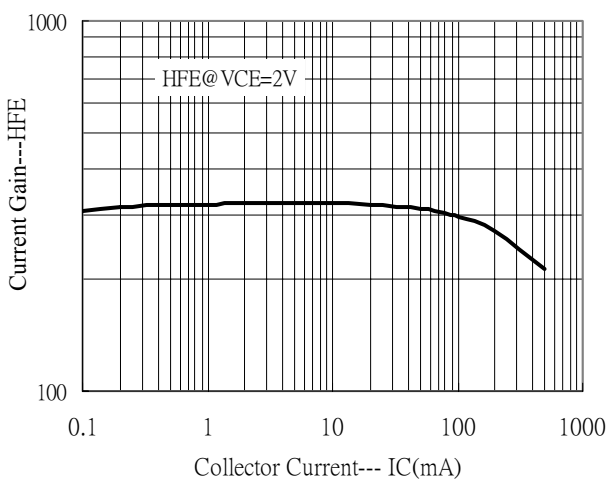
\*Pulse Test : Pulse Width ≤380us, Duty Cycle ≤2%

**Classification Of hFE2**

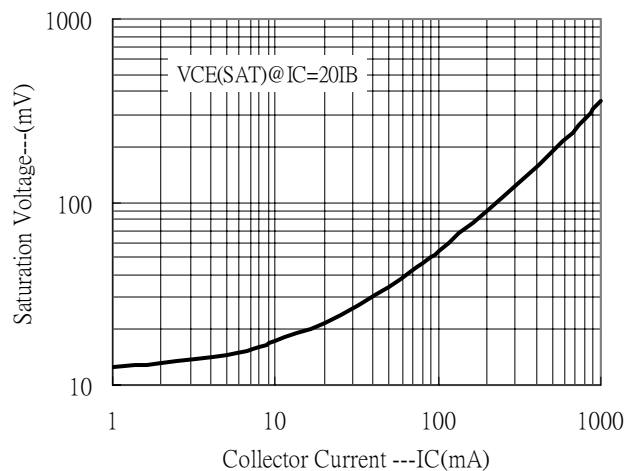
Rank	P	Q	R	S
Range	82~180	120~270	180~390	270~560

**Characteristic Curves**

Current Gain vs Collector Current

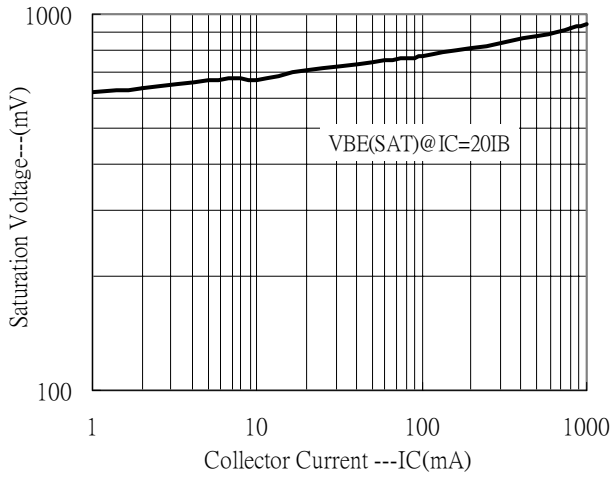


Saturation Voltage vs Collector Current

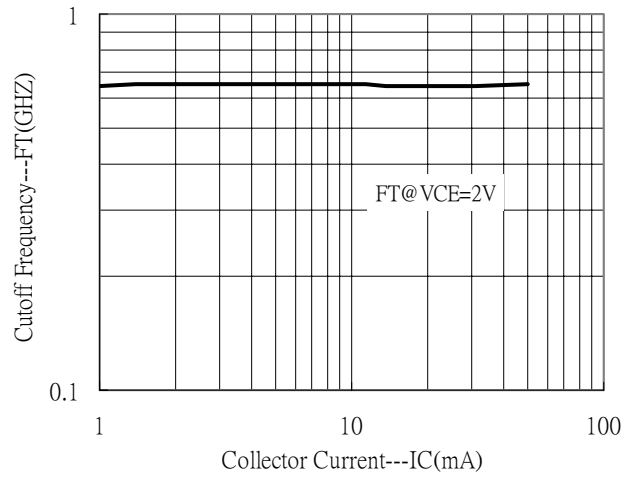




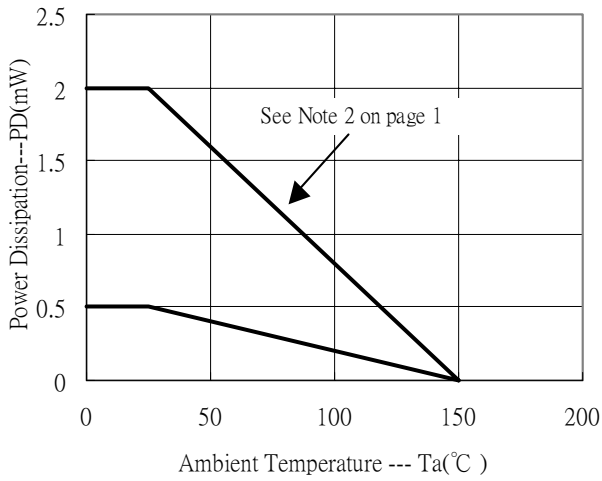
Saturation Voltage vs Collector Current



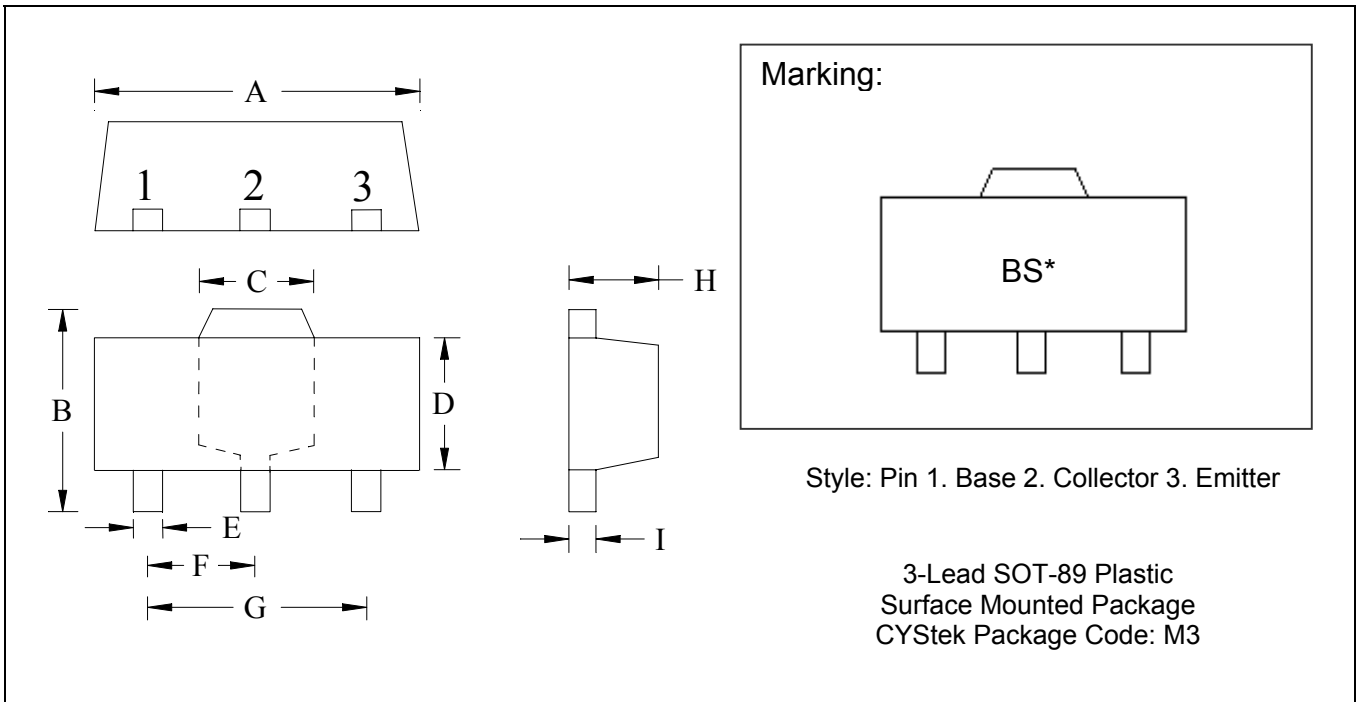
Cutoff Frequency vs Collector Current



Power Derating Curve



**SOT-89 Dimension**



\*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1732	0.1811	4.40	4.60	F	0.0583	0.0598	1.48	1.527
B	0.1594	0.1673	4.05	4.25	G	0.1165	0.1197	2.96	3.04
C	0.0591	0.0663	1.50	1.70	H	0.0551	0.0630	1.40	1.60
D	0.0945	0.1024	2.40	2.60	I	0.0138	0.0161	0.35	0.41
E	0.01417	0.0201	0.36	0.51					

- Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: 42 Alloy ; solder plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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